



# PROTOTYPE V1.0.0

Power

**POWER**

File: Power.kicad\_sch

Sensors

**SENSORS**

File: Sensors.kicad\_sch

MCU

**MCU**

File: MCU.kicad\_sch

Transceiver

**TX/RX**

File: Transceiver.kicad\_sch

GNSS

**GNSS**

File: GNSS.kicad\_sch

Conn

**CONN**

File: Conn.kicad\_sch

TODO: Optimize connectors for weight,  
replace JSTs with solder pads in final revision.  
Determine panel arrangement and connection.  
Port telemetry and pwm connections to  
power delivery and ESC board.

## MECHANICAL

- H1 MountingHole
- H2 MountingHole
- H3 MountingHole
- H4 MountingHole
- FID1 Fiducial
- FID2 Fiducial
- FID3 Fiducial

→ **UNDER REVISION** ←  
→ **DO NOT** ←  
→ **MANUFACTURE** ←

Sheet: /  
File: uas\_fc.kicad\_sch

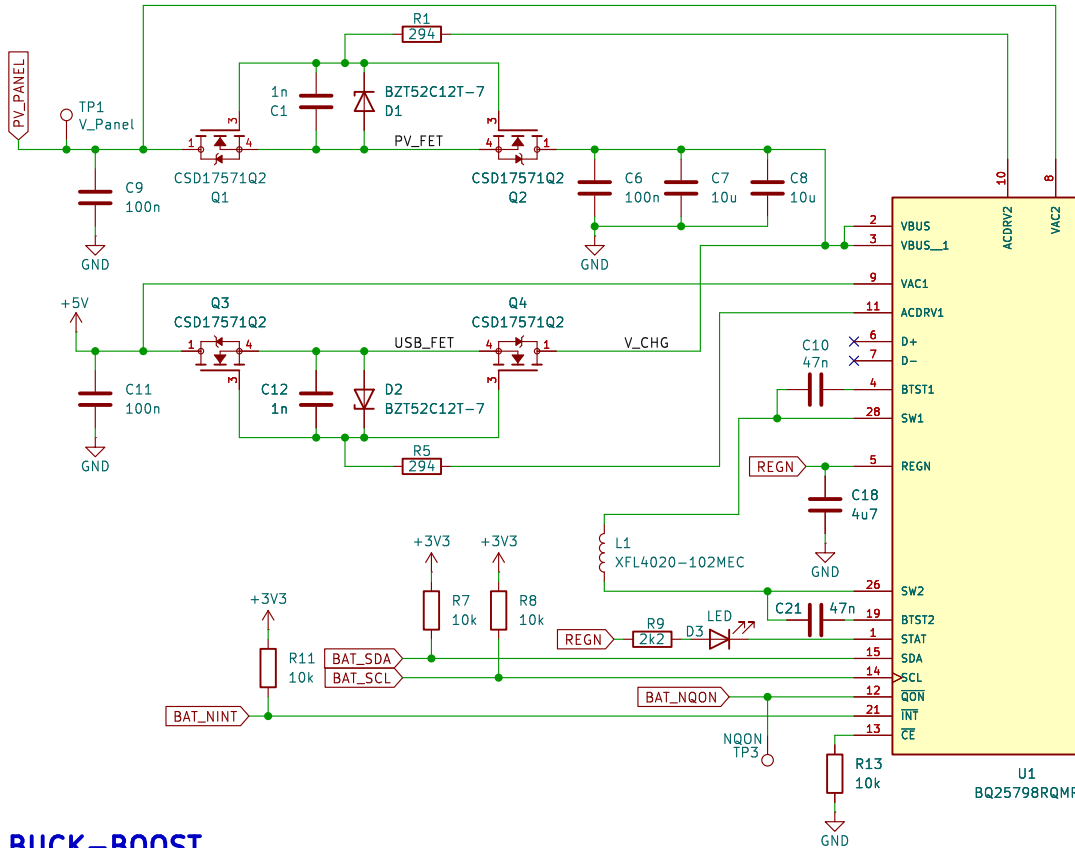
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KiCad E.D.A. 8.0.4

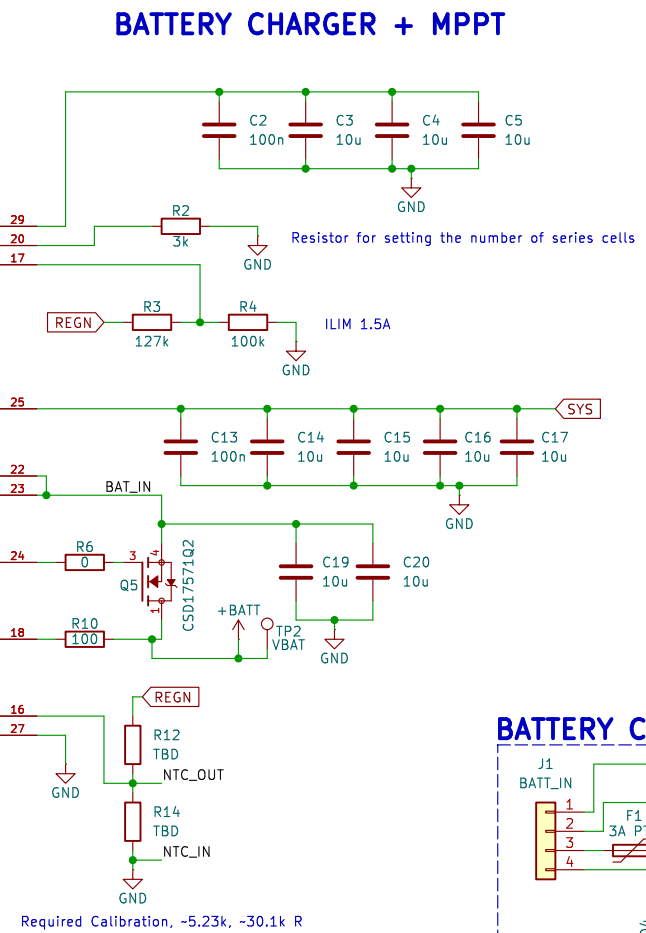
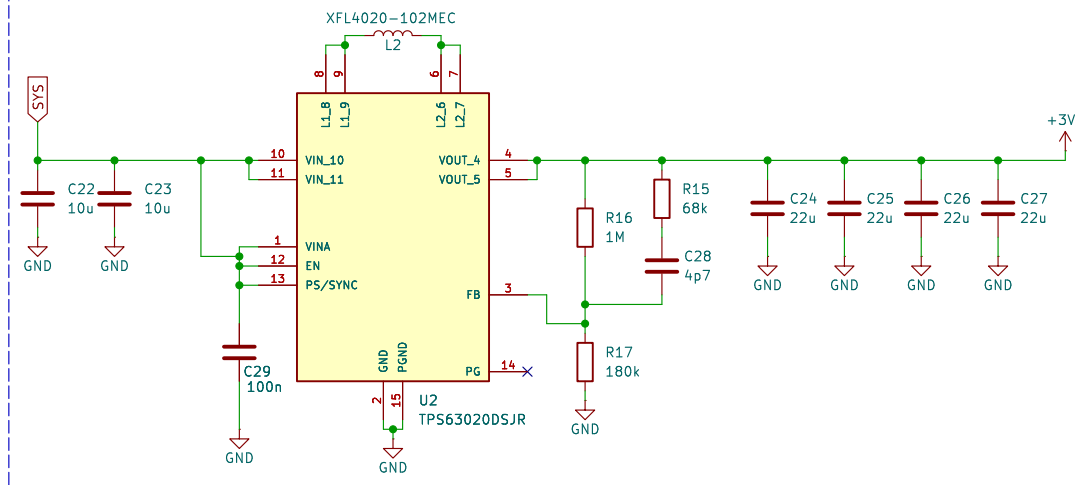
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**Rev:**

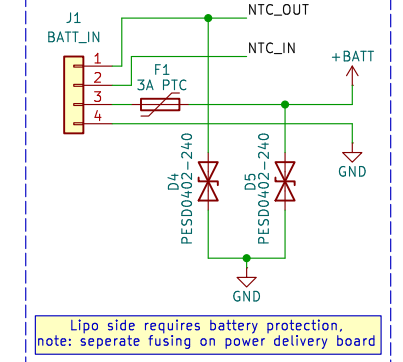
Id: 1/7



## BUCK-BOOST



## BATTERY CONN



Sheet: /Power/  
File: Power.kicad\_sch

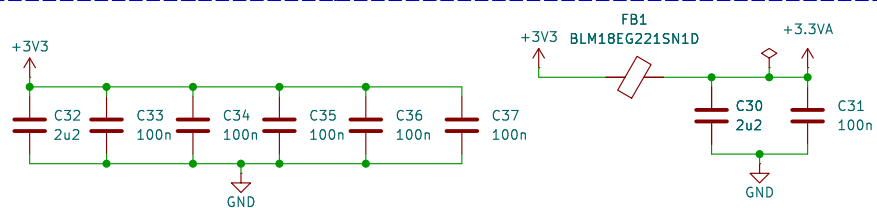
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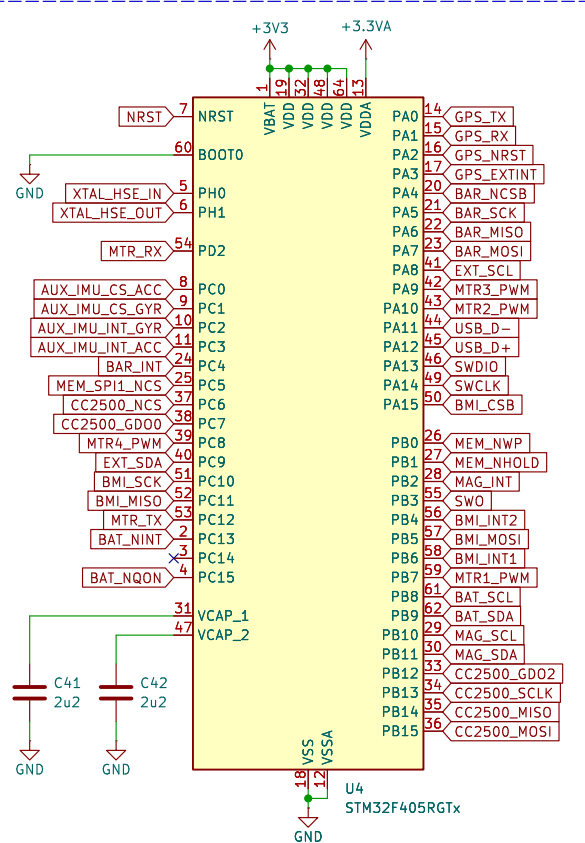
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Rev:  
Id: 2/7

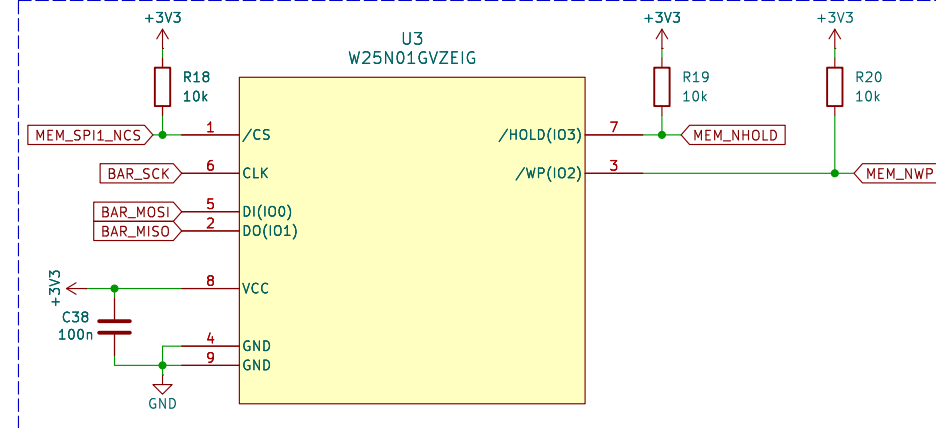
## DECOUPLING (VDD,VDDA)



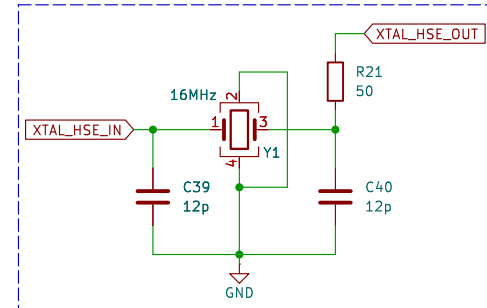
## MCU



## FLASH



## XTAL HSE



Sheet: /MCU/  
File: MCU.kicad\_sch

### Title:

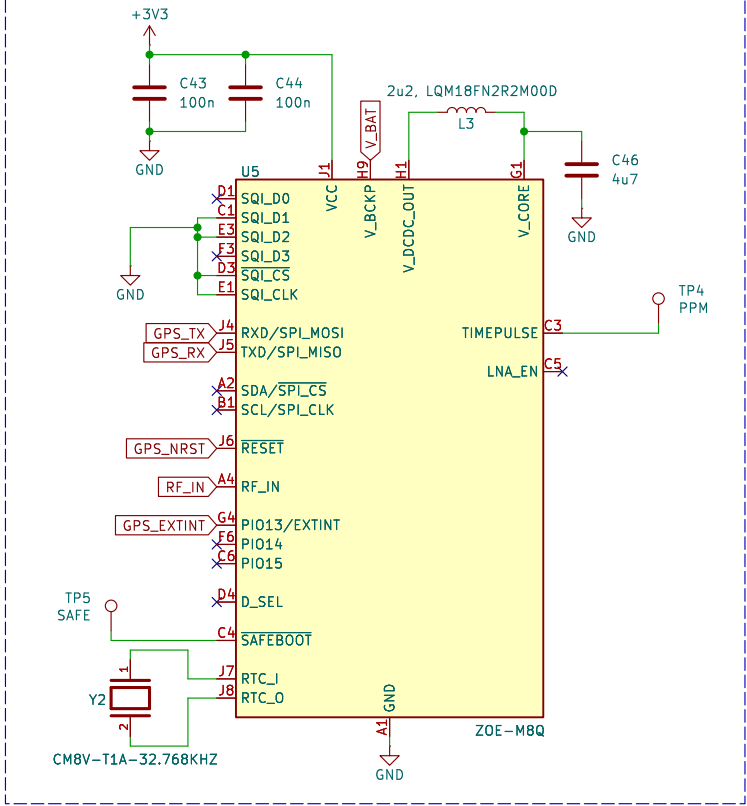
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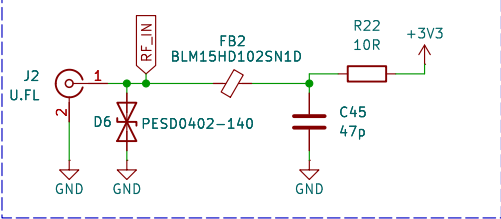
Rev:

Id: 3/7

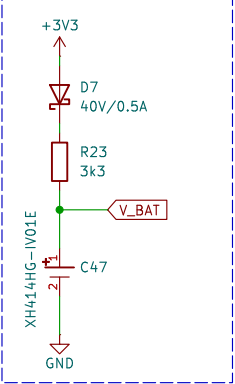
GNSS



SMA BIAS-T



GPS BACKUP



Sheet: /GNSS/  
File: GNSS.kicad\_sch

Title:

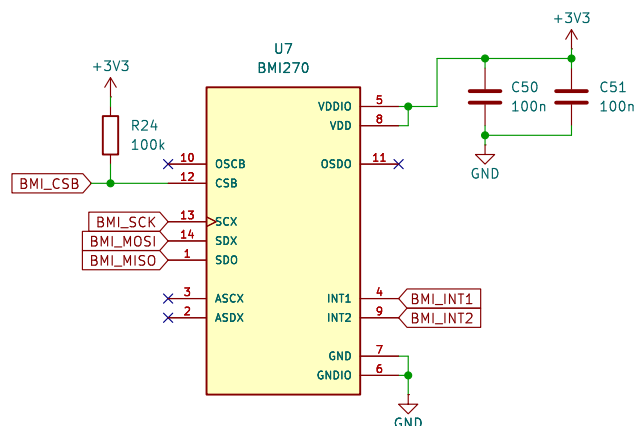
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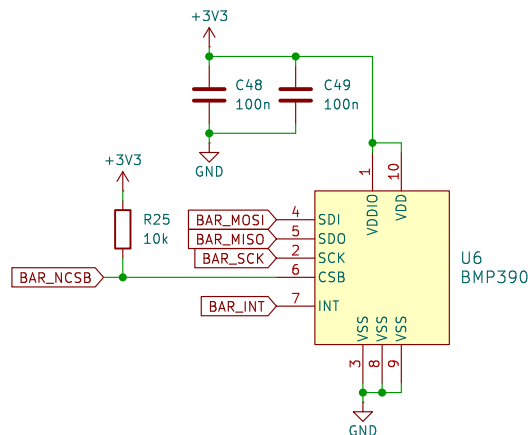
Rev:

Id: 4/7

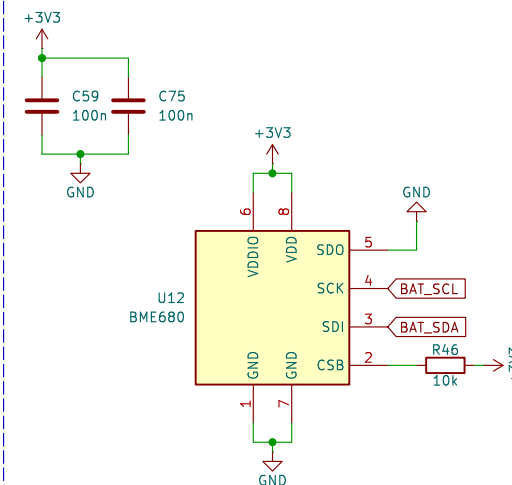
## IMU



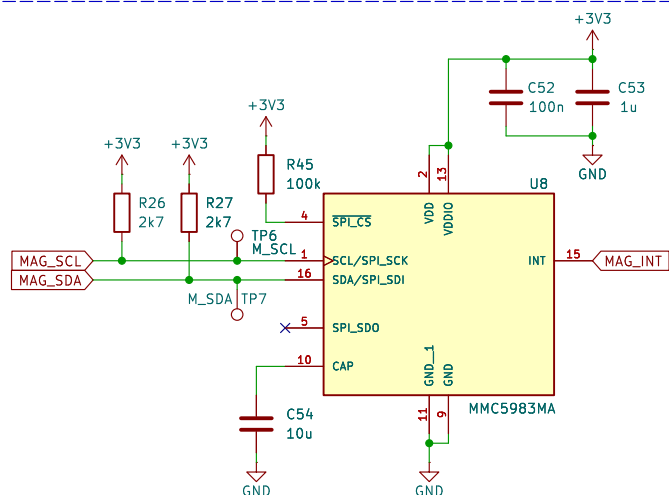
## BAROMETER



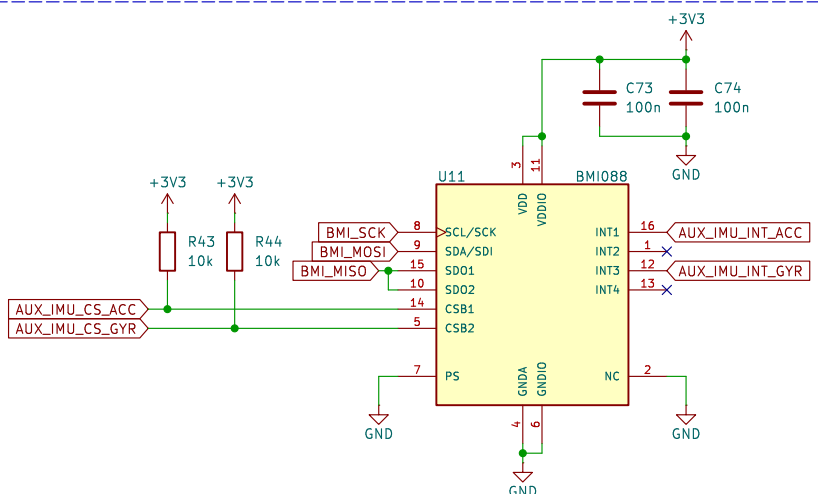
## GAS, HUMIDITY, PRESSURE, TEMPERATURE SENSOR



## MAGNETOMETER



## AUXILIARY IMU



Sheet: /Sensors/  
File: Sensors.kicad\_sch

**Title:**

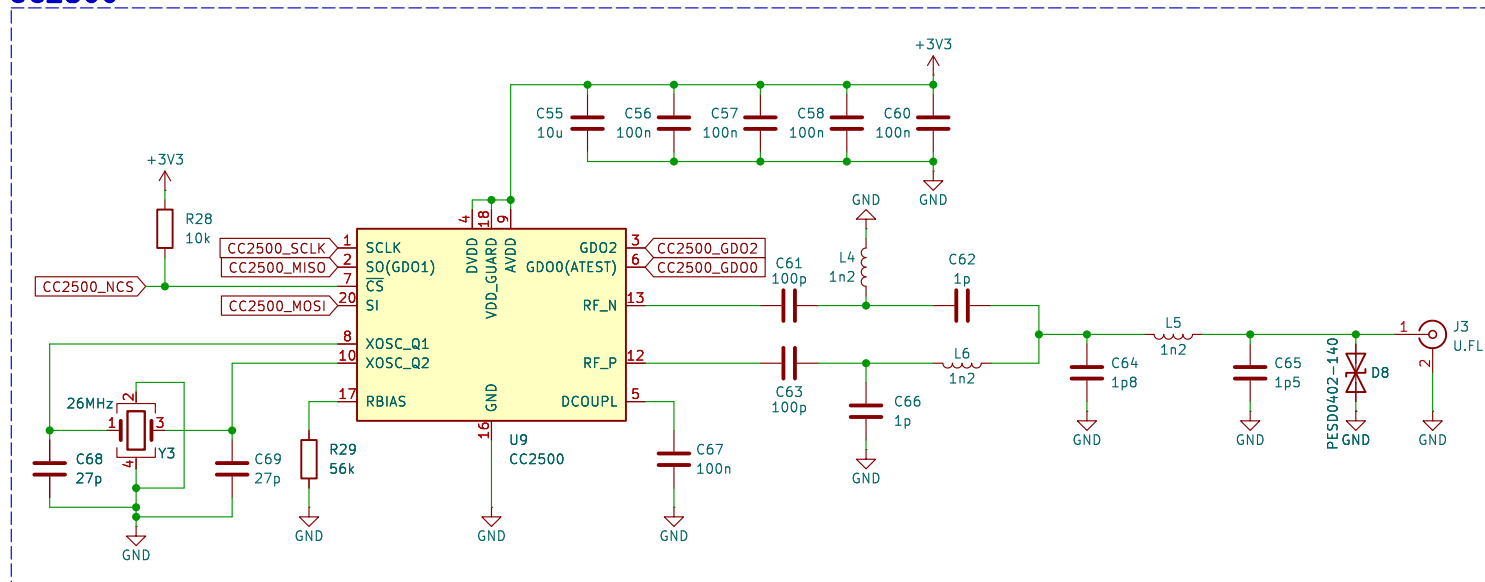
Size: A4 Date:

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# CC2500



Sheet: /Transceiver/  
File: Transceiver.kicad\_sch

## Title:

Size: A4

Date:

KiCad E.D.A. 8.0.4

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The diagram illustrates the wiring for a USB-C to USB-LC adapter. A USB-C receptacle (J4) is connected to a USB-LC connector (U10). The receptacle pins are labeled A4, A5, B5, A7, B7, A6, B6, A8, and B8. The LC connector pins are labeled 1, 2, 3, 4, 5, and 6. The diagram includes labels for VBUS, GND, and USB\_D- and USB\_D+ signals. Resistor values R31 (5k1) and R32 (5k1) are indicated.

The schematic diagram illustrates the SWD interface circuit for the ESDA5V35C5 device. The circuit includes a PES00402-140 level shifter, a 3V3 supply, a J9 connector, resistors R35, R38, R39, and R40, a 100nF capacitor C72, and the D10 ESDA5V35C5 device. The SWDIO pin is connected to the SWDIO pin of the device, SWCLK to SWCLK, SWO to SWO, and NRST to NRST.

ESC PWM Outputs, esd protection, and EMF diodes on ESC

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