

Hardware-Assisted Virtualization

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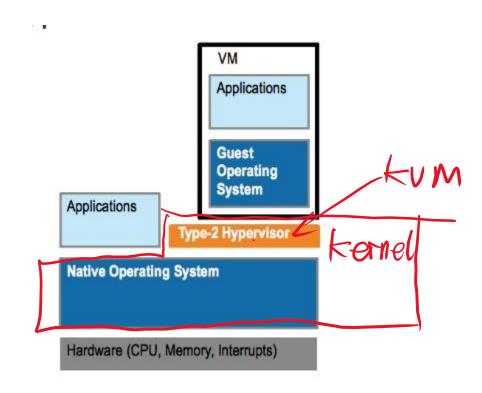


What's KVM?



What's KVM?

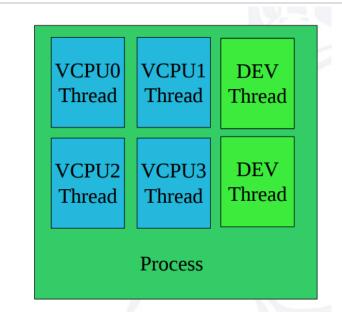
Type 2 Hypervisor



Abstraction Model

Design

- 1. One process per VM
- 2. One thread per-VCPU
- 3. Device models run concurrent in VCPU thread
- 4. Long running operations run in additional device thread

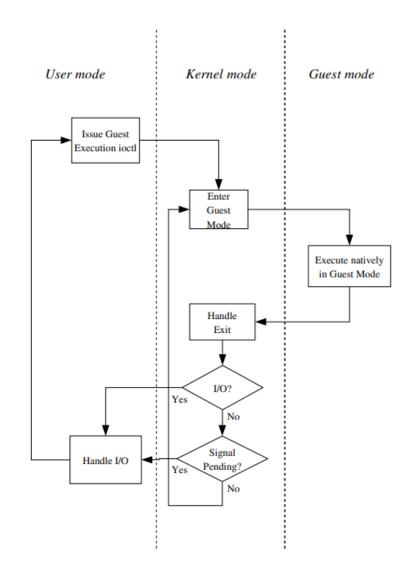


•••	Memory	1.00 Gi g /3.00 GiB
	Processors	2 (1 sockets, 2 cores)
	BIOS	Default (SeaBIOS)
Ţ	Display	Standard VGA (std)
Q°	Machine	q35
9	SCSI Controller	VirtIO SCSI
	Hard Disk (scsi0)	local-lvm:vm-101-disk-0,backup=0,size=25G,ssd=1
	Hard Disk (scsi1)	BackupSsd:vm-101-disk-1,size=55064M
=	Network Device (net0)	virtio = A2:43:2D:23:3C:3E, bridge = vmbr0, firewall = 1
=	PCI Device (hostpci0)	00:02.0,pcie=1,x-vga=1
(III)	Serial Port (serial0)	socket

```
1:24.84 /sbin/init
                                           /usr/sbin/pvefw-logger
                          0.0 0:11.71
0 86172
                                           └ /usr/sbin/pvefw-logger
                          0.0 0:11.71
                                           /lib/systemd/systemd --user
                          0.1 0:00.05
0 21404
                                           — (sd-pam)
                          0.0
                               0:00.00
                                           /usr/bin/kvm -id 101 -name ubuntu-20.04 -no-
                                           /usr/bin/kvm -id 101 -name ubuntu-20.04
                      0.0 39.7 0:00.00
                                              /usr/bin/kvm -id 101 -name ubuntu-20.04
                               4h35:28
                                              /usr/bin/kvm -id 101 -name ubuntu-20.04
0 3910M 3120M 10932 S 0.0 39.7 0:00.05
                                           /usr/bin/kvm -id 101 -name ubuntu-20.04
```

Programming Model

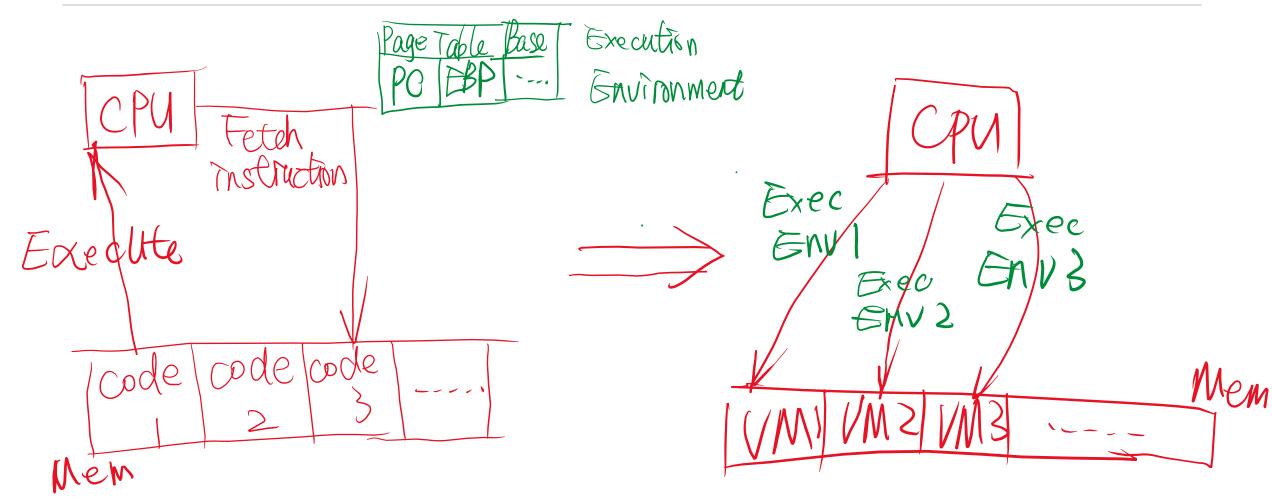
- 1. Event-Loop like model
- 2. Hide the detail of cross-platform hardware features



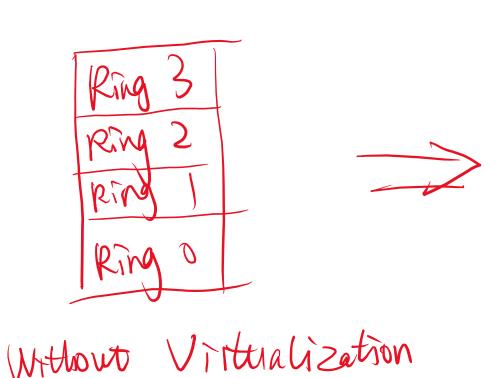
Hardware Virtualization and Implementation of KVM



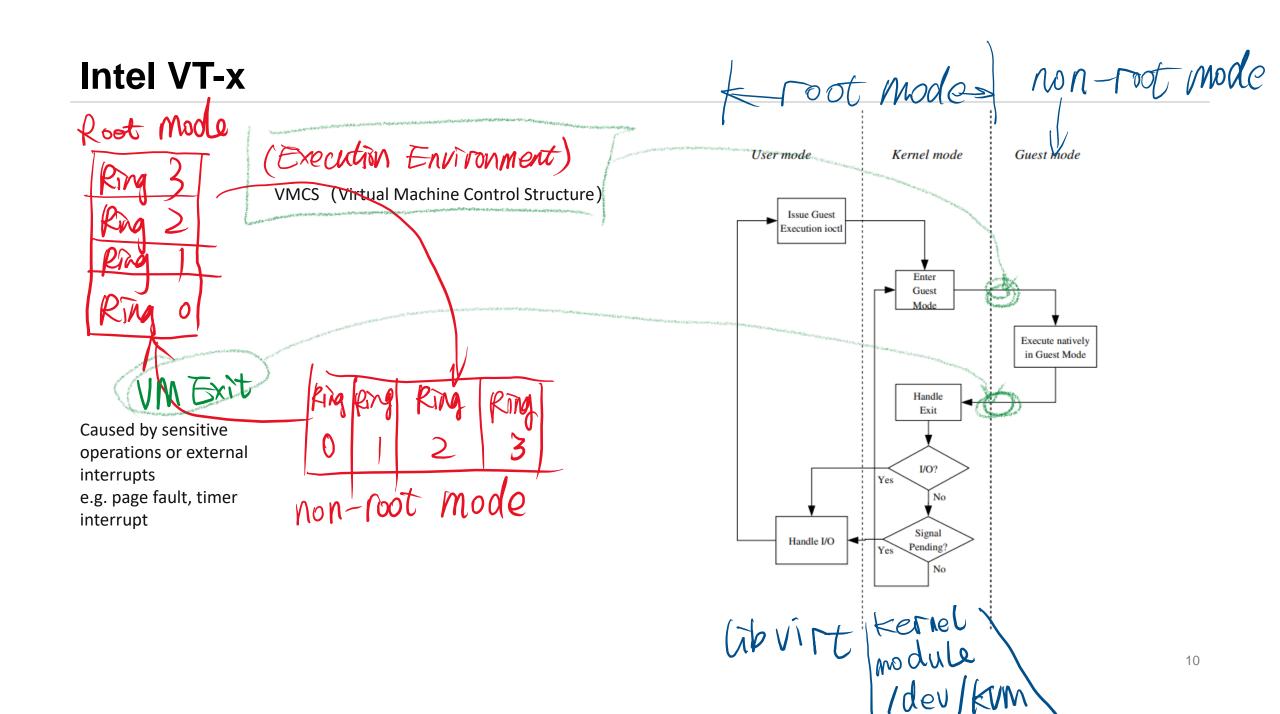
Hardware Virtualization and Implementation of KVM



Intel VT-x



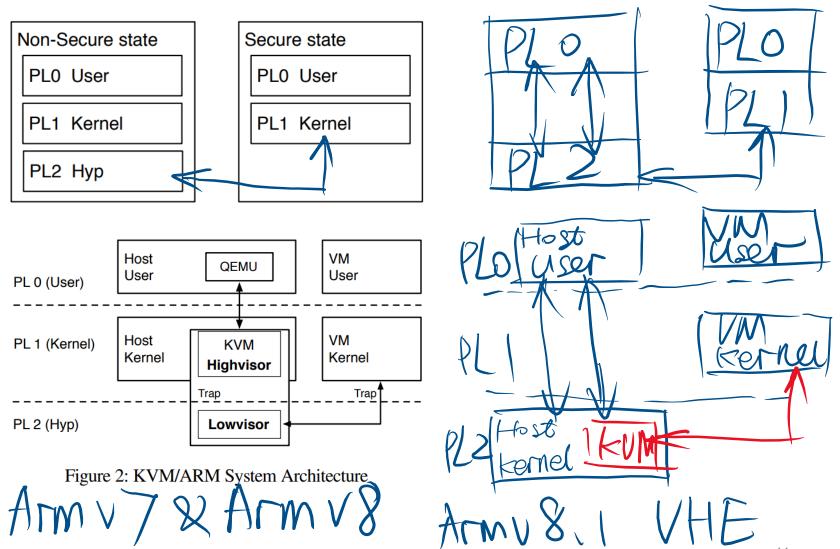
Root Mode (Execution Environment)
(Virtual Machine Control Structure) VMCS Caused by sensitive operations or external interrupts non-root mode e.g. page fault, timer interrupt



ARM

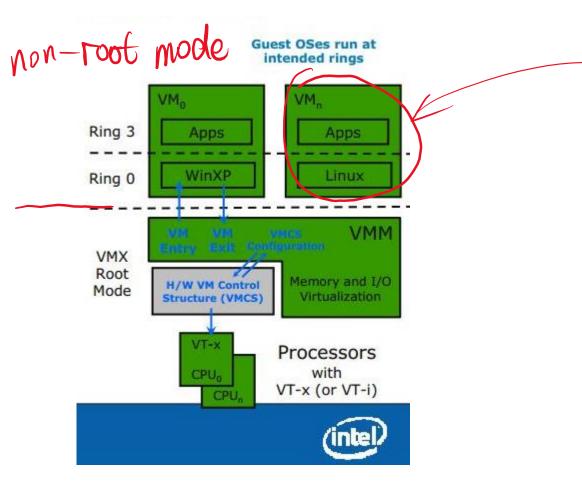


ARM without virtualization



Dune: Safe User-level Access to Privileged CPU Features





Can we replace it with a host process?

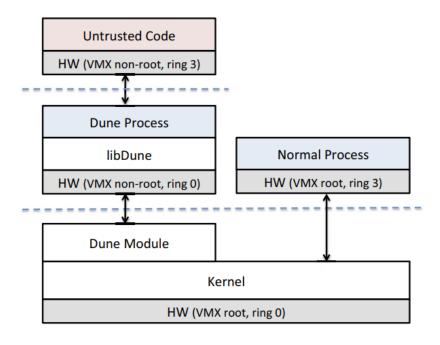


Figure 1: The Dune system architecture.

Benefits

Hardware features exposed by Dune and their corresponding privileged x86 instructions.

Mechanism	Privileged Instructions
Exceptions	LIDT, LTR, IRET, STI, CLI
Virtual Memory	MOV CRn, INVLPG, INVPCID
Privilege Modes	SYSRET, SYSEXIT, IRET
Segmentation	LGDT, LLDT

Allow some applications to improve their performance by exploiting the exposed hardware features

Application?

- 1. Faster GC
 - 1. No syscall or IRQ overhead
 - 2. More exploitable information(Dirty bits...)
 - 3. TLB flush batching
- 2. Sandbox
 - 1. Faster context switching
 - 2. More flexible filtering policies on sensitive operations without kernel modification
- 3. Trace
 - 1. A better way to bypass anti-debugging mechanism?

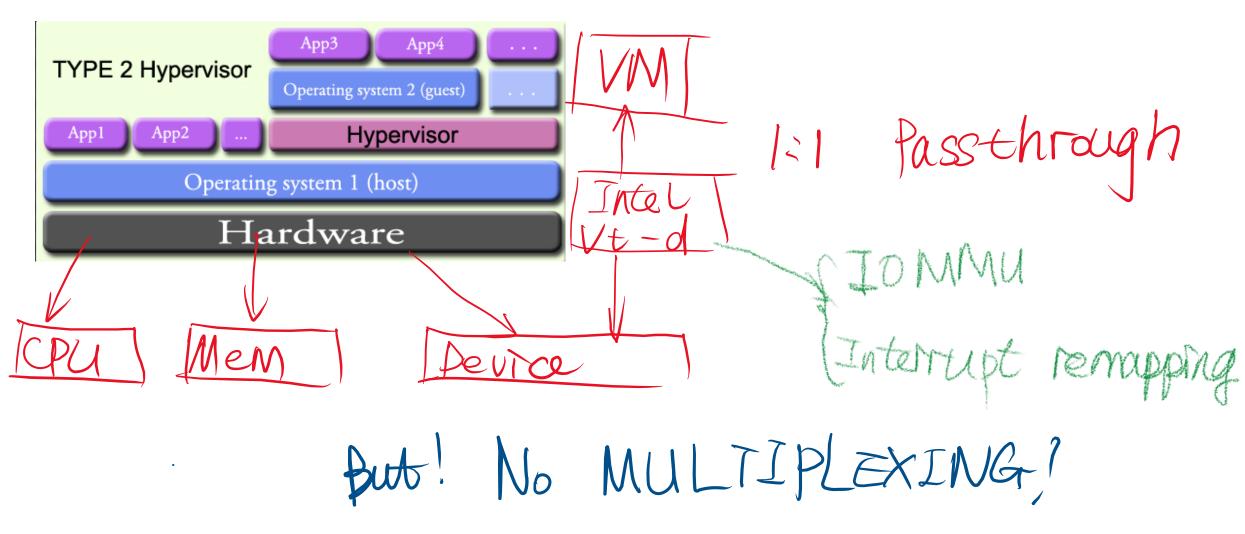
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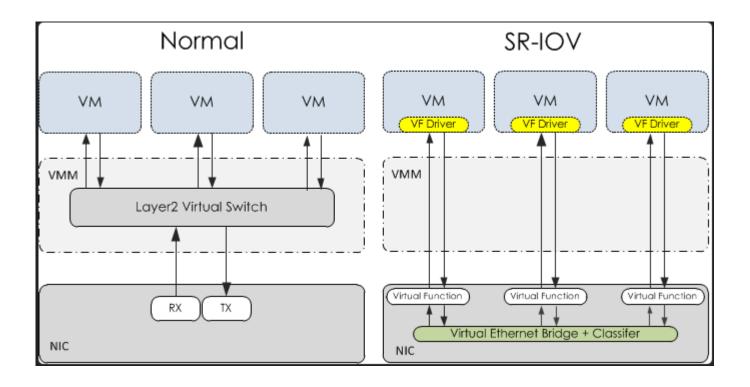
Arrakis: The Operating

System is the Control Plane



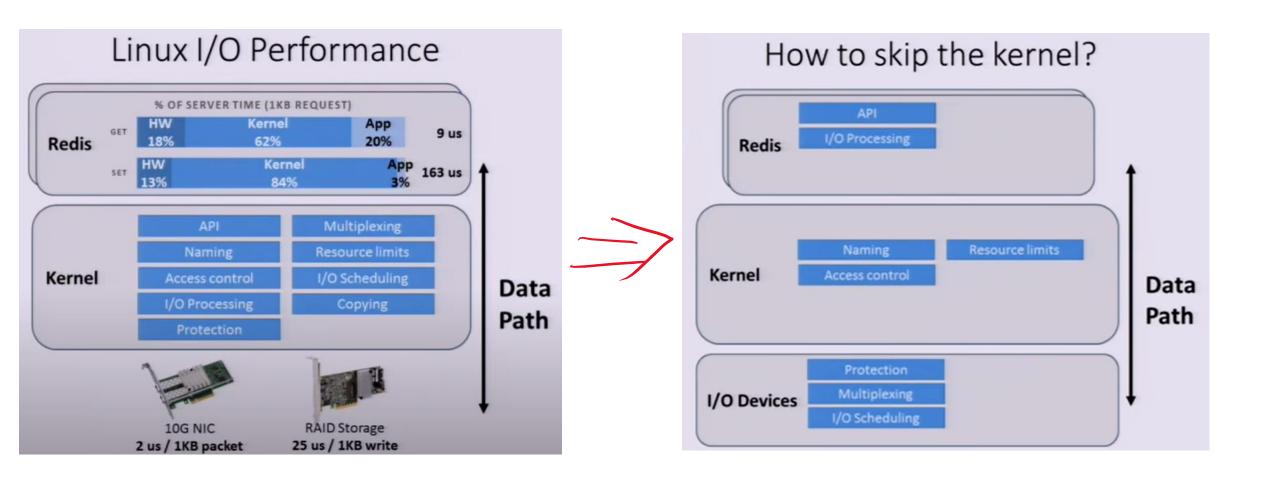




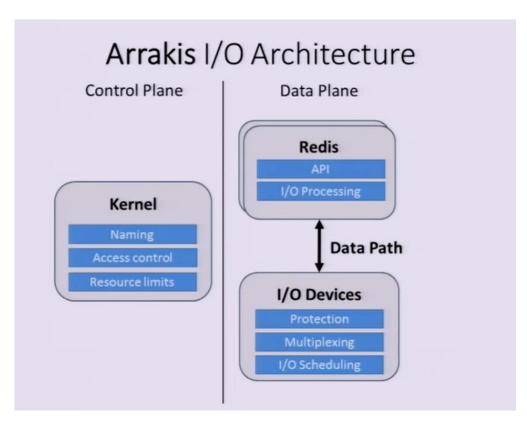


With SR-IOV, all hardware resources are able to be multiplexed at the hardware level.

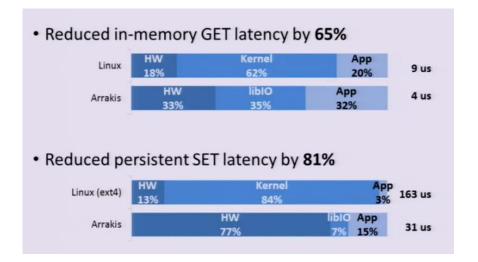
So can we reorganize the OS now?



Design and Result







DPDK? Openwrt? SArrakis provides a generie architechture

Summary / 15



Summary

- 1. KVM and Dune How to integrate with the existing system gracefully
- 2. Arrakis How to reorganize the existing system gracefully

