



# Smart Coding User Guide

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# About This Document

## Purpose

This document describes information about H.264 and H.265 smart coding, which mainly involves the following four parts:

- Part 1: group of pictures (GOP) structure. Different GOP structures apply to different scenarios. The GOP structure can be configured dynamically according to the scenario to optimize encoding performance.
- Part 2: input information of the encoder. The encoder input information APIs can interact with other smart analysis modules to protect the analyzed regions of interest (ROIs) or important regions by using QpMap or implement better bit rate control based on the customer's algorithms.
- Part 3: output information of the encoder. The customer can provide more reference input information for smart analysis algorithms based on the output information of the encoder.
- Part 4: Cyclic Intra Refresh. This technology does not encode the instantaneous decoding refresh (IDR) frames. The I macroblock is encoded periodically in the P-frames to implement smooth bit rate in special application scenarios.



### NOTE

- Unless otherwise stated, Hi3559C V100 and Hi3559A V100 contents are consistent.
- Unless otherwise stated, Hi3516A V300 and Hi3516D V300 contents are consistent.
- Unless otherwise stated, Hi3516E V300, Hi3518E V300, Hi3516D V200, and Hi3516E V200 contents are consistent.

## Related Versions

The following table lists the product versions related to this document.

Product Name	Version
Hi3559A	V100ES
Hi3559A	V100
Hi3559C	V100
Hi3519A	V100
Hi3516C	V500



Product Name	Version
Hi3516D	V300
Hi3516A	V300
Hi3516E	V200
Hi3516E	V300
Hi3518E	V300
Hi3516D	V200

## Intended Audience

This document is intended for:

- Technical support engineers
- Software development engineers

## Change History

Changes between document issues are cumulative. The latest document issue contains all changes made in previous issues.

### Issue 00B05 (2019-04-15)

This issue is the fifth draft release, which incorporates the following changes:

In section 1.8, Table 1-2 is changed.

### Issue 00B04 (2018-11-13)

This issue is the fourth draft release, which incorporates the following changes:

The descriptions in the Hi3516E V200, Hi3516E V300, and Hi3518E V300 are added.

### Issue 00B03 (2018-06-15)

This issue is the third draft release, which incorporates the following changes:

Sections 1.5 and 1.8 are modified.

### Issue 00B02 (2018-01-15)

This issue is the second draft release, which incorporates the following changes:

Sections 1.7.2, 1.8, and 2.2.1 are modified.

In section 2.1.2, figure 2-2 and figure 2-3 are changed.



## Issue 00B01 (2017-05-20)

This issue is the first draft release.



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# 1 GOP Structure and Application Scenarios

## 1.1 GOP Mode Glossary

Table 1-1 GOP mode glossary

GOP Mode	Number of Reference Frames that Can Be Referenced by the P-frame Simultaneously	Remarks
NormalP	1	A P-frame uses one reference frame.
SmartP	2	A P-frame uses a long-term reference frame and a short-term reference frame.
AdvSmartP	2	A P-frame uses a long-term reference frame and a short-term reference frame.
DualP	2	A P frame uses two reference frames.
BiPredB	2	<ul style="list-style-type: none"><li>A P frame uses two reference frames.</li><li>A B-frame uses a forward reference frame and a backward reference frame.</li></ul>

## 1.2 GOP Structure and Usage in NormalP Mode



### NOTE

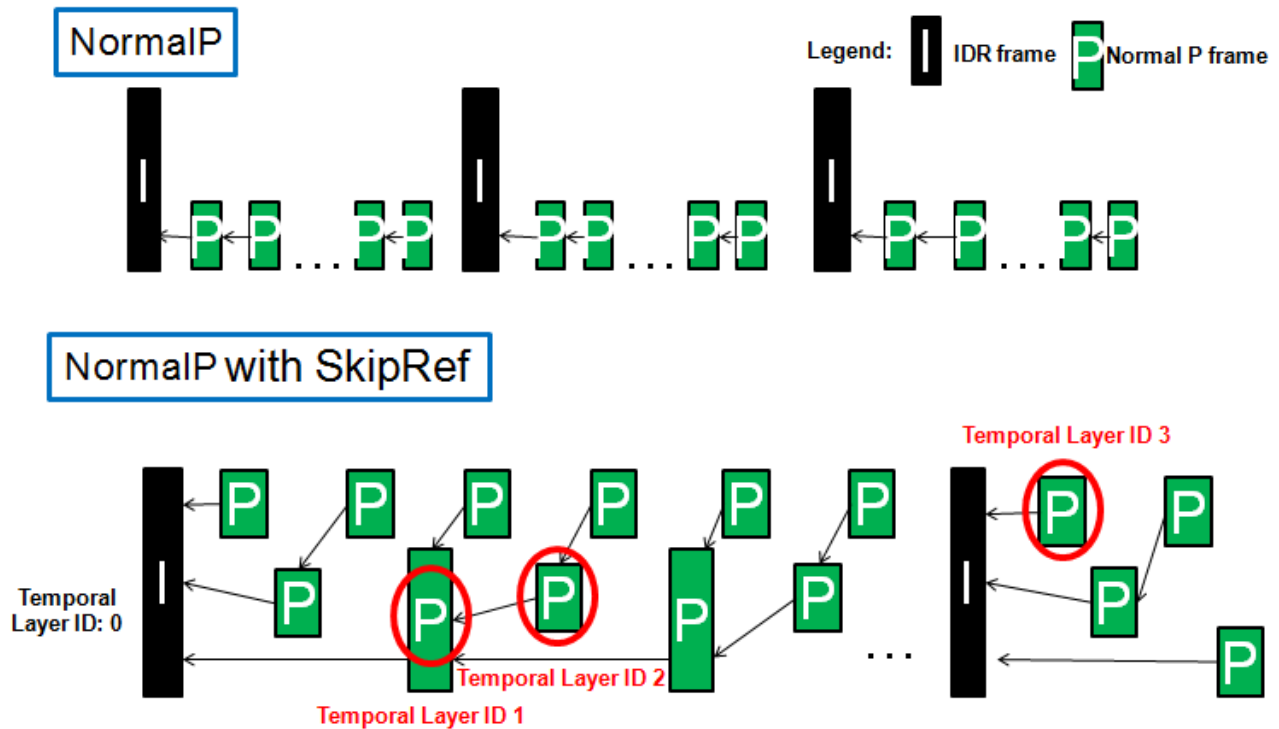
NormalP is a most common GOP structure. All HiSilicon chips support this mode unless otherwise specified.

## 1.2.1 Structure

- The reference relationship of the NormalP mode is simple. Each P-frame uses one forward reference frame.
- The NormalP mode can be used in any scenarios.

Figure 1-1 shows the GOP structure in NormalP mode.

**Figure 1-1** GOP structure in NormalP mode



## 1.2.2 Usage

[API]

HI\_MPI\_VENC\_CreateChn

[Parameters]

VENC\_CHN\_ATTR\_S::stGopAttr.enGopMode = VENC\_GOPMODE\_NORMALP

VENC\_CHN\_ATTR\_S::stGopAttr.stNormalP.s32IPQpDelta (The recommended value is 3. A larger value indicates a larger I-frame and better quality of the I-frame.

## 1.3 GOP Structure and Usage in DualP Mode



### NOTE

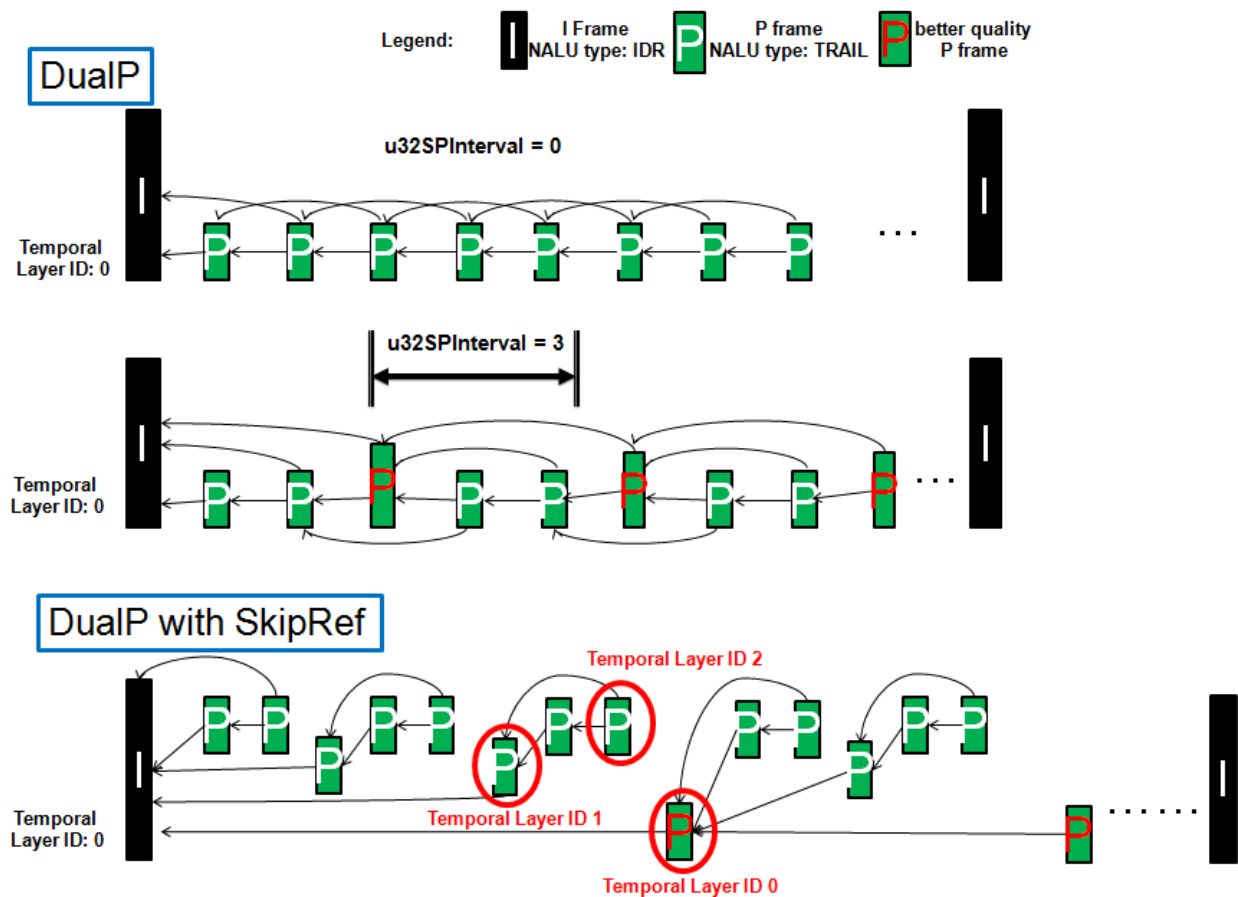
This section is not supported by Hi3559A V100ES.

### 1.3.1 Structure

- SP indicates a special P-frame (also called an SP frame). It is recommended that the QP value of the SP frame be less than that of any other P-frame. If **u32SpInterval** is 0, the SP frame is not supported.
- In DualP mode, the P-frame uses the nearest two forward reference frames. Using the time domain correlation of more reference frames enhances the encoding compression performance. This mode is mainly used in motion scenarios with low delay requirements. The compression performance of the DualP mode is lower than that of the BipredB mode but higher than that of the NormalP mode. The DualP mode features no encoding and decoding delay because the two reference frames are forward reference frames.

Figure 1-2 shows the GOP structure in DualP mode.

**Figure 1-2** GOP structure in DualP mode



### 1.3.2 Usage

[API]

HI\_MPI\_VENC\_CreateChn

[Parameters]

VENC\_CHN\_ATTR\_S::stGopAttr.enGopMode = VENC\_GOPMODE\_DUALP



P-frames with better quality (that is, SP frames) can be encoded periodically to optimize the picture quality.

The interval between SP frames can be configured by setting  
**VENC\_CHN\_ATTR\_S::stGopAttr.stDualP.u32SPInterval.**

## 1.4 GOP Structure and Usage in SmartP Mode



### NOTE

This section is not supported by Hi3559A V100ES.

### 1.4.1 Structure

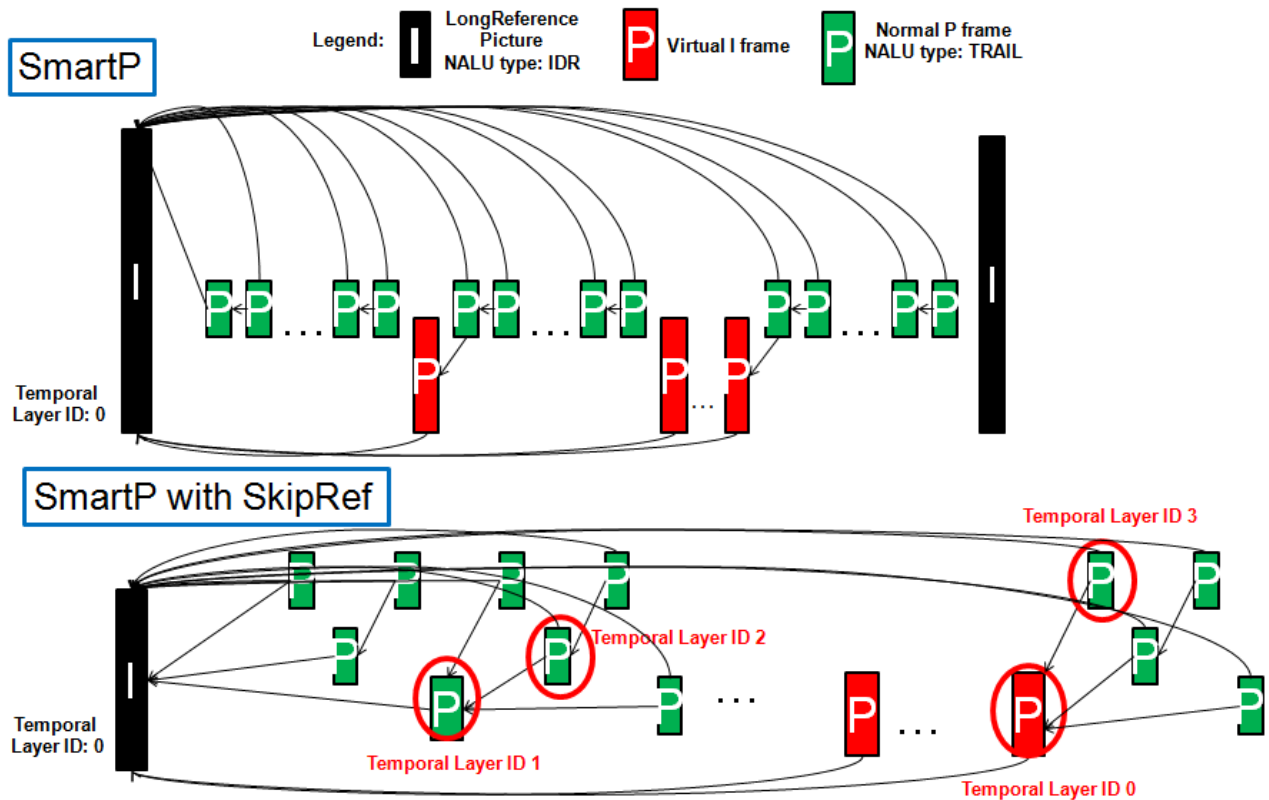
In SmartP mode, the P-frame uses the IDR frame (long-term reference frame) and forward reference frame (short-term reference frame) as reference frames. The time domain correlation of two reference frames is used to improve the encoding compression performance. This mode is mainly used in the monitoring scenario.

In the monitoring scenario, the camera is fixed at a position, and the human and objects in the scenario may be static or moving.

- In static regions, the time domain correlation of the long-term reference frame and the current frame significantly reduces the bit rate and respiratory and smearing effects.
- In motion regions, motion estimation is performed by using the short-term reference frame. In SmartP mode, the IDR frame interval is prolonged, and the virtual I-frame is inserted periodically, which significantly reduces the bit rate by 30% to 50% in the monitoring scenario and improves the picture quality. However, this mode is not applicable to scenarios in which the camera can move.

Figure 1-3 shows the GOP structure in SmartP mode.

Figure 1-3 GOP structure in SmartP mode



## 1.4.2 Usage

[API]

HI\_MPI\_VENC\_CreateChn

[Parameters]

- VENC\_CHN\_ATTR\_S::stGopAttr.enGopMode = VENC\_GOPMODE\_SMARTP
- VENC\_CHN\_ATTR\_S::stGopAttr.stSmartP.u32BgInterval = 1200; // 30fps, 40seconds
- VENC\_CHN\_ATTR\_S::stGopAttr.stSmartP.s32BgQpDelta = 7
- VENC\_CHN\_ATTR\_S::stGopAttr.stSmartP.s32ViQpDelta = 2
- VENC\_CHN\_ATTR\_S::stRcAttr.u32Gop = 30; // virtual I interval
- VENC\_CHN\_ATTR\_S::stRcAttr.u32StatTime = 40; // 40 seconds

## 1.5 GOP Structure and Usage in AdvSmartP Mode



### NOTE

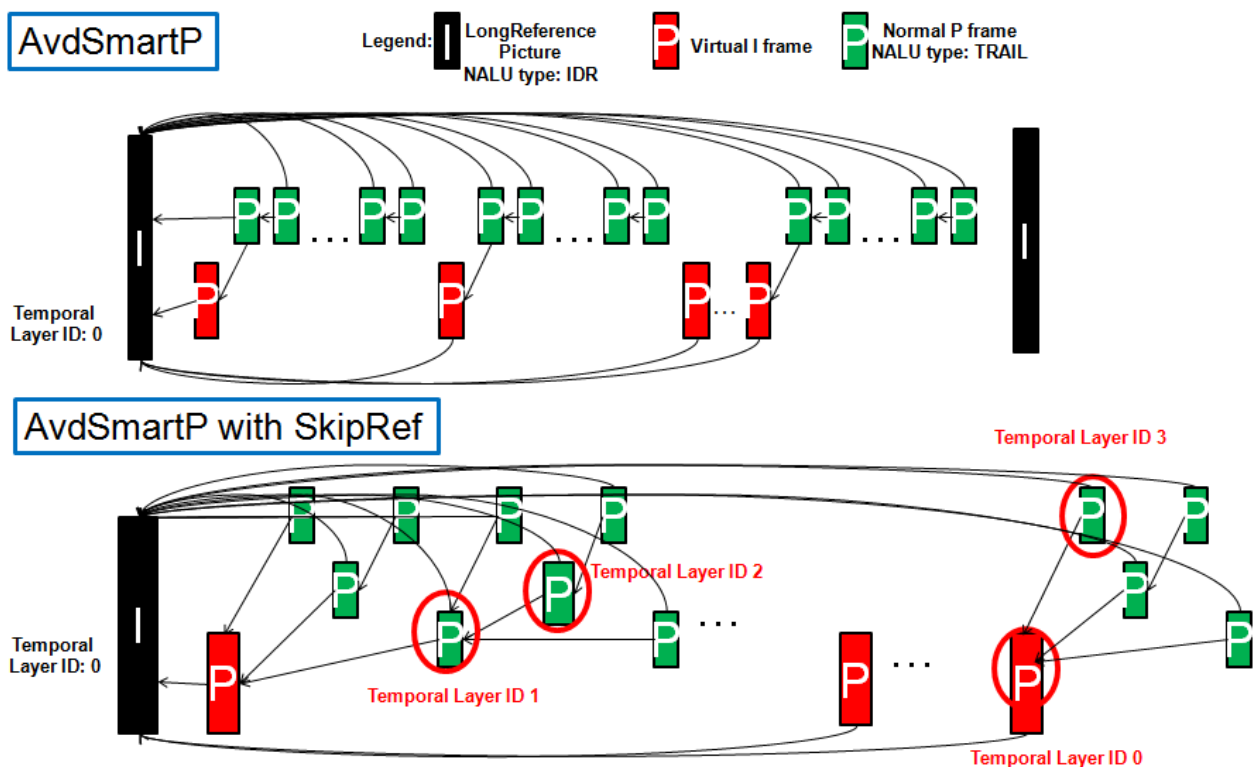
This section is not supported by Hi3559A V100ES/Hi3519A V100/Hi3516C V500/Hi3516D V300/Hi3516E V200.

## 1.5.1 Structure

- The purple frames are encoded as IDR frames and used as long-term reference frames. The red frames are encoded as VI frames (virtual I-frames, which are actually common P-frames). The virtual I-frames use only the IDR frames as reference frames, and it is recommended that the QP value of a virtual I-frame be less than that of other P-frames.
- The GOP structure in AdvSmartP mode is almost the same as that in SmartP mode, and the only difference is that the long-term reference frame IDR frame is internally generated by the encoder and therefore not displayed by the player.

Figure 1-4 shows the GOP structure in AdvSmartP mode.

**Figure 1-4** GOP structure in AdvSmartP mode



## 1.5.2 Usage

[APIs]

- HI\_MPI\_VENC\_CreateChn
- HI\_MPI\_VENC\_EnableAdvSmartP

[Parameters]

- VENC\_CHN\_ATTR\_S::stGopAttr.enGopMode = VENC\_GOPMODE\_ADVSMARTP;
- VENC\_CHN\_ATTR\_S::stGopAttr.stSmartP.u32BgInterval = 1200; // 30fps, 40seconds
- VENC\_CHN\_ATTR\_S::stGopAttr.stSmartP.s32BgQpDelta = 7
- VENC\_CHN\_ATTR\_S::stGopAttr.stSmartP.s32ViQpDelta = 2

- VENC\_CHN\_ATTR\_S::stRcAttr.u32Gop = 30; // virtual I interval
- VENC\_CHN\_ATTR\_S::stRcAttr.u32StatTime = 40; // 40 seconds

## 1.6 GOP Structure and Usage in BiPredB Mode

### 1.6.1 Structure

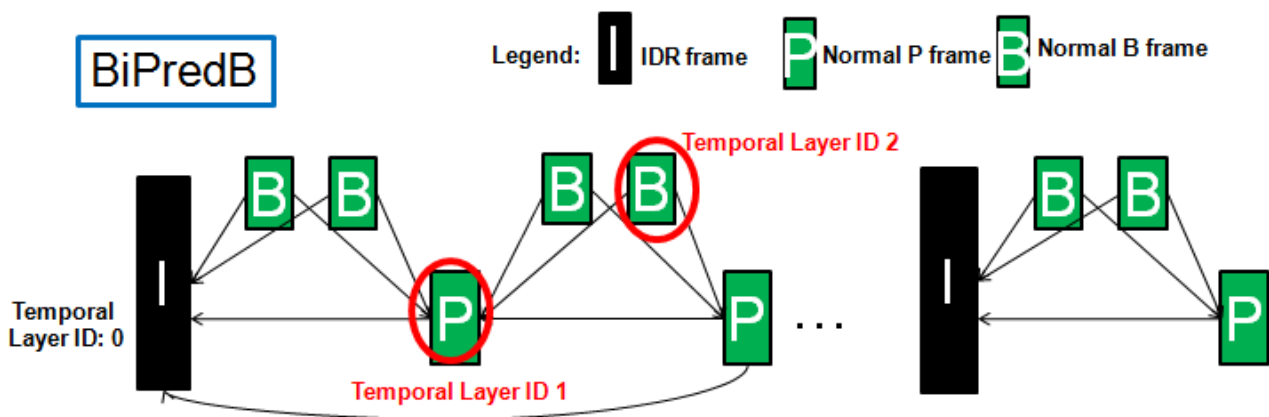
#### NOTE

This section is not supported by Hi3559A V100ES/Hi3516C V500/Hi3516D V300/Hi3516E V200.

- **u32BFrmNum** indicates the number of B-frames between the IDR frame and P-frame or between the P-frames. For example, in [Figure 1-5](#), **u32BFrmNum** is 2. The last frame of each GOP must be a P-frame, and the number of B-frames before it may not meet the requirement of **u32BFrmNum**.
- In BiPredB mode, one to three B-frames can be inserted between two adjacent P-frames. The B-frames are not referenced. One forward reference frame and one backward reference frame are used. For motion scenarios, bidirectional prediction implements better motion estimation and supports weighted prediction, and therefore improves the compression performance of the encoder.

[Figure 1-5](#) shows the GOP structure in BiPredB mode.

**Figure 1-5** GOP structure in BiPredB mode



### 1.6.2 Usage

[API]

HI\_MPI\_VENC\_CreateChn

[Parameters]

- VENC\_CHN\_ATTR\_S::stGopAttr.enGopMode = VENC\_GOPMODE\_BIPREDB
- VENC\_CHN\_ATTR\_S::stGopAttr.stBipredB.u32BFrmNum = 2
- VENC\_CHN\_ATTR\_S::stGopAttr.stBipredB.s32IPQpDelta = 3

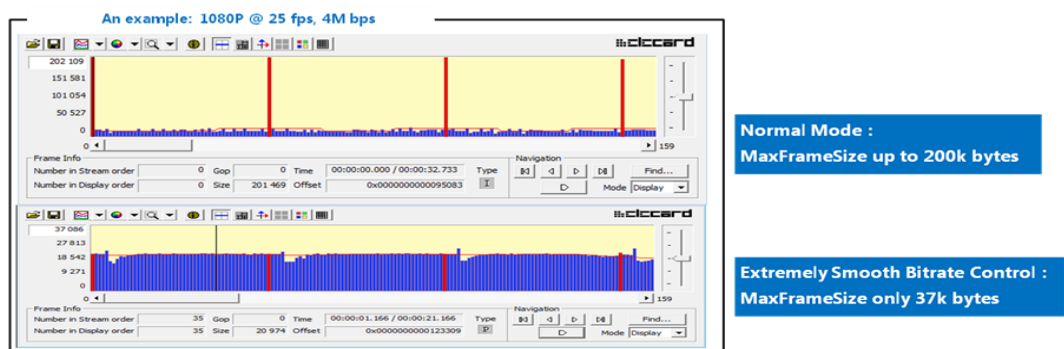
- `VENC_CHN_ATTR_S::stGopAttr.stBipredB.s32BQpDelta = -2`

## 1.7 Principles and Usage of Cyclic Intra Refresh

### 1.7.1 Principles

In common scenarios, the size of an IDR frame is many times larger than that of a P-frame, especially in scenarios with small motions, in which the size of an IDR frame is tens or even a hundred times of a P-frame. During network transmission, wireless transmission in particular, IDR frames cause transient network shocks, which result in frame loss and increase the delay. For general encoding technologies, the QP value of the IDR frame is increased to decrease the size of the IDR frame. However, this also reduces the quality of the IDR frame and causes the respiratory effect. The Cyclic Intra Refresh technology does not change the quality of the IDR frame. It distributes the intra largest coding units (LCUs)/macroblocks of an IDR frame into multiple P-frames, which makes the frame sizes relatively even. For example, by using the Cyclic Intra Refresh technology, the maximum frame size in the 1080P@25 fps monitoring scenario with 4 Mbit/s bit rate can be decreased from 200 KB to 37 KB, ensuring smooth bit rate, as shown in Figure 1-6.

Figure 1-6 Cyclic Intra Refresh effect



### 1.7.2 Usage

[API]

`HI_MPI_VENC_SetIntraRefresh`

[Parameters]

- `VENC_PARAM_INTRA_REFRESH_S::bRefreshEnable = HI_TRUE`
- `VENC_PARAM_INTRA_REFRESH_S::enIntraRefreshMode = INTRA_REFRESH_ROW`; // Refreshing the macroblocks and LCUs by row
- `VENC_PARAM_INTRA_REFRESH_S::u32RefreshNum`; // Number of refreshed macroblock/LCU rows of each frame
- `VENC_PARAM_INTRA_REFRESH_S::u32ReqIQp`; // QP value for requesting the I-frame





## Advantages

- The bit rate is smooth, and the impact on the network is small. This technology applies to the wireless network transmission scenario.
- The encoding, decoding, and network delay is small.
- This technology does not reduce the quality of I-frames or cause serious respiratory effect.

## Limitations

- The first few frames of the video are incomplete because the intra LCUs/macroblocks are distributed in multiple P-frames.
- The decoder must support decoding of streams without IDR frames.
- This technology supports the GOP structure of only the NormalP mode.
- This technology mainly applies to scenarios with high requirements on bit rate smoothness. It does not reduce the bit rate and therefore does not apply to low bit rate scenarios.
- The Hi3559A V100ES/Hi3559A V100 supports only the refreshing of the macroblocks and LCUs by row.

# 1.8 Memory Usage, Delay, Application Scenarios, and Compatibility of the GOP Structure



### NOTE

- **PicSize** is the size of the frame buffer for reference frames, and **AdvInfoSize** is the size of the information frame buffer (the picture format is YUV420) for AdvSmartp.
- For details about how to calculate **PicSize** and **AdvInfoSize**, see the following section.
- **SrcPicSize** is the size of the source picture before encoding. Some source pictures need to be buffered because B-frames are delayed.
- Delay occurs on both the encoding end and decoding end in BiPredB mode, and it does not occur in other modes.

**Table 1-2** Memory usage, delay, and application scenarios

GOP Mode	DDR Usage		Delay		Application Scenarios
	H.264&H.265 Enc	H.264&H.265 Dec	H.264&H.265 Enc	H.264&H.265 Dec	
NormalP	2 x PicSize	2 x PicSize	N/A	N/A	Typical scenarios
SmartP	3 x PicSize	3 x PicSize	N/A	N/A	Monitoring scenarios with fixed cameras
AdvSmartP	3 x PicSize + AdvInfoSize	3 x PicSize	N/A	N/A	Monitoring scenarios with fixed cameras



GOP Mode	DDR Usage		Delay		Application Scenarios
	H.264&H.265 Enc	H.264&H.265 Dec	H.264&H.265 Enc	H.264&H.265 Dec	
DualP	3 x PicSize	3 x PicSize	N/A	N/A	Motion scenarios with moving cameras, such as the event data recorder and handheld DV
BiPredB ( <i>N</i> indicates the number of B-frames.)	3 x PicSize + <i>N</i> x SrcPicSize	3 x PicSize	<i>N</i> frames	1 frame	Motion scenarios with moving cameras, such as the event data recorder and handheld DV

## Calculating PicSize

- The size of each VB of the encoding frame buffer (reference frame and reconstructed frame) is calculated as follows:  

$$\text{PicSize} = \text{YHeaderSize} + \text{CHheaderSize} + \text{YSize} + \text{CSize} + \text{PmeSize} + \text{PmeInfoSize} + \text{TmvSize} + \text{NbiUpSize}$$
- For details about how to calculate sub-items for the frame buffer size, see chapter 6 "VENC" in the *HiMPP V4.0 Media Processing Software Development Reference*.

## Calculating the Frame Buffer of Background Frames in AdvSmartP Mode

The AdvSmartP mode requires an additional frame buffer for the background frames. The size is calculated as follows:

$$\text{BgModelsize} = \text{YSize} + \text{CSize} + \text{ExtYSize} + \text{ExtCSize} + \text{InfoSize}$$

Sub Item of the Frame Buffer		H.265
YSize		Align(Width,64) x Align(Height,16)
CSize		YSize/2
ExtYSize	8 bits	0
	10 bits	Align(Width,64) x Align(Height,16)/4
ExtCSize	8 bits	0
	10 bits	Align(Width,64) x Align(Height,16)/8
InfoSize	8 bits	[Align(Width,64)>>6] x [Align(Height,64)>>6] x 76 x 128
	10 bits	[Align(Width,64)>>6] x [Align(Height,64)>>6] x 100 x 128



## Compatibility

Table 1-3 describes the back-end product compatibility of HiSilicon.

**Table 1-3** Back-end product compatibility of HiSilicon

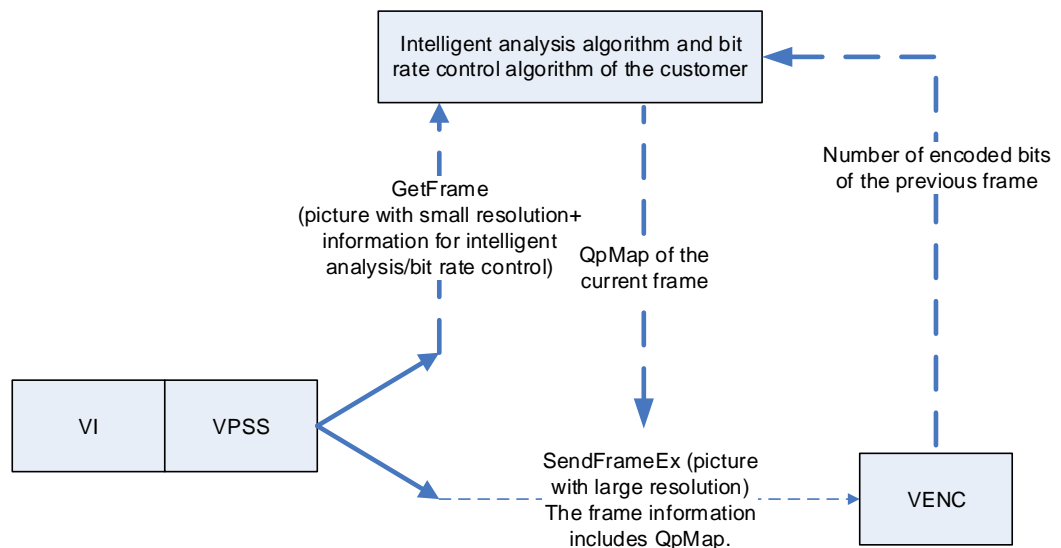
GOP Mode	Hi_H264 Decoder	Hi_H265 Decoder	Hi3536		Hi3531 A/Hi3521A/Hi3520 DV300	Hi3535	Hi3559A V100ES/Hi3559A V100		Hi3519A V100		Hi3516E V200	
	H.264	H.265	H.264	H.265	H.264	H.264	H.264	H.265	H.264	H.265	H.264	H.265
NormalP	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes
SmartP	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes
AdvSmartP	No	Yes	No	Yes	No	No	Yes	Yes	No	No	No	No
DualP	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes
BiPredB	No	Yes	Yes	Yes	No	Yes	Yes	Yes	Yes	Yes	No	No
CyclicIntraRefresh	Yes	Yes	Yes	Yes	Yes	No	Yes	Yes	Yes	Yes	Yes	Yes

# 2 Input Information of the Encoder

## 2.1 QpMap/SkipMap API Definitions

Figure 2-1 shows the overall design system block diagram of the QpMap encoding solution.

**Figure 2-1** Overall design of the QpMap encoding solution



The solution implementation details and precautions are described as follows:

- To ensure that SrcPic and QpMap of each frame are synchronized, the solution needs to be implemented in a user-mode App. The App fetches pictures from the VI/VPSS in user mode, calculates the QpMap of each macroblock in a frame by smart analysis and frame-level bit rate control, and configures the SrcPic and QpMap by using the SendFrameEx API. The frame rate control and frame-level bit rate control are bypassed within the SDK, and the smart analysis and frame-level bit rate control algorithms are developed by the customer.
- There are no picture and stream buffers within the HiSilicon SDK. However, scheduling delay is inevitable. The customer can assess the solution performance and delay.
- For the H.264 and H.265 protocols, QpMap is configured based on the 16x16 block size. However, the encoding coding units (CUs) of H.265 may be greater than 16x16.

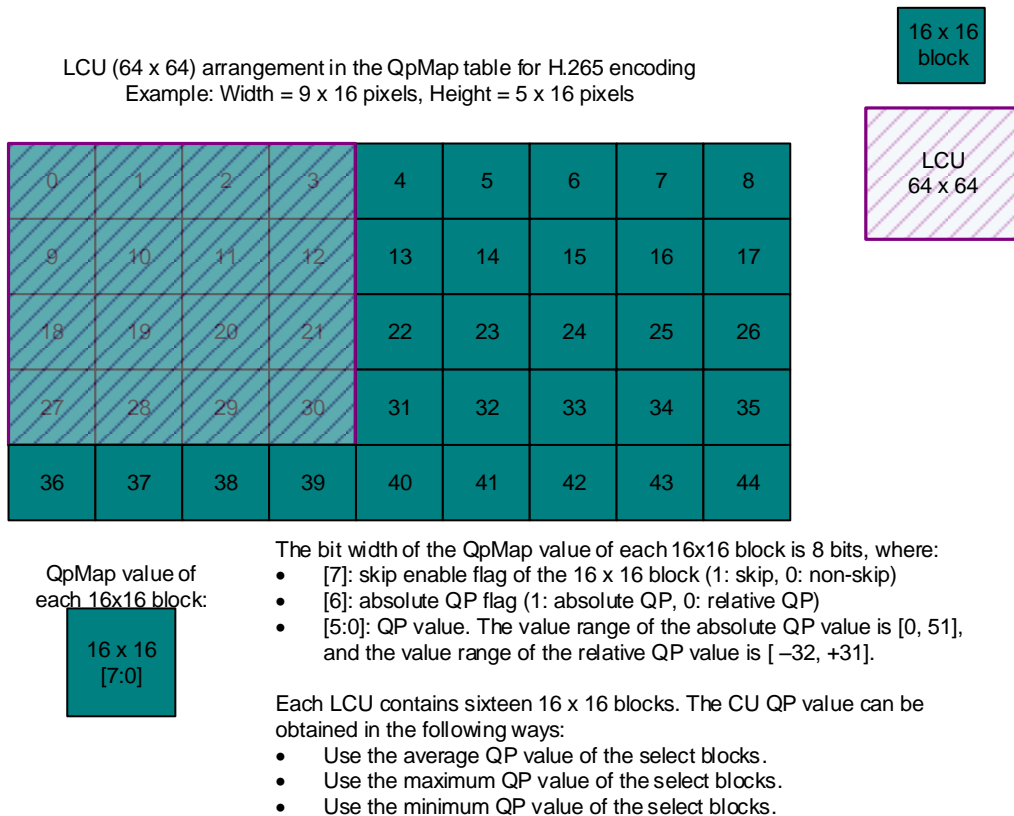
Therefore, the same CU may correspond to multiple QP values. In this case, HiSilicon provides an API for selecting any of the following three modes:

- Using the maximum value of multiple QP values
- Using the minimum value of multiple QP values
- Using the average value of multiple QP values
- The configuration of QpMap supports the relative QP mode and absolute QP mode.
  - In relative QP mode, the code control (macroblock level) calculated based on the internal logic of the encoder also takes effect. The QP input externally by using the QpMap is a variable Qpdelta, which is overlaid on the basic of the original code control (macroblock level).
  - In absolute QP mode, the QP value of each encoding block is specified by external user input but not the internal macroblock level code control algorithm.
- The internal recoding and frame dropping mechanisms of the encoder may not be used.
- The HiSilicon SDK provides the following information for each picture frame:
  - ISP-related information
  - Number of encoded bytes of the previous frame

## 2.1.1 QpMap Table Memory Arrangement

The QP arrangement in the QpMap table for H.265 encoding differs from that for H.264 encoding because the basic encoding unit differs. Figure 2-2 and Figure 2-3 show the QP arrangement for H.264 and H.265 encoding respectively.

**Figure 2-2** LCU arrangement in the QpMap table for H.265 encoding





**Figure 2-3** MB arrangement in the QpMap table for H.264 encoding

MB (16 x 16, 8 bits) arrangement in the QpMap table for H.264 encoding  
Example: Width = 9 x 16 pixels, Height = 4 x 16 pixels

MB

0	1	2	3	4	5	6	7	8
9	10	11	12	13	14	15	16	17
18	19	20	21	22	23	24	25	26
27	28	29	30	31	32	33	34	35

QpMap value of each MB:

16 x 16  
[7:0]

The bit width of the QpMap value of each MB is 8 bits, where:

- [7]: skip enable flag of the 16 x 16 block (1: skip, 0: non-skip)
- [6]: absolute QP flag (1: absolute QP, 0: relative QP)
- [5:0]: QP value. The value range of the absolute QP value is [0, 51], and the value range of the relative QP value is [-32, +31].

## 2.2 Definitions of Encoding APIs

### 2.2.1 Bit Rate Control APIs

```
typedef enum hiVENC_RC_MODE_E
{
    VENC_RC_MODE_H264CBR = 1,
    VENC_RC_MODE_H264VBR,
    VENC_RC_MODE_H264AVBR,
    VENC_RC_MODE_H264FIXQP,
    VENC_RC_MODE_H264QPMP,
    VENC_RC_MODE_MJPEGCBR,
    VENC_RC_MODE_MJPEGVBR,
    VENC_RC_MODE_MJPEGFIXQP,
    VENC_RC_MODE_H265CBR,
    VENC_RC_MODE_H265VBR,
    VENC_RC_MODE_H265AVBR,
    VENC_RC_MODE_H265FIXQP,
}
```



```
VENC_RC_MODE_H265QPMP,
VENC_RC_MODE_BUTT,

}VENC_RC_MODE_E;
typedef struct hiVENC_RC_ATTR_S
{
    VENC_RC_MODE_E enRcMode;
    union
    {
        VENC_H264_CBR_S    stH264Cbr;
        VENC_H264_VBR_S    stH264Vbr;
        VENC_H264_AVBR_S   stH264AVbr;
        VENC_H264_FIXQP_S  stH264FixQp;
        VENC_H264_QPMP_S   stH264QpMap;

        VENC_MJPEG_CBR_S   stMjpegCbr;
        VENC_MJPEG_VBR_S   stMjpegVbr;
        VENC_MJPEG_FIXQP_S stMjpegFixQp;

        VENC_H265_CBR_S    stH265Cbr;
        VENC_H265_VBR_S    stH265Vbr;
        VENC_H265_AVBR_S   stH265AVbr;
        VENC_H265_FIXQP_S  stH265FixQp;
        VENC_H265_QPMP_S   stH265QpMap;
    };
}VENC_RC_ATTR_S;
```

- The QpMap is used as the adaptive ROI mode, the RC mode is set to the constant bit rate (CBR) or variable bit rate (VBR), and the QpMap uses the relative QP mode.
- The QpMap is used as external bit rate control, and the RC mode is set to the QpMap mode. The internal RC algorithm is bypassed, and the frame-level bit rate control and macroblock-level bit rate control are implemented externally in this case.

## 2.2.2 Encoding Image Transmission API

```
typedef struct hiUSER_RC_INFO_S
{
    HI_BOOL bQpMapValid;
    HI_BOOL bSkipWeightValid;
    HI_U32 u32BlkStartQp;
    HI_U64 u64QpMapPhyAddr;
    HI_U64 u64SkipWeightPhyAddr;
    VENC_FRAME_TYPE_E enFrameType;
} USER_RC_INFO_S;

typedef struct hiUSER_FRAME_INFO_S
```

```
{  
    VIDEO_FRAME_INFO_S stUserFrame;  
    USER_RC_INFO_S      stUserRcInfo;  
} USER_FRAME_INFO_S;  
/*-1:block 0:nonblock >0 : overtime */  
HI_S32 HI_MPI_VENC_SendFrameEx(VENC_CHN VeChn, USER_FRAME_INFO_S  
*pstFrame ,HI_S32 s32MilliSec);
```

## 2.3 Adaptive ROI by Using the QpMap/SkipMap

Figure 2-4 shows an example of an ROI.

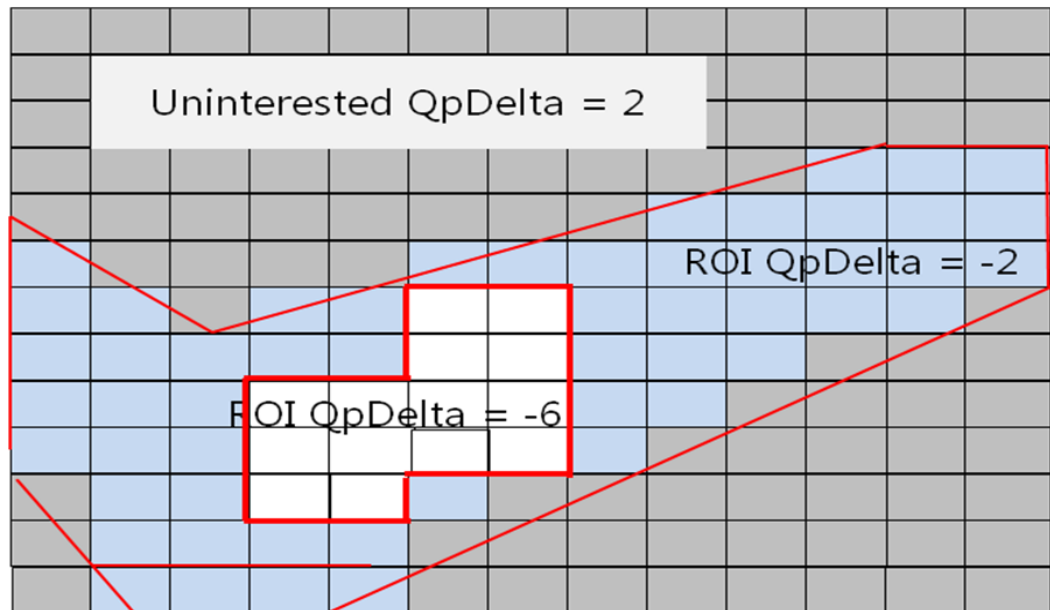
**Figure 2-4** ROI example



The QpMap is used to adjust the QP values of the ROI and non-ROI, as shown in Figure 2-5.



**Figure 2-5** Adjusting the QP values of the ROI and non-ROI by using the QpMap



You can draw ROIs with any shape and any quantity by using the QpMap. You can also improve or reduce the picture quality in ROIs by reducing or increasing the QP values respectively. In this mode, the relative QP adjustment mode is required.

## 2.4 External Bit Rate Control by Using QpMap/SkipMap

During external bit rate control by configuring the QpMap, the QP values can be configured in relative or absolute mode. The QP values of all LCUs/MBs are externally specified, and the frame-level bit rate control does not take effect.



# 3 Output Information of the Encoder

---

## 3.1 SSE and PSNR Information

- PSNR indicates the signal-to-noise ratio of the entire frame picture. A larger value indicates better picture quality.
- Besides measuring the PSNR of the entire frame picture, the encoder also supports the output of SSE information that reflects local picture quality. The encoder supports the output of SSE of a maximum of eight regions. You can calculate the region SNR based on the SSE and the size of the eight regions, which is currently not supported by Hi3559A V100ES.

## 3.2 HeaderBits and ResidualBits Information

**HeaderBits** and **ResidualBits** are CU-level information. **HeaderBits** contains the block type and inter-frame and intra-frame prediction information, and **ResidualBits** contains the residual information after transformation and quantization.

- A larger **HeaderBits** value indicates:
  - More picture textures and finely divided blocks
  - More complex motions, more relative motions, and more MV information.
- A larger **ResidualBits** value indicates:
  - More irregular motions or excessive motions (which are beyond the range of the search window), and more residuals after motion compensation
  - More complex spatial textures and more residuals after intra-frame prediction
  - Larger noises of the original picture

## 3.3 Madi and Madp Information

**Madi** is used to measure the spatial texture complexity of the current frame, that is, the texture variation based on pixel values within a block. The block size can be 16 x 16, 32 x 32, or 64 x 64.

**Madi** is calculated as follows:



$$f = \frac{1}{N} \sum_{n=0}^{N-1} |\text{Pix}_n - \overline{\text{Pix}}|$$

$$\overline{\text{Pix}} = \frac{1}{N} \sum_{n=0}^{N-1} \text{Pix}_n$$

- $N$  indicates the number of pixels in a 16x16, 32x32, or 64x64 block.  $\overline{\text{Pix}}$  is the average luminance value within a block, and  $f$  is the luminance complexity within a block.
- The encoder supports the reporting of frame-level Madi statistic information (average Madi values of all LCU units within a frame). This reporting of information is supported only by H.265.

**Madp** is used to measure the temporal motion complexity of the current frame. It is the average sum of absolute difference (SAD) after motion search and compensation on all blocks (unit: a 16x16 block) in a frame picture (which is a 1/4 down-sampling picture of the original picture). It measures the average encoding temporal complexity of all blocks. Its value reflects the encoding pressure of a frame picture. This reporting of information is supported only by H.265.

**Madp** is calculated as follows:

$$SAD = \left( \sum_{n=0}^{N-1} \sum_{m=0}^{M-1} |\text{Pix\_ori}(n, m) - \text{Pix\_pre}(n, m)| \right)$$

$$f = \left( \sum_{n=0}^{N-1} \sum_{m=0}^{M-1} |\text{Pix\_ori}(n, m) - \text{Pix\_pre}(n, m)| \right) / M * N$$

SADs are classified into block-level SADs and frame-level SADs. For the block-level SAD (16x16 block),  $M$  and  $N$  are 16; for the frame-level SAD,  $M$  is the picture height, and  $N$  is the picture width. **Pix\_ori(n,m)** indicates the original pixel, **Pix\_pre(n,m)** indicates the predicted pixel based on the ME search, and  $f$  indicates the Madp.

## 3.4 QP Histogram

The QP histogram is a histogram that displays the QP values used by all blocks (unit: 4x4 block). The QP values are actual QP values used by the luminance component (Y) during the encoding process in skip mode or non-skip mode. The actual QP values are inferred from the frame-level, row-level, and CU-level bit rate control. The histogram has 52 levels (supported by both H.264 and H.265).

## 3.5 Other Reporting Information



### NOTE

Unless otherwise specified, the reporting information described in [Table 3-1](#) is supported by the product versions to which this document applies.



**Table 3-1** Other reporting information

Name		Description
MeanQp		Average QP value for encoding the entire frame picture. It reflects the encoding pressure during the encoding process.
StartQp		Starting QP of the current frame
H.264	Inter16x16MbNum	Number of macroblocks to be encoded in inter16x16 prediction encoding mode in the current frame
	Inter8x8MbNum	Number of macroblocks to be encoded in inter8x8 prediction encoding mode in the current frame
	Intra16MbNum	Number of macroblocks to be encoded in intra16 prediction encoding mode in the current frame
	Intra8MbNum	Number of macroblocks to be encoded in intra8 prediction encoding mode in the current frame
	Intra4MbNum	Number of macroblocks to be encoded in intra4 prediction encoding mode in the current frame
H.265	Inter64x64CuNum	Number of CUs to be encoded in inter64x64 prediction mode in the current frame
	Inter32x32CuNum	Number of CUs to be encoded in inter32x32 prediction mode in the current frame
	Inter16x16CuNum	Number of CUs to be encoded in inter16x16 prediction mode in the current frame
	Inter8x8CuNum	Number of CUs to be encoded in inter8x8 prediction mode in the current frame
	Intra32x32CuNum	Number of CUs to be encoded in intra32x32 prediction mode in the current frame
	Intra16x16CuNum	Number of CUs to be encoded in intra16x16 prediction mode in the current frame
	Intra8x8CuNum	Number of CUs to be encoded in intra8x8 prediction mode in the current frame
	Intra4x4CuNum	Number of CUs to be encoded in intra4x4 prediction mode in the current frame