

# **HiFB Development Guide**

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# **About This Document**

# **Purpose**

As a module of the HiSilicon digital media processing platform (HiMPP), the HiSilicon frame buffer (HiFB) is used to manage the graphics layers. The HiFB is developed based on the Linux frame buffer. Besides the basic functions provided by the Linux frame buffer, the HiFB also provides extended functions for controlling graphics layers such as the interlayer alpha and origin setting. This document describes how to load the HiFB, and how to develop products or solution by using the HiFB for the first time.

#### NOTE

- Unless otherwise specified in this document, the descriptions of Hi3516D and Hi3516A are the same. The descriptions of Hi3516E V100 and Hi3516C V300 are the same.
- Unless otherwise specified in this document, the descriptions of Hi3518E V201, Hi3516C V200, and Hi3518E V200 are the same.
- Unless otherwise specified in this document, the descriptions of Hi3556 V100 and Hi3559 V100 are the same. The descriptions of Hi3516A V200 and Hi3519 V101 are the same.
- Unless otherwise specified in this document, the descriptions of Hi3559C V100, Hi3519A V100, and Hi3559A V100 are the same. The descriptions of Hi3556A V100 and Hi3519A V100 are the same.
- Unless otherwise specified in this document, the descriptions of Hi3516D V300, Hi3516A V300, Hi3559 V200, Hi3556 V200, and Hi3516C V500 are the same.

### **Related Versions**

The following table lists the product versions related to this document.

<b>Product Name</b>	Version
Hi3516A	V100
Hi3516D	V100
Hi3518E	V200
Hi3518E	V201
Hi3516C	V200
Hi3516C	V300
Hi3516E	V100

Product Name	Version
Hi3519	V100
Hi3519	V101
Hi3559	V100
Hi3556	V100
Hi3516A	V200
Hi3559A	V100ES
Hi3559A	V100
Hi3559C	V100
Hi3519A	V100
Hi3556A	V100
Hi3516C	V500
Hi3516D	V300
Hi3516A	V300
Hi3559	V200
Hi3556	V200
Hi3516E	V200
Hi3516E	V300
Hi3518E	V300
Hi3516D	V200

# **Intended Audience**

This document is intended for:

- Technical support engineers
- Software development engineers

# **Related Version**

The following table lists the product version related to this document.



Symbol	Description
<b>▲ DANGER</b>	Indicates an imminently hazardous situation which, if not avoided, will result in death or serious injury.
<b><u>∧</u> WARNING</b>	Indicates a potentially hazardous situation which, if not avoided, could result in death or serious injury.
<b>⚠ CAUTION</b>	Indicates a potentially hazardous situation which, if not avoided, may result in minor or moderate injury.
NOTICE	Indicates a potentially hazardous situation which, if not avoided, could result in equipment damage, data loss, performance deterioration, or unanticipated results.  NOTICE is used to address practices not related to personal injury.
Ш NОТЕ	Calls attention to important information, best practices and tips.  NOTE is used to address information not related to personal injury, equipment damage, and environment deterioration.

# **Change History**

Changes between document issues are cumulative. The latest document issue contains all changes made in previous issues.

#### Issue 16 (2019-02-28)

Section 2.2 is modified.

#### Issue 15 (2019-01-14)

In section 1.2, a note is added to "Standard and Extended Functions."

Section 2.3 is modified.

#### Issue 14 (2018-12-20)

Section 3.1 is modified.

#### Issue 13 (2018-11-23)

The descriptions of Hi3516E V200, Hi3516E V300, and Hi3518E V300 are added.

#### Issue 12 (2018-09-04)

The descriptions of Hi3516C V500 and Hi3516D V300 are added.

#### Issue 11 (2018-07-31)

The description of Hi3556A V100 is added.

#### Issue 10 (2018-04-25)

The descriptions of Hi3516C V300 and Hi3516E V100 are added.

Chapter 4 is added.

#### Issue 09 (2017-11-30)

The description of Hi3559A V100 is added.

#### Issue 08 (2017-05-27)

The description of Hi3559A V100ES is added.

#### Issue 07 (2017-02-25)

The description of Hi3556 V100 is added.

#### Issue 06 (2016-12-20)

The description of Hi3559 V100 is added.

#### Issue 05 (2016-05-10)

The description of Hi3519 V101 is added.

#### Issue 04 (2015-12-28)

In section 2.2, the description of the **softcursor** parameter is deleted.

#### Issue 03 (2015-08-20)

The description of Hi3519 V100 is added.

#### Issue 02 (2015-06-23)

The descriptions of Hi3518E V200/V201 and Hi3516C V200 are added.

Sections 1.2 and 2.2 are modified.

#### Issue 01 (2014-12-20)

The description of Hi3516D is added.

#### Issue 00B01 (2014-09-14)

This issue is the first draft release.



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# 1 Overview

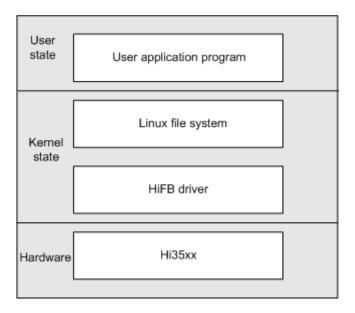
#### 1.1 HiFB Overview

As a module of the HiMPP, the HiSilicon Framebuffer (HiFB) is used to manage the graphics layers. The HiFB provides not only the basic functions of the Linux FB, but also some extended functions such as the interlayer colorkey, interlayer colorkey mask, interlayer Alpha, and origin offset.

# 1.1.1 System Architecture

The application program uses the HiFB through the Linux file system. **Figure 1-1** shows the system architecture of the HiFB.

Figure 1-1 System architecture of the HiFB



# 1.1.2 Application Scenarios

The HiFB applies to the following scenarios:



- MiniGUI window system
  - The MiniGUI window system supports the Linux FB. With a slight modification, the MiniGUI window system can be ported to the Hi35xx quickly.
- Other application programs based on the Linux FB
   Without modification or with a slight modification, the Linux-FB-based application programs can be ported to the Hi35xx quickly.

# 1.2 Comparing the HiFB with the Linux FB

#### **Managing Graphics Layers**

For the Linux FB, a sub device number corresponds to a video device. For the HiFB, a sub device number corresponds to a graphics layer. The HiFB manages multiple graphics layers and the number of the graphics layers is determined by the chip.

#### NOTE

- The Hi3516A/Hi3518E V200/Hi3516C V300/Hi3519 V100/Hi3519 V101/Hi3559 V100 HiFB manages at most one graphics layer (G0) that corresponds to device file /dev/fb0.
- The Hi3559A V100ES HiFB manages at most two graphics layers (G0 and G1) that correspond to device files /dev/fb0 and /dev/fb1.
- The Hi3559A V100/Hi3519A V100 HiFB manages at most three graphics layer (G0, G1, and G2) that correspond to device files /dev/fb0, /dev/fb1, and /dev/fb2.
- The Hi3516A/Hi3518E V200/Hi3516C V300/Hi3519 V100/Hi3519 V101/Hi3559 V100 supports
  one SD VO device (DSD0) that can be overlaid with graphics layers. Table 1-1 describes the
  mapping between the graphics layer and VO device of the Hi3516A/Hi3518E V200/Hi3519 V100/
  Hi3519 V101/Hi3559 V100.
- The Hi3559A V100ES/Hi3559A V100/Hi3519A V100 supports two HD VO device (DHD0 and DHD1) that can be overlaid with graphics layers. Table 1-2 describes the mapping between the graphics layer and VO device of the Hi3559A V100ES/Hi3559A V100/Hi3519A V100.
- Hi3516C V500/Hi3516D V300/Hi3516E V200/Hi3516E V300/Hi3518E V300 supports one HD VO device (DHD0) that can be overlaid with graphics layers. Table 1-3 describes the mapping between the graphics layer and VO device of the Hi3516C V500/Hi3516D V300/Hi3516E V200/Hi3516E V300/Hi3518E V300.

**Table 1-1** Mapping between the graphics layers and VO devices of the Hi3516A/Hi3518E V200/Hi3516C V300/Hi3519 V100/Hi3519 V101/Hi3559 V100

FB Device File	Graphics Layer	Corresponding Display Device
/dev/fb0	G0	G0 is displayed only on DSD0.

**Table 1-2** Mapping between the graphics layers and VO devices of the Hi3559A V100ES/ Hi3559A V100/Hi3519A V100

FB Device File	Graphics Layer	Corresponding Display Device
/dev/fb0	G0	G0 is displayed only on DHD0.

FB Device File	Graphics Layer	Corresponding Display Device
/dev/fb1	G1	G1 is displayed only on DHD1.
/dev/fb2	G3	G3 is dynamically bound to and displayed on DHD0 or DHD1.

**Table 1-3** Mapping between the graphics layers and VO devices of the Hi3516C V500/Hi3516D V300/Hi3516E V200/Hi3516E V300/Hi3518E V300

FB Device File	Graphics Layer	Corresponding Display Device
/dev/fb0	G0	G0 is displayed only on DHD0.

By setting the module loading parameter, you can configure the HiFB to manage one or more graphics layers and operate graphics layers as easily as files.

# **Differences Among Chips**

Chip	Supporte d Graphics Layer	Compression	Colorkey	Binding Relationship	Soft Cursor Layer
Hi3516A/Hi3518E V200/Hi3516C V300/Hi3519 V100/ Hi3519 V101/ Hi3559 V100	G0	G0 does not support compression.	Supported	G0 is always bound to DSD0.	Not supported
Hi3559A V100ES	G0	G0 supports compression.	Supported	G0 is always bound to DHD0.	Not supported
	G1	G1 supports compression.	Supported	G1 is always bound to DHD1.	Not supported
Hi3559A V100/ Hi3519A V100	G0	G0 supports compression.	Supported	G0 is always bound to DHD0.	Not supported
	G1	G1 supports compression.	Supported	G1 is always bound to DHD1.	Not supported
	G3	G3 does not support compression.	Supported	G3 is dynamically bound to DHD0 or DHD1.	Not supported



Chip	Supporte d Graphics Layer	Compression	Colorkey	Binding Relationship	Soft Cursor Layer
Hi3516C V500/ Hi3516D V300/ Hi3516E V200/ Hi3516E V300/ Hi3518E V300	G0	G0 does not support compression.	Supported	G0 is always bound to DHD0.	Not supported

#### **Controlling the Timing**

The Linux FB provides the controlling modes (hardware support required) such as the synchronous timing, scanning mode, and synchronous signal mechanism. The contents of the physical display buffer are displayed through different output devices such as the PC monitor, TV, and LCD. At present, the HiFB does not support the controlling modes such as synchronous timing, scanning mode, and synchronous signal mechanism.

#### **Standard and Extended Functions**

The HiFB supports the following standard functions of the Linux FB:

- Create/Destroy a map between the physical display buffer and the virtual memory.
- Operate the physical display buffer like a common file.
- Set the hardware display resolution and the pixel format. The maximum resolution and the pixel format supported by each graphics layer can be obtained through the support capability interface.
- Perform the read, write and display operations from any position of the physical display buffer.
- Set and obtain 256-color palette when the graphics layer supports the index format.

The HiFB has the following extended functions:

- Set and obtain the Alpha value of the graphics layer.
- Set and obtain the colorkey values of the graphics layer.
- Set the start position of the current graphics layer (namely, the offset from the screen origin).
- Set and obtain the display state of the current graphics layer (display/hide).
- Set the size of HiFB physical display buffer and manage the number of the graphics layers through the module loading parameters.
- Set and obtain the status of anti-flicker.
- Set and obtain the premultiplication mode.
- Set and obtain the status of the compression mode.
- Set and obtain the DDR detection status.
- Set and obtain the refresh mode of graphics layers (non-buffer mode, single-buffer mode, and dual-buffer mode).

• Operations related to the soft cursor layer

The HiFB does not support the following standard functions of the Linux FB:

- Set and obtain the Linux FB of corresponding console.
- Obtain the real-time information about hardware scanning.
- Obtain the hardware-related information.
- Obtain the hardware synchronous timing.
- Obtain the hardware synchronous signal mechanism.

#### NOTE

To support the **fb\_imageblit** function on Hi3516C V500 or Hi3516D V300, enable the **CONFIG\_FB\_CFB\_IMAGEBLIT** configuration in the kernel source code.

#### Refresh Mode of Graphics Layers - Extended FB Mode

The HiFB provides a comprehensive refresh scheme for upper-layer users, which is called extended FB mode. You can select an appropriate refresh type based on the system performance, memory size, and graphics display effect. The supported refresh modes include:

• Non-buffer mode (HIFB\_LAYER\_BUF\_NONE)

The canvas buffer for upper-layer users is the display buffer. In this mode, the required memory is reduced, and the refresh speed is the fastest, but users can view the graphics drawing process. The diagram is shown in **Figure 1-2**.

• Single-buffer mode (HIFB\_LAYER\_BUF\_ONE)

The display buffer is provided by the HiFB. Therefore, a certain memory is required. In this mode, the display effect and required memory are balanced. However, jaggies appear. See **Figure 1-3**.

Dual-buffer mode

The display buffer is provided by the HiFB. Compared with the preceding two modes, the dual-buffer mode requires the most memory, but provides the best display effect, as shown in **Figure 1-4**. The refresh modes are as follows:

- HIFB\_LAYER\_BUF\_DOUBLE
- HIFB LAYER BUF DOUBLE IMMEDIATE

The difference between these two refresh modes is that the corresponding functions are returned only after the drawn contents are displayed when the HIFB LAYER BUF\_DOUBLE IMMEDIATE refresh mode is used.

Figure 1-2 Non-buffer mode

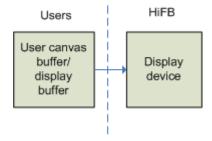




Figure 1-3 Single-buffer mode

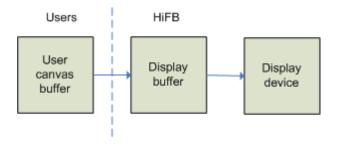
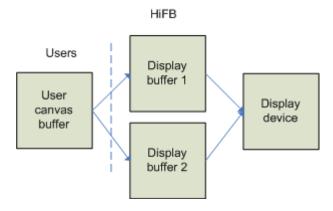


Figure 1-4 Dual-buffer mode



#### NOTE

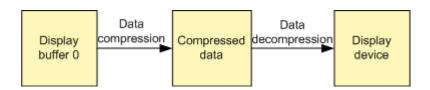
The preceding resolutions are canvas resolution (resolution of user canvas buffers), display buffer resolution, and screen display resolution. When drawn contents are transferred from the user canvas buffer to the display buffer, scaling and anti-flicker are supported. When drawn contents are transferred from the display buffer to the display device, scaling and anti-flicker are not supported. Therefore, the display buffer resolution is always the same as the screen display resolution.

#### **Graphics Layer Compression**

Graphics layer compression indicates that graphics layers compress the display buffer to generate compressed data, and then decompress the data for display. When the data in the display buffer does not change, graphics layers load and decompress the compressed data for display. When the compression function of a graphics layer is enabled, the bandwidth loaded by the bus is reduced, but an extra memory for storing a frame of compressed data is required.

Figure 1-5 shows the workflow of a graphics layer compression buffer.

Figure 1-5 Workflow of a graphics layer compression buffer



The compression function is irrelevant to the refresh mode. To be specific, the compression function can be enabled or disabled in both the standard mode of the Linux FB and extended FB refresh mode.

#### **DDR** Detection

DDR detection indicates that graphics layers detect whether the data in the display buffer changes. DDR detection takes effect only when the refresh mode is non-buffer mode and the compression function is enabled. When the DDR change is detected, the compression function is enabled to update the compressed data, which prevents refresh operations for display.

# 1.3 Related Document

See the HiFB API Reference.



# 2 Loading Drivers

# 2.1 Principle

The display attributes of some Linux FB drivers (for example versa), such as resolution, color depth, and timing cannot be changed during the operation. The Linux provides a mechanism that allows the system to transfer options to the Linux FB through the parameters in the case of the kernel booting or module loading. The kernel booting parameters can be set in the kernel loader. For the HiFB driver, only physical video display size can be set in the case of module loading.

When the HiFB driver **hifb.ko** is loaded, ensure that the standard FB driver **fb.ko** has been loaded. If **fb.ko** is not loaded, run **modprobe fb** to load **fb.ko** and **hifb.ko** in sequence.

# 2.2 Parameter Configuration

The HiFB can be used to set the size of the physical display buffer for the managed graphics layers. The size of the physical display buffer determines the maximum capacity of the physical display buffer used in the HiFB and the virtual resolution. When loading the HiFB module, the size of the physical display buffer is set through the parameter. The size of the physical display buffer cannot be changed after it is set.

#### video Parameter

video="hifb:vram0 size:xxx, vram1 size:xxx,..."

#### NOTE

- Items are separated with commas (,).
- An item and an item value are separated with a colon (:).
- If the size of the physical display buffer corresponding to a graphics layer is not set, the buffer is 0 by default.
- vram0 size-vram3 size correspond to G0-G3.

Where, **vramn\_size:** xxx indicates the size of the physical display buffer configured for the graphics layer n.

(1) For the standard FB mode, the relationship between vramn\_size and virtual resolution is as follows:

Vramn\_size \* 1024 >= xres\_virtual \* yres\_virtual \* bpp;

where **xres\_virtual** \* **yres\_virtual** indicates the virtual resolution, and **bpp** indicates the number of bytes occupied by each pixel.

(2) For the extended FB mode, the required memory depends on the value of **displaysize**, pixel format of the graphics layer, and refresh mode. The relationship is as follows:

```
vramn size * 1024 >= displaywidth * displayHeight * bpp * BufferMode;
```

Assume that the refresh mode is dual-buffer mode, the resolution is 1280x720, and the pixel format is ARGB8888 format. The required memory of G0 is calculated as follows: vram0 size =  $1280 \times 720 \times 4 \times 2 = 7200 \text{ KB}$ .

NOTE

The value of **vramn\_size** must be a multiple of **PAGE\_SIZE** (4 KB). Otherwise, the HiFB rounds up the value to a multiple of **PAGE\_SIZE**.

#### apszLayerMmzNames Parameter

This parameter determines that the memory used by each graphics layer is allocated from which media memory zone (MMZ). This parameter is a string array and has at most four values that correspond to **fb0–fb3**. After the HiFB driver is loaded, the MMZ whose memory is used by each graphics layer can be determined. If this parameter is not set, the anonymous MMZ is used.

#### **Default Parameter Values**

If the program has no parameter when the HiFB driver is loaded, the default parameter values are as follows:

- Hi3516A/Hi3518EV200/Hi3516CV300/Hi3519V100/Hi3519V101/Hi3559V100/ Hi3559AV100ES/Hi3559AV100/Hi3519AV100/Hi3516CV500/Hi3516DV300 video="hifb:vram0\_size:8100"
- Hi3516E V200: video="hifb:vram0\_size:1620"

You must configure the graphics layers managed by the HiFB, specify the MMZ whose memory is allocated, and allocate appropriate display buffer for each graphics layer from a global aspect.

# 2.3 Configuration Examples

The examples of configuring the graphics layers managed by the HiFB are as follows:

NOTE

The module file of the HiFB driver is hifb.ko for Linux and libhifb.a for Huawei LiteOS.

Configure the HiFB to manage one graphics layer

If the HiFB manages only G0, the maximum virtual resolution is 720 x 576, and the pixel format is ARGB1555, the minimum display buffer of G0 is 720 x 576 x 2 = 829440 = 810 K. The parameter is set as follows:

insmod hifb.ko video="hifb:vram0 size:810, vram2 size:0".

If the dual-buffer mode is used, the value of **vram0\_size** must be multiplied by 2. That is, the parameter is set as follows:

#### insmod hifb.ko video="hifb:vram0\_size:1620, vram2\_size:0"

• Configure the HiFB to manage multiple graphics layers

If the HiFB manages G0 and G1, the maximum virtual resolution is 720 x 576, and the pixel format ARGB1555, the minimum display buffer of the two graphics layers is 720 x 576 x 2 = 829440 = 810 K. The parameter is set as follows:

insmod hifb.ko video="hifb:vram0\_size:810, vram1\_size: 810".

# 2.4 Exception

If the physical display buffer for a graphics layer is incorrectly configured, the HiFB does not manage the graphics layer.

# 3 Initial Development Application

# 3.1 Development Process

The HiFB displays two-dimensional images (in the mode of operating on the physical display buffer directly).

Figure 3-1 shows the development process of the HiFB.



Enable the VO device.

Enable the HIFB device.

Set parameters such as the pixel format, screen height, screen width.

Obtain the fixed information such as the physical display buffer size and stride.

Map the physical display buffer to the virtual memory space.

Perform drawing by using the virtual memory.

Clear the memory mapping.

Stop the device.

Figure 3-1 Development process of the HiFB

To develop the HiFB, perform the following steps:

- **Step 1** Call the VO initialization interface to enable the VO device.
- Step 2 Call the open function to start the HiFB device.
- **Step 3** Call the ioctl function to set parameters of the HiFB, such as the pixel format, screen height, and screen width. For details, see the HiFB API Reference.
- **Step 4** Call the ioctl function to obtain the fixed information about the HiFB, such as the physical display buffer size and the stride. You can call the ioctl function to use the interlayer colorkey, interlayer colorkey mask, interlayer Alpha, and origin offset provided by the HiFB.
- **Step 5** Call the mmap function to map the physical display buffer to the virtual memory space.
- **Step 6** Operate the virtual memory to perform the specific drawing tasks. In this step, you can use the dual-buffer page up/down function provided by the HiFB to implement drawing effects.



- **Step 7** Call munmap to clear the display buffer mapping.
- **Step 8** Call the close function to stop the device.

#### ----End

#### NOTE

The modification of the virtual resolution may change the HiFB fixed information fb fix screeninfo::line length (stride). To ensure that the drawing program runs properly, it is recommended that you set the HiFB variable information fb var screeninfo and then obtain the HiFB fixed information fb fix screeninfo::line length.

Table 3-1 lists tasks of the HiFB completed in each development phase.

Table 3-1 Tasks of the HiFB completed in each development phase

Phase	Task
Initialization	Set the display attributes and map the physical display buffer.
Drawing	Perform the specific drawing operations.
Termination	Clean up resources.

# 3.2 Examples

In this example, PAN\_DISPLAY is used to display 15 consecutive pictures with the 640 x 352 resolution for the dynamic display effect.

Each file stores only pure ARGB1555 data (picture data without attached information).

#### [Reference Codes]

```
#include <stdio.h>
#include <fcntl.h>
#include <sys/ioctl.h>
#include <sys/mman.h>
#include <linux/fb.h>
#include "hifb.h"
#define IMAGE WIDTH
                        640
#define IMAGE HEIGHT
                        352
#define IMAGE SIZE
                        (640*352*2)
#define IMAGE NUM
                        14
                        "./res/%d.bits"
#define IMAGE PATH
static struct fb bitfield g r16 = {10, 5, 0};
static struct fb bitfield g g16 = {5, 5, 0};
static struct fb_bitfield g_b16 = {0, 5, 0};
static struct fb bitfield g al6 = {15, 1, 0};
int main()
    int fd;
    int i;
    struct fb fix screeninfo fix;
```



```
struct fb var screeninfo var;
    unsigned char *pShowScreen;
    unsigned char *pHideScreen;
    HIFB POINT S stPoint = {40, 112};
FILE *fp;
VO_PUB_ATTR_S stPubAttr = {0};
    char image name[128];
    /*0. open VO device 0 */
/* .... initialize the attributes for stPubAttr */
    HI_MPI_VO_SetPubAttr(0, &stPubAttr);
HI MPI VO Enable(0);
    /*1. open Framebuffer device overlay 0*/
    fd = open("/dev/fb0", O RDWR);
    if(fd < 0)
        printf("open fb0 failed!\n");
        return -1;
    }
    /*2. set the screen original position*/
    if (ioctl(fd, FBIOPUT SCREEN ORIGIN HIFB, &stPoint) < 0)
        printf("set screen original show position failed!\n");
        return -1;
    }
    /*3. get the variable screen info*/
    if (ioctl(fd, FBIOGET VSCREENINFO, &var) < 0)</pre>
        printf("Get variable screen info failed!\n");
        close(fd);
        return -1;
    }
    /*4. modify the variable screen info
          the screen size: IMAGE WIDTH*IMAGE HEIGHT
          the virtual screen size: IMAGE WIDTH*(IMAGE HEIGHT*2)
          the pixel format: ARGB1555
    */
    var.xres = var.xres virtual = IMAGE WIDTH;
    var.yres = IMAGE HEIGHT;
    var.yres virtual = IMAGE HEIGHT*2;
   var.transp= g a16;
    var.red = g_r16;
    var.green = g g16;
    var.blue = g b16;
    var.bits per pixel = 16;
    /*5. set the variable screeninfo*/
    if (ioctl(fd, FBIOPUT VSCREENINFO, &var) < 0)</pre>
        printf("Put variable screen info failed!\n");
        close(fd);
        return -1;
    }
    /*6. get the fix screen info*/
    if (ioctl(fd, FBIOGET FSCREENINFO, &fix) < 0)
```



```
printf("Get fix screen info failed!\n");
        close(fd);
        return -1;
    }
    /*7. map the physical video memory for user use*/
   pShowScreen = mmap(NULL, fix.smem len, PROT READ|PROT WRITE,
MAP SHARED, fd, 0);
   pHideScreen = pShowScreen + IMAGE SIZE;
    memset(pShowScreen, 0, IMAGE_SIZE);
    /*8. load the bitmaps from file to hide screen and set pan display
the hide screen*/
    for(i = 0; i < IMAGE NUM; <math>i++)
        sprintf(image name, IMAGE PATH, i);
        fp = fopen(image name, "rb");
        if(NULL == fp)
            printf("Load %s failed!\n", image name);
            close(fd);
            return -1;
        fread(pHideScreen, 1, IMAGE_SIZE, fp);
        fclose(fp);
        usleep(10);
        if(i%2)
            var.yoffset = 0;
            pHideScreen = pShowScreen + IMAGE SIZE;
        }
        else
            var.yoffset = IMAGE HEIGHT;
            pHideScreen = pShowScreen;
        }
        if (ioctl(fd, FBIOPAN DISPLAY, &var) < 0)</pre>
            printf("FBIOPAN DISPLAY failed!\n");
            close(fd);
            return -1;
        }
    }
    printf("Enter to quit!\n");
getchar();
    /*9. close the devices*/
   close(fd);
HI_MPI_VO_Disable(0);
    return 0;
}
```



# 4 Using the Virtual HiFB

# 4.1 Principle

With the video graphics subsystem (VGS), the virtual HiFB take an image as on-screen display (OSD) and overlay it with VO channel 0. This principle is used when the chip does not have the graphics layer logic.

In the Linux operating system, before loading the virtual HiFB driver hifb.ko, you have to load the standard frame buffer driver fb.ko to the kernel by running the command modprobe fb.

# 4.2 Change Description

The virtual HiFB supports the standard mode interfaces of the original HiFB and extended mode interfaces such as alpha, colorkey, and premultiplication.

The standard mode interfaces of the virtual HiFB are the same as those of the original HiFB.

#### **NOTICE**

Currently, only the Hi3516C V300 supports the virtual HiFB.