1

Hi3518PERB VER.B02

Provide by Hisilicon

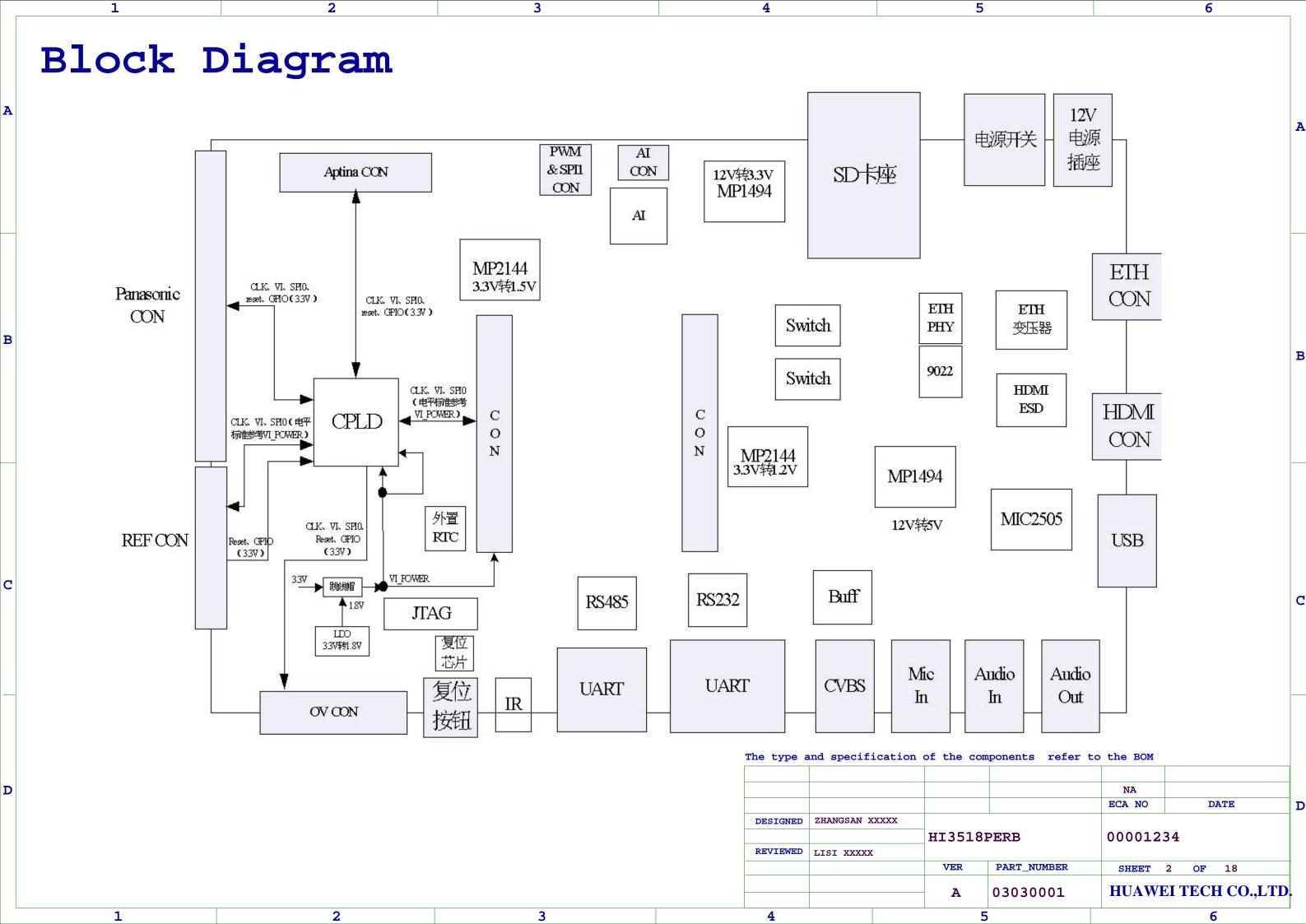
PAGE	1	Cover
PAGE	2	Block Diagram
PAGE	3	Power Tree
PAGE	4-5	DCDC Power
PAGE	6-8	CPLD
PAGE	9	ETH
PAGE	10	ETH & HDMI SWITCH
PAGE	11-12	HDMI PHY & OUTPUT
PAGE	13	UART & JTAG & RESET & RTC
PAGE	14	AUDIO
PAGE	15	USB & SD_CARD & VADC
PAGE	16	AI & AF & IR & Flash_trig
PAGE	17	Connector to Mainboard
PAGE	18	Connector to Sensor board

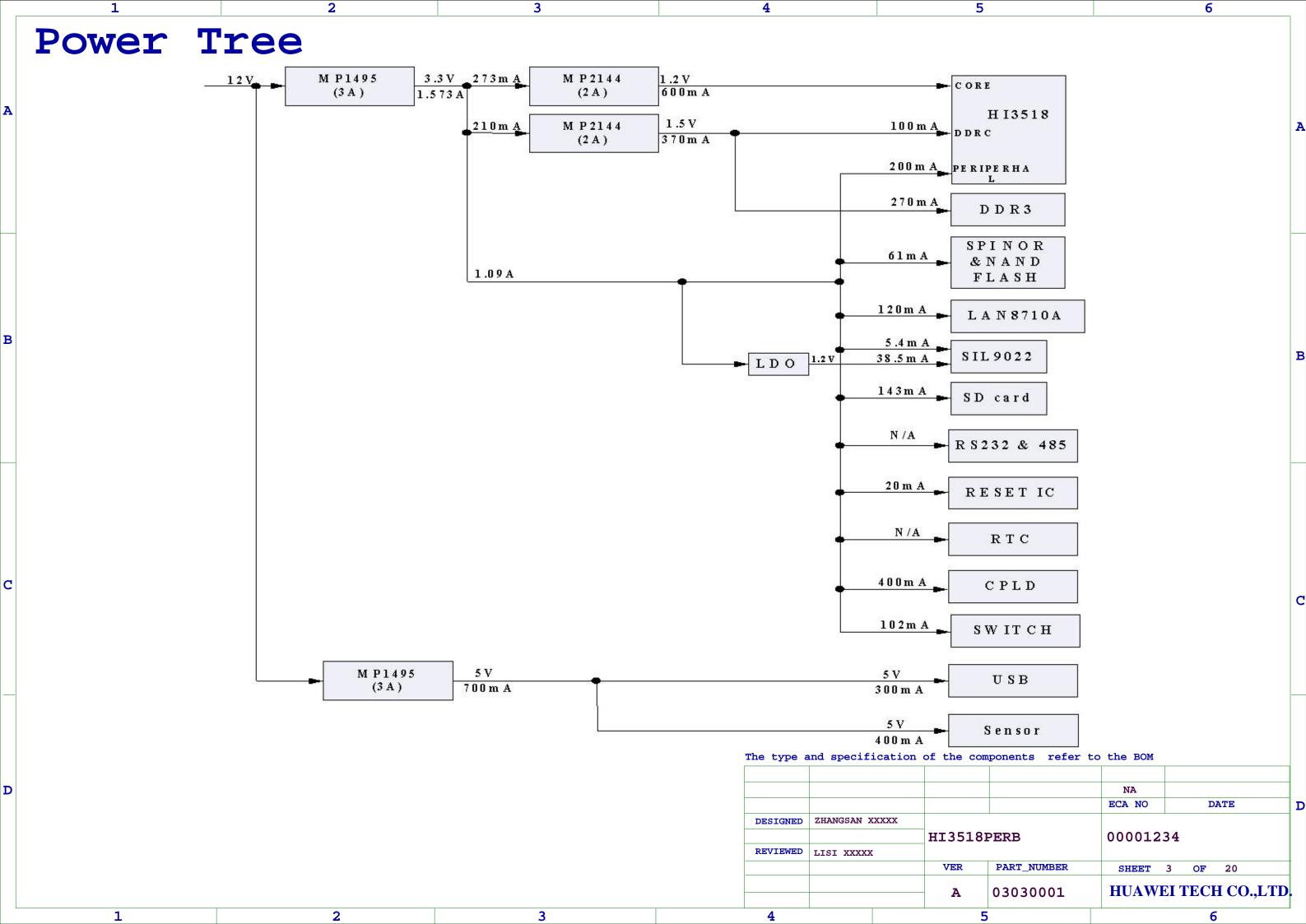
6

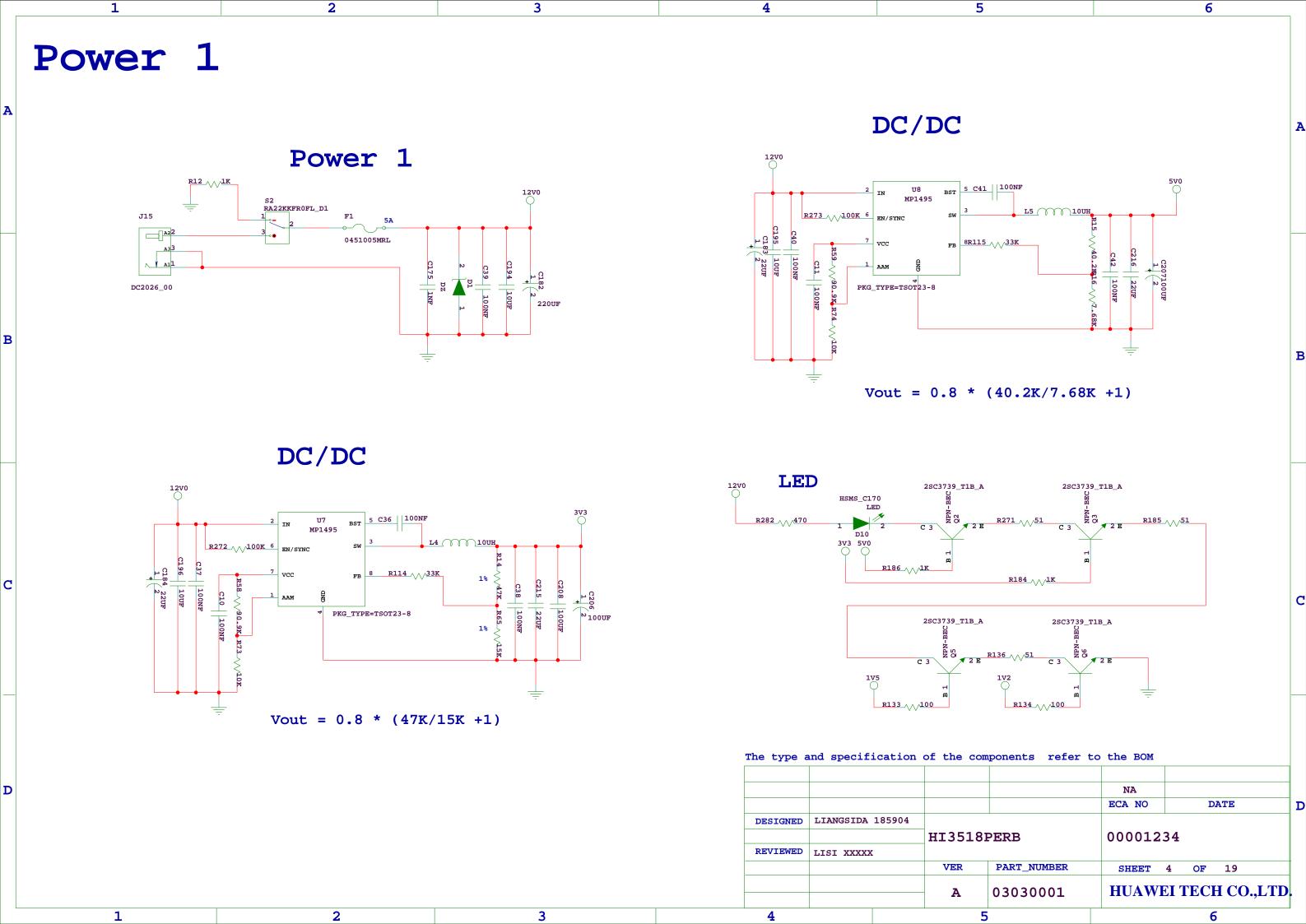
Th	e type	and	specification	οf	the	components	refer	to	the I	BOM
----	--------	-----	---------------	----	-----	------------	-------	----	-------	-----

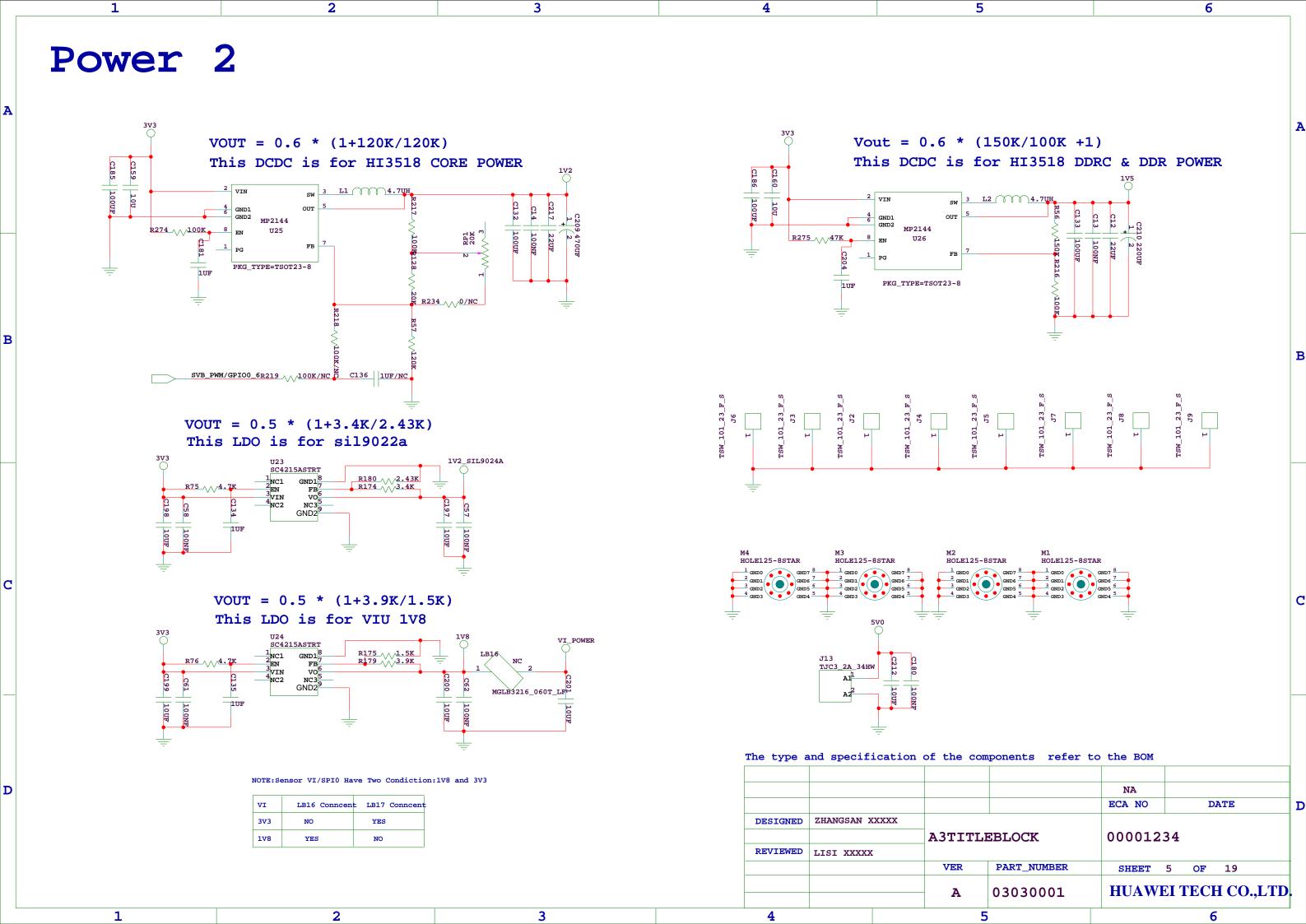
					NA			
					ECA NO		DA!	re
DESIGNED	ZHANGSAN 2	xxxx						
			HI35181	PERB	000012	34		
REVIEWED	LISI XXXXX	ζ						
			VER	PART_NUMBER	SHEET	1	OF	18
			A	03030001	HIIAW	EI T	ECH	CO.,LTD
					1101111			CO.,LTD
			_				_	

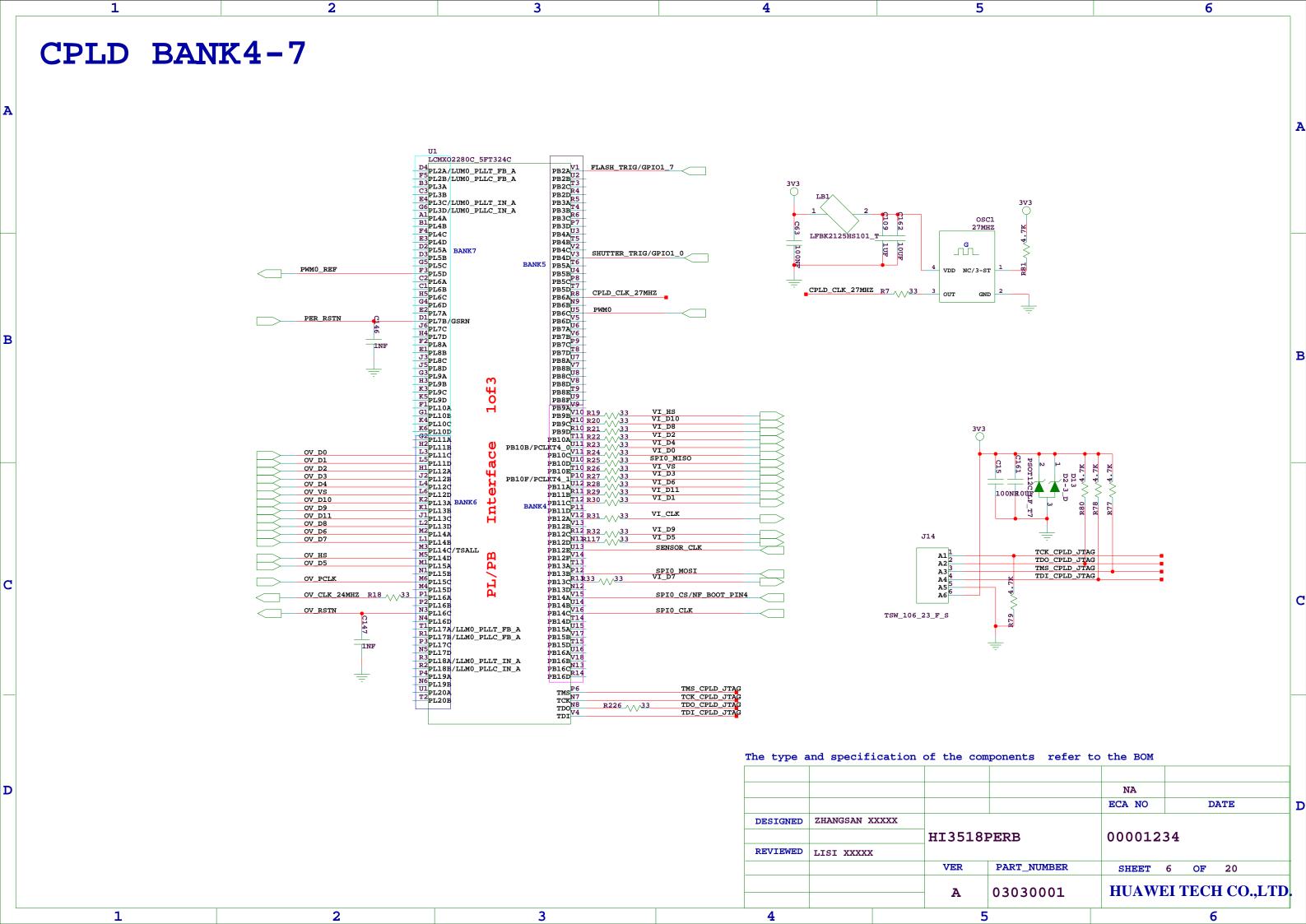
2 3 4 5

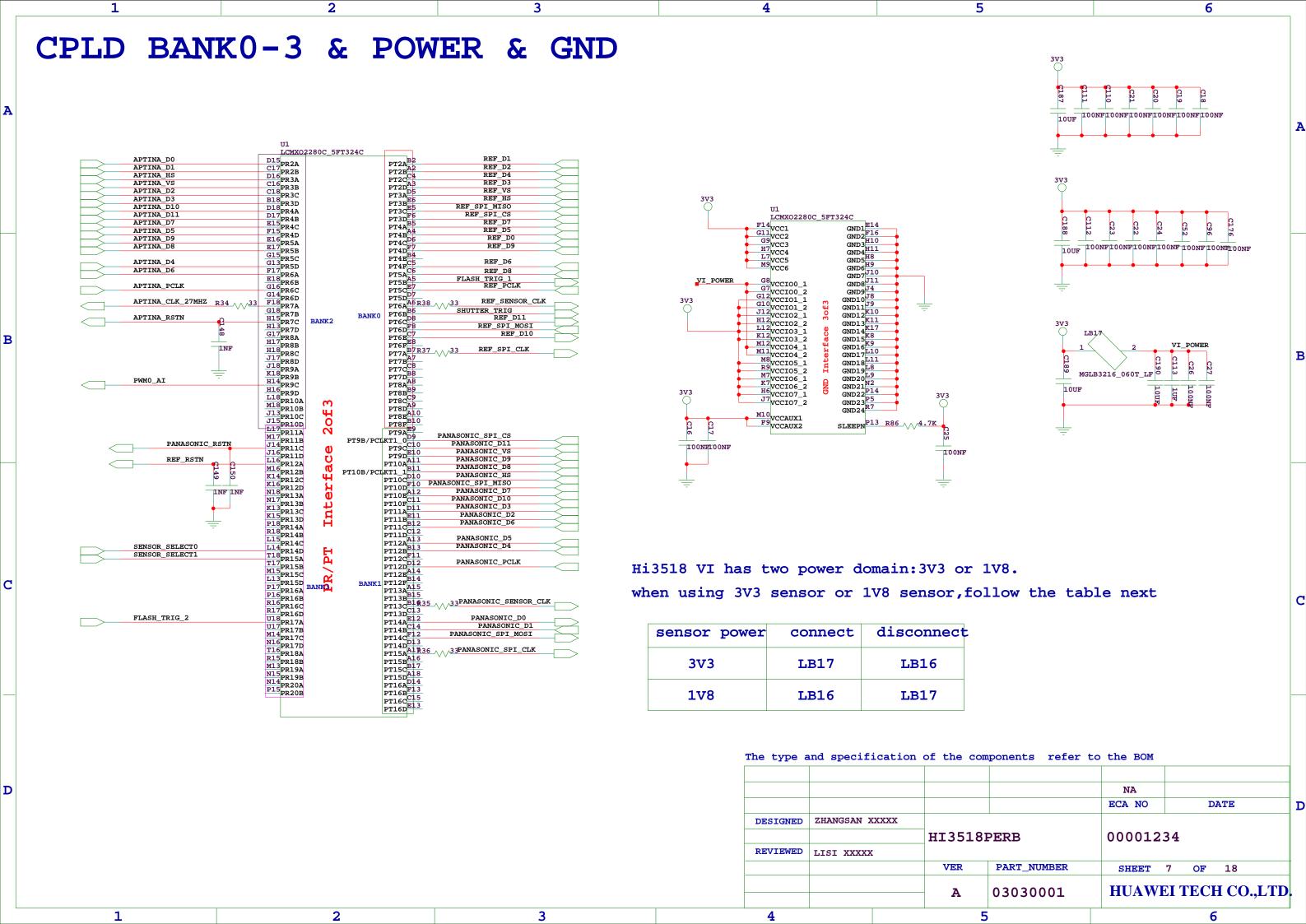




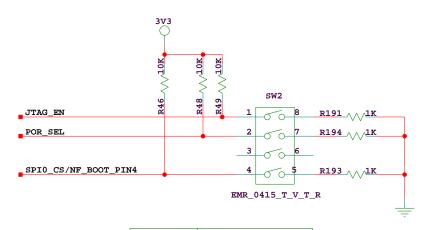








HI3518 CONFIGURE & ADC Check



	V10K V10K V10K	
_ SENSOR_SELECTO	R82 \ R83 \ R85 \ R85 \	SW1
SENSOR_SELECT1		2 0 7
VA_S0		3 6
VA_S1		4 0 5
		EMR_0415_T_V_T_R

	303 ~~10K ~~10K	
	R45 R44 R43 R50	SW3
NF_BOOT_PIN0	R4 R4 R5	1 8 R190 VV1K
NF_BOOT_PIN1		2 7 R187 \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\
NF_BOOT_PIN2		3 6 R189 VIK
NF_BOOT_PIN3		4 5 R188 \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
-	•	EMR_0415_T_V_T_R

5

6

SW2 P1	JTAG_EN
0	Disable JTAG
1	Enable JTAG
SW2 P2	POR_SEL
0	Enable POR
1	Disable POR

SW1[2:1]	SENSOR
00	MN34031
01	AR0130
10	OV9715
11	REF

3v3
R47
NOTE:R192 and R47 should be placed near U3
BOOT_SEL boot mode

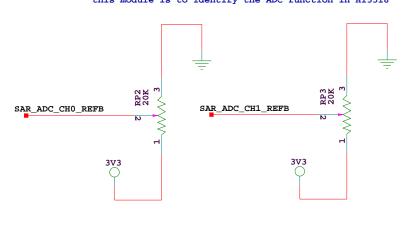
SW1[4:3]	ETH/VOU1120
00	/
01	ETH
10	VOU1120
11	/

sw1[4:3]	ETH/VOU1120
00	/
01	ETH
10	VOU1120
11	/

NF_BOOT_PIN[4:0]	Page size	ECC	Block size
00001	2k Bytes	1 bit	64 pages
00011	2k Bytes	4 bit	64 pages
00101	2k Bytes	24 bit	64 pages
00110	2k Bytes	1 bit	64 pages
01000	4k Bytes	4 bit	128 pages
01001	4k Bytes	4 bit	64 pages
01010	2k Bytes	4 bit	64 pages
01011	4k Bytes	24 bit	128 pages
01101	8k Bytes	2 4 bit	128 pages
10000	8k Bytes	24 bit	64 pages
10001	4k Bytes	24 bit	64 pages
10011	4k Bytes	1 bit	64 pages
10101	2k Bytes	4 bit	128 pages
11001	2k Bytes	2 4 bit	128 pages

this module is to identify the ADC function in ${\tt HI3518}$

SPI FLASH NAND FLASH



The type and specification of the components refer to	\ the BOM	

ine eype e	pecificación		ponencs refer to	CHE DOM			
				NA			
				ECA NO		DA	TE
DESIGNED	ZHANGSAN XXXXX	HI3518PERB					
				00001234			
REVIEWED	LISI XXXXX						
		VER	PART_NUMBER	SHEET	8	OF	18
		A	03030001	HUAWI	EI 1	TECH	CO.,LTD
				_== 312 , , =			= = = = = = = = = = = = = = = = = = = =

