

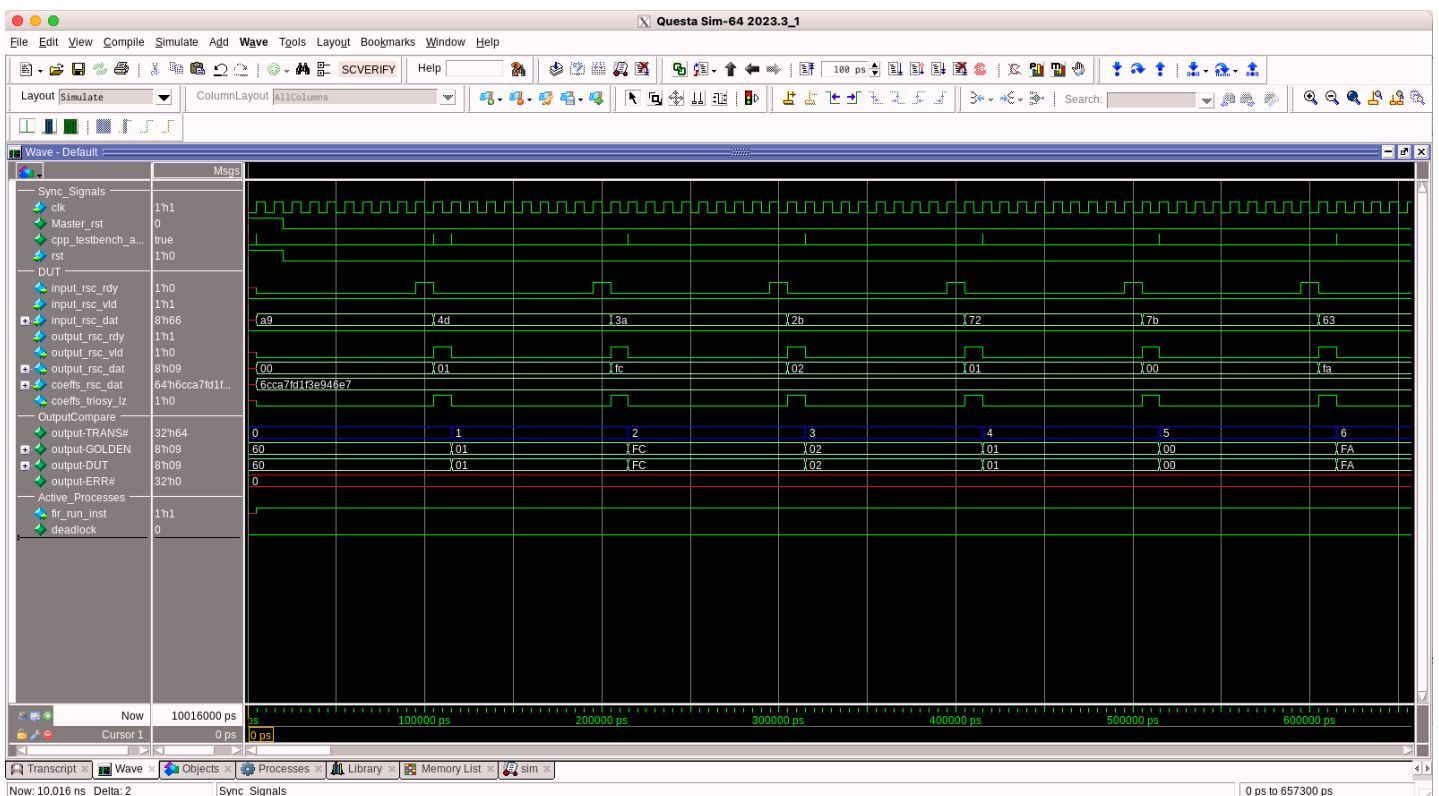
Advanced SoC Design Report

Student ID: 311511022

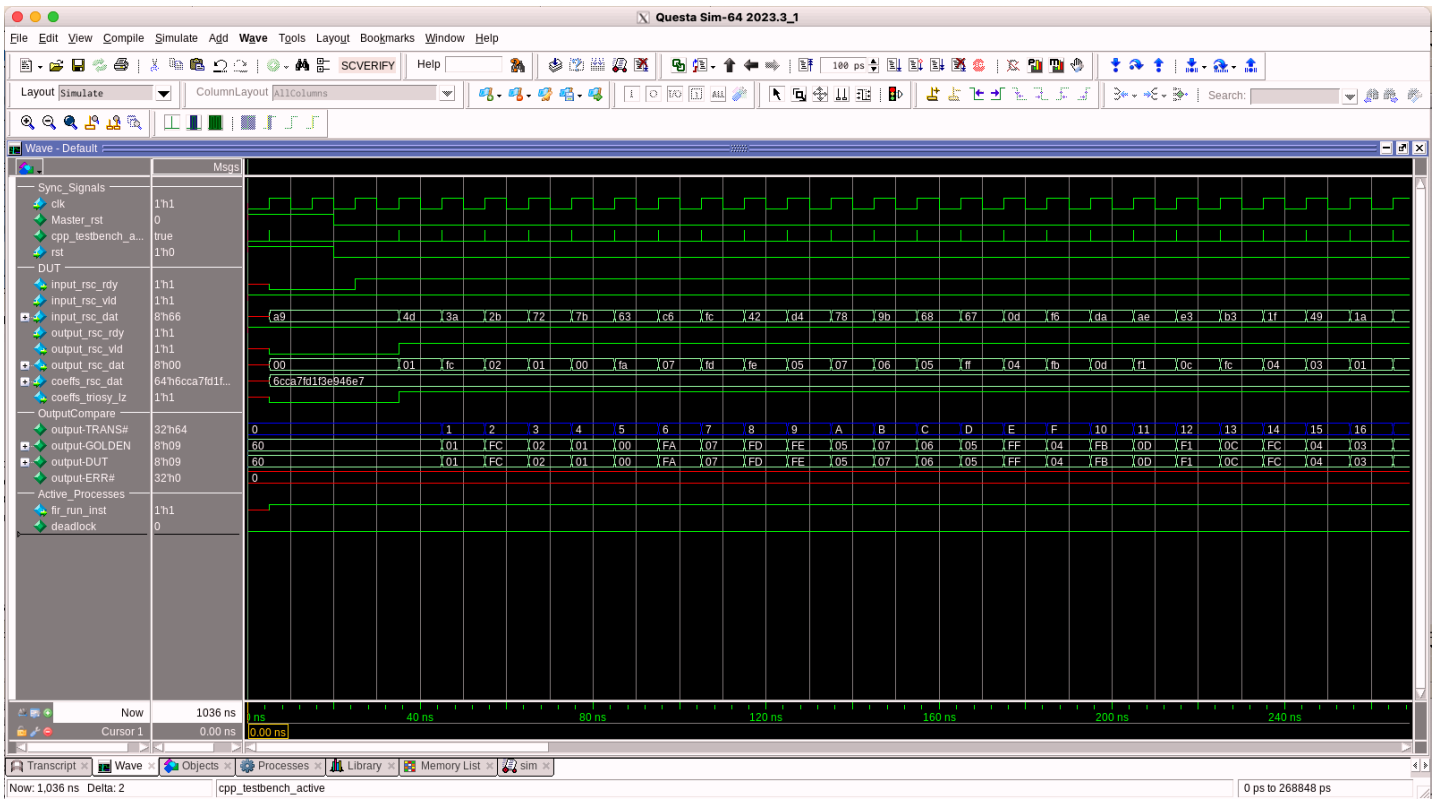
Lab #2-1 Catapult-FIR

1. Walkthrough loops

- w/o pipeline and unroll

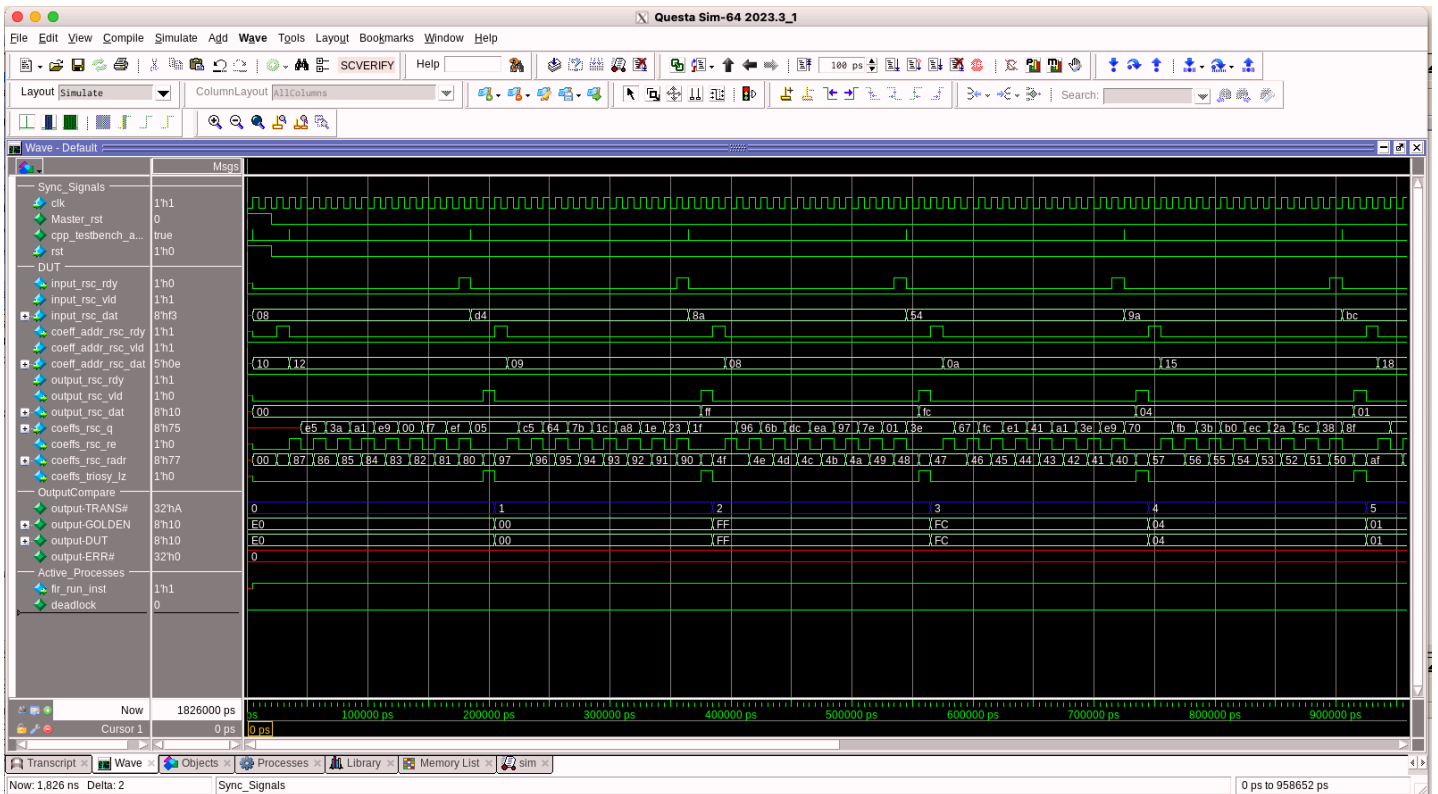


- w/ pipeline and unroll

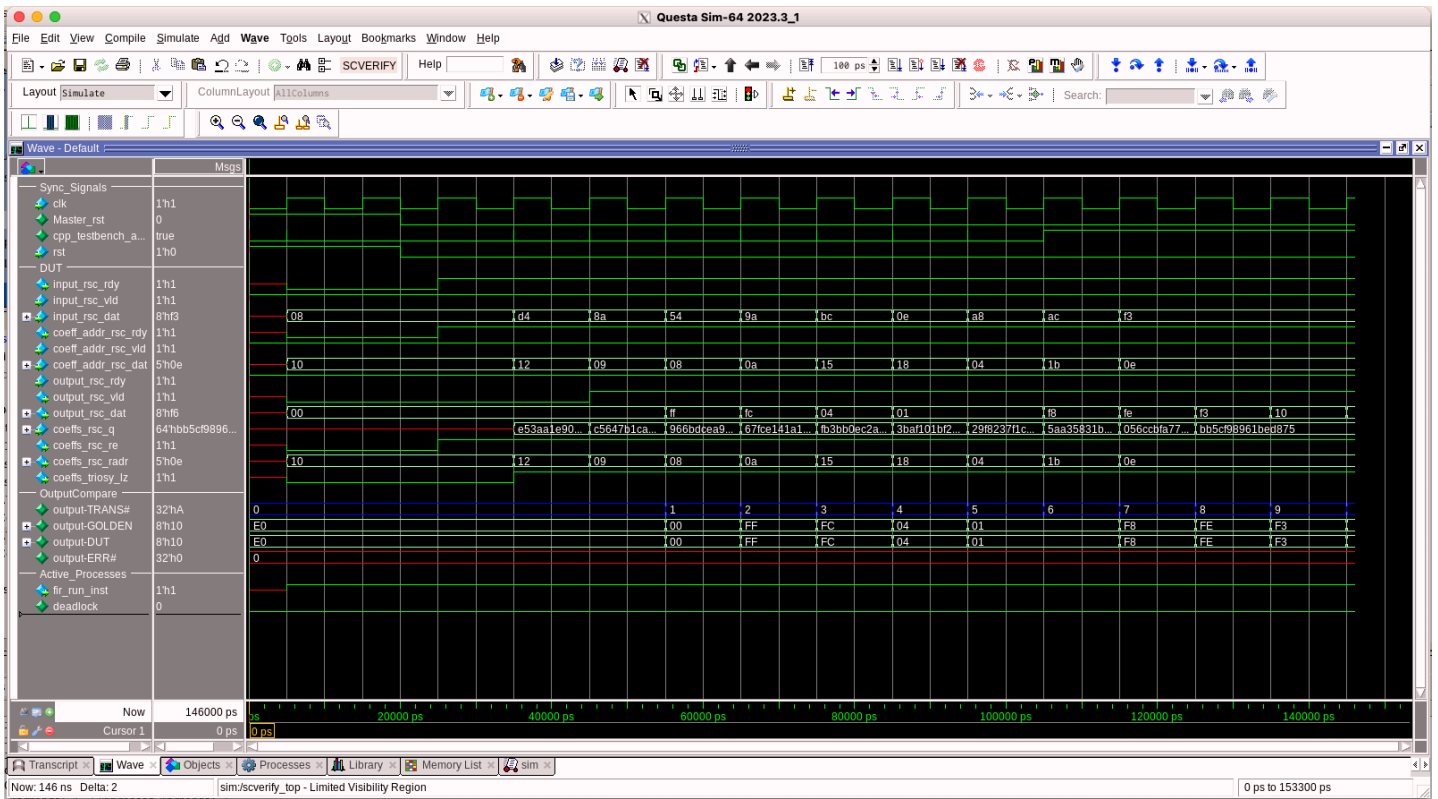


2. Memory interface

- 256x8 coeff SRAM



- 32x64 coeff SRAM



3. Multiple blocks

