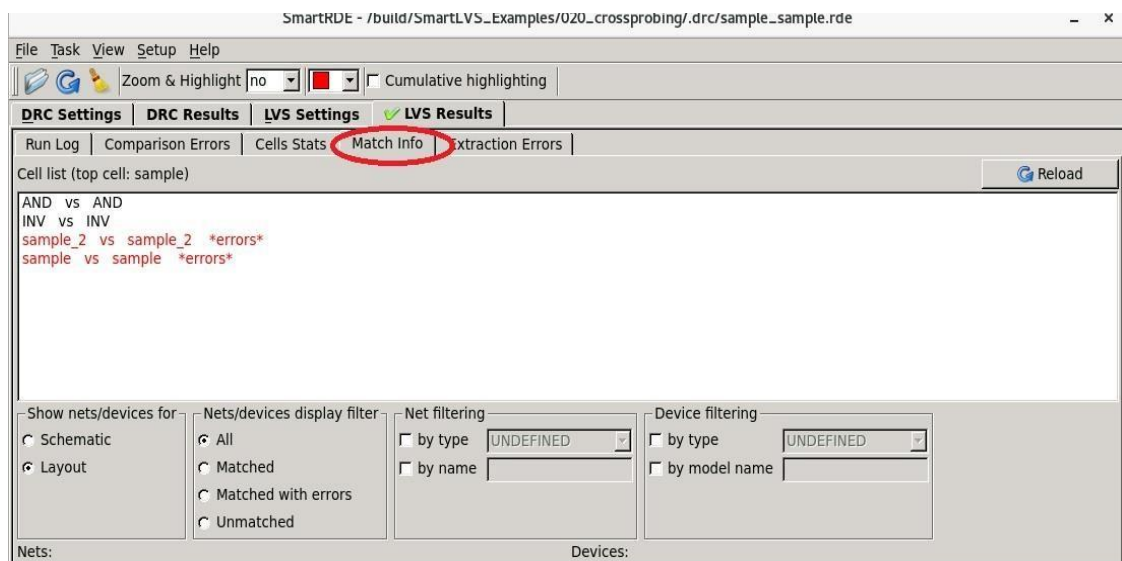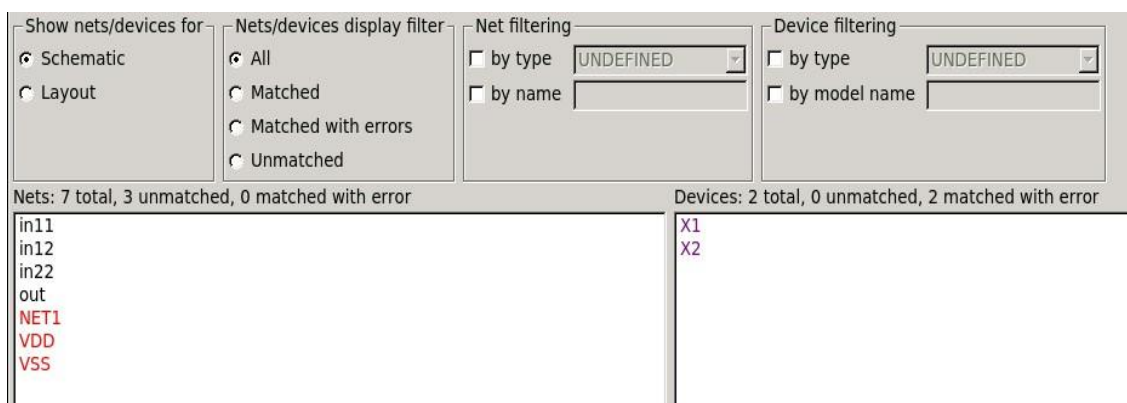**Crossprobing nets and devices using SmartRDE and Gateway**

Post completion of a SmartLVS run, mismatched nets and devices between schematic and layout can be crossprobed within Gateway. Below are detailed some of the steps using the *04_lvs_crossprobing* example within the *examples/Analog/Feature_exlib* folder.
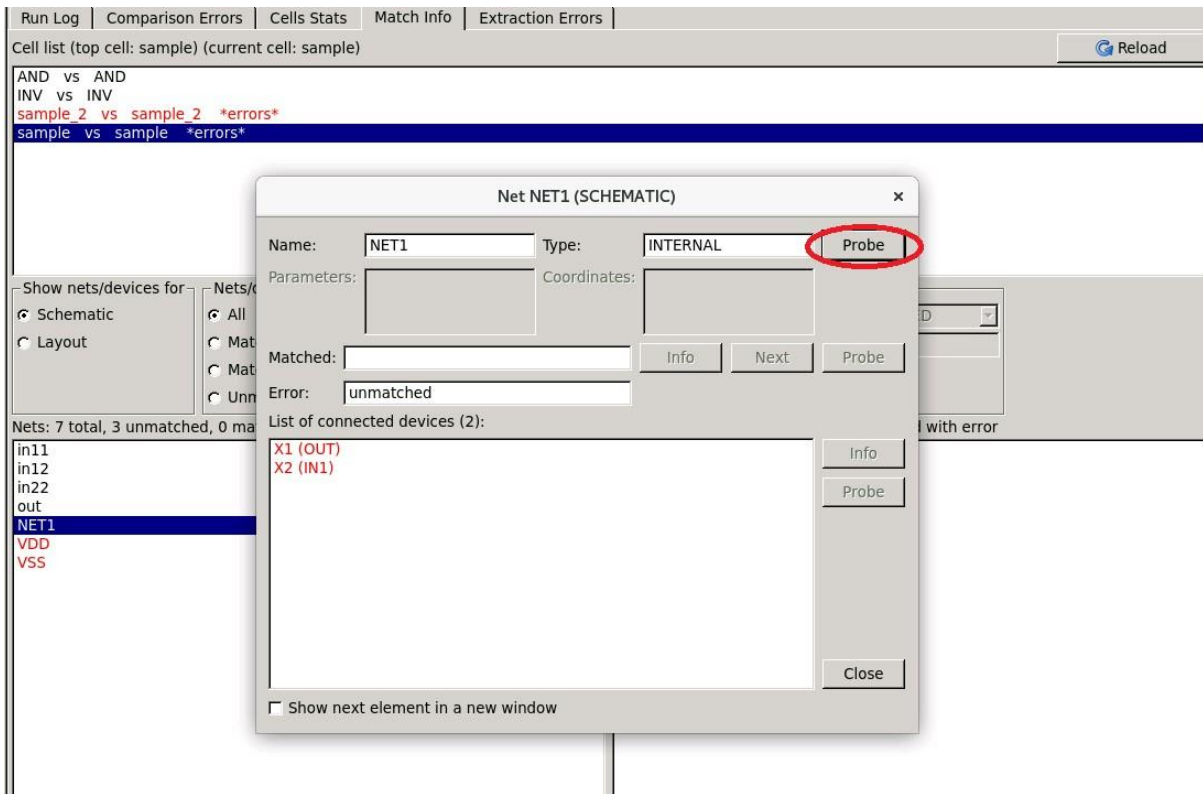
1. Run SmartLVS using the 004_lvs_crossprobe.eld and rule file as provided in the examples folder. Once LVS is completed, within the SmartRDE window, click on tabs **LVS Results** and **Match Info**. In this view, the top panel shows a list of all cells within the design. Cells highlighted in red are cells with mismatches between layout and schematic after the last LVS run.
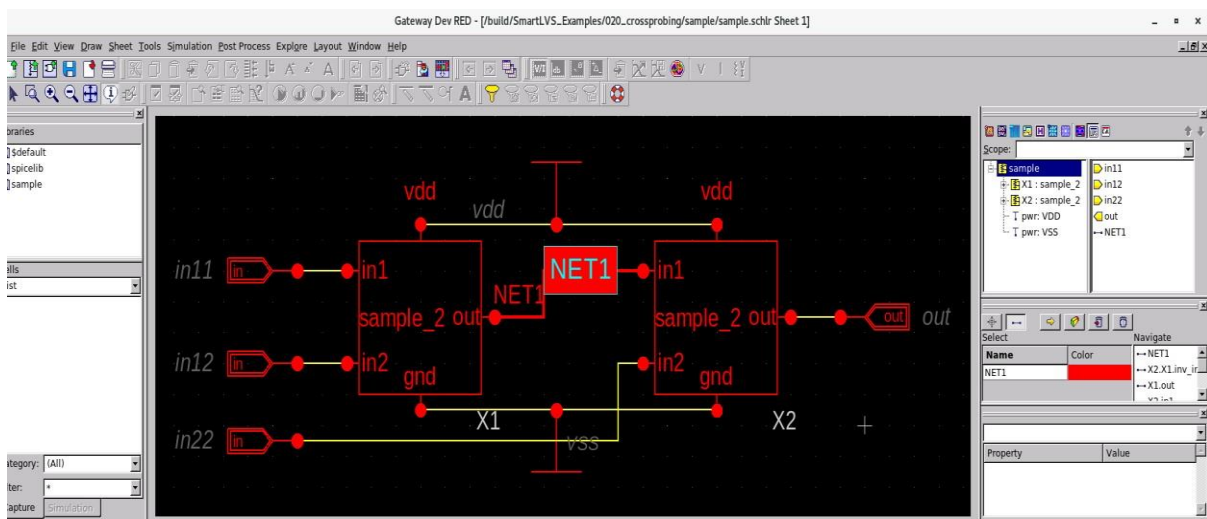

2. Double-click on the cell *004_lvs_crossprobe *errors**. The nets and devices of the cell are listed in the bottom two panels (as shown below). Unmatched nets are listed in red. The names of the nets can be toggled between the names as in schematic or layout.

3. Double-click on one of the unmatched nets, say NET1. This will open up a new dialog which allows to probe the net/device.

4. Click on the **Probe** button as shown in the above figure. This will probe the NET1 onto Gateway's schematic view. As can be seen in the schematic below, NET1 is highlighted in red as the net in 004_lvs_crossprobe.schlr

Note: If a net is not highlighted in Gateway after cross-probing, enable **View ❷ Info** to highlight the net.

Similarly, devices can also be crossprobed from SmartRDE using the above steps and clicking on instance names (bottom right panel) in Step 2 to highlight them in Gateway's schematic view.