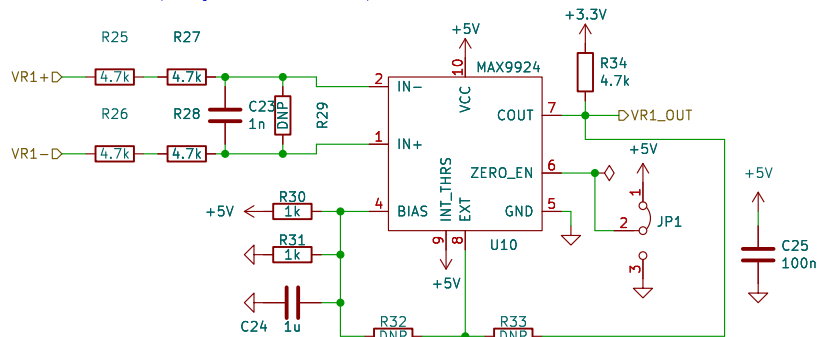


Yes, we know that the polarity is flipped going in to the VR interface chips.
While originally on accident, it's no big deal since they invert anyway:
When VR+ transitions to a higher voltage than VR-, VR_OUT will output a rising edge.
See issue <https://github.com/mck1117/proteus/issues/57> for more detail



Optional setting in Mode C for no adaptive threshold timeout – See MAX9924 Datasheet
Components as specified put chip in mode A1. Flip jumper and install R1219+R2020 for mode C.

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Sheet: /VR Conditioner/
File: VR Conditioner.kicad_sch

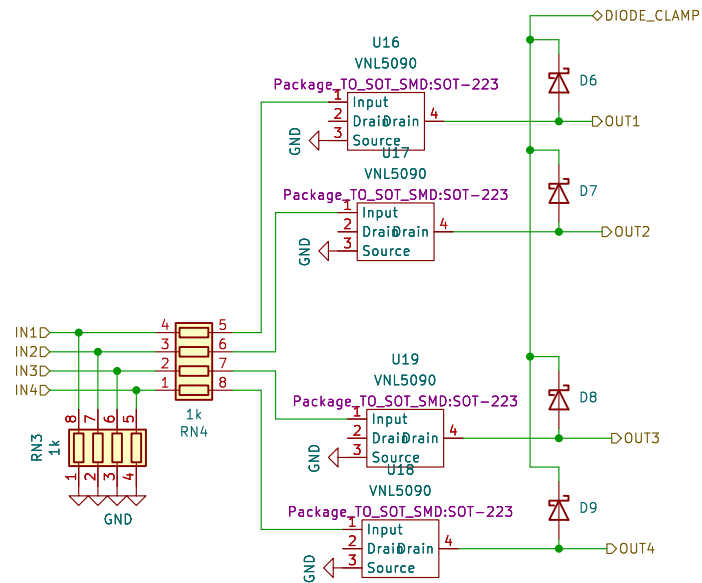
Title: Polygonus Universal 88p

Size: A4 Date: 2023-11-12

KiCad E.D.A. 9.0.3

Rev: v1.0

Id: 4/10



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Sheet: /Lowside Adder1/
File: Lowside Adder.kicad_sch

Title: Polygonus Universal 88p

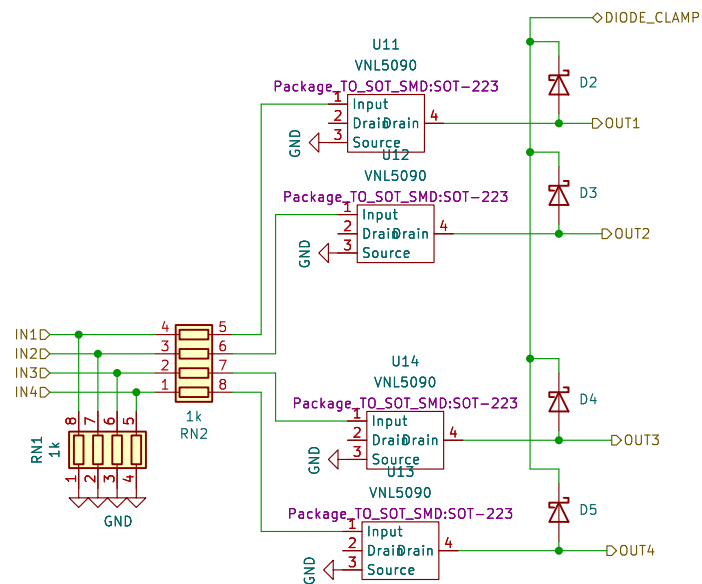
Size: A4

Date: 2023-11-12

Rev: v1.0

KiCad E.D.A. 9.0.3

Id: 8/10



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Sheet: /Lowside Adder/
File: Lowside Adder.kicad_sch

Title: Polygonus Universal 88p

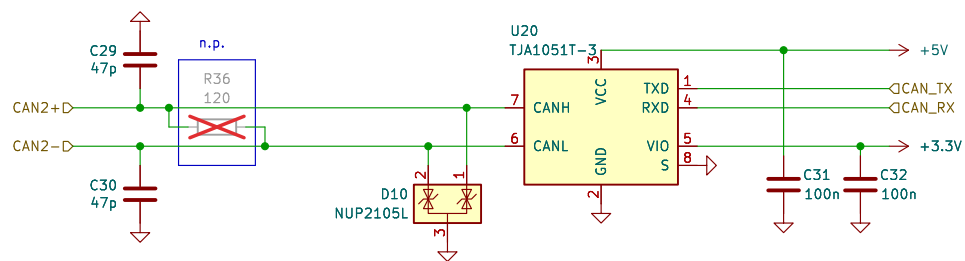
Size: A4

Date: 2023-11-12

Rev: v1.0

KiCad E.D.A. 9.0.3

Id: 6/10



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Sheet: /WBO-CAN-TRANS/
File: wbo-can-trans.kicad_sch

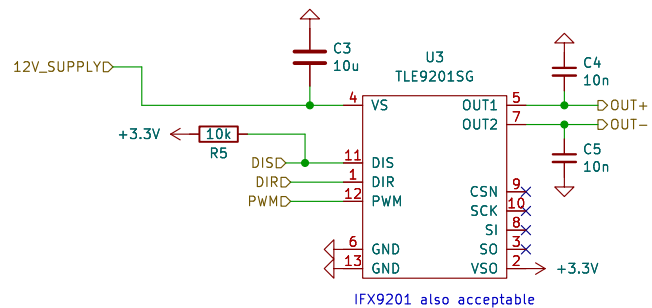
Title: Polygonus Universal 88p

Size: A4 Date: 2023-11-12

KiCad E.D.A. 9.0.3

Rev: v1.0

Id: 9/10



IFX9201 also acceptable

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Sheet: /etb-2/

File: etb-9201.kicad_sch

Title: Polygonus Universal 88p

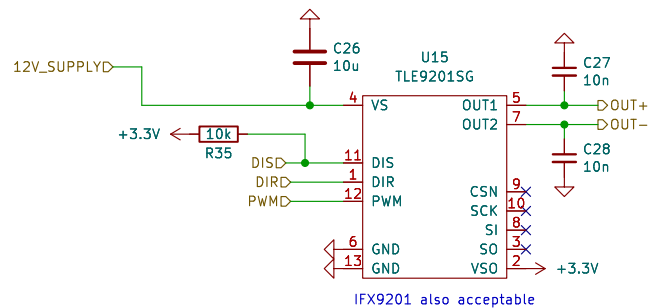
Size: A4

Date: 2023-11-12

Rev: v1.0

KiCad E.D.A. 9.0.3

Id: 2/10



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Sheet: /etb-1/

File: etb-9201.kicad_sch

Title: Polygonus Universal 88p

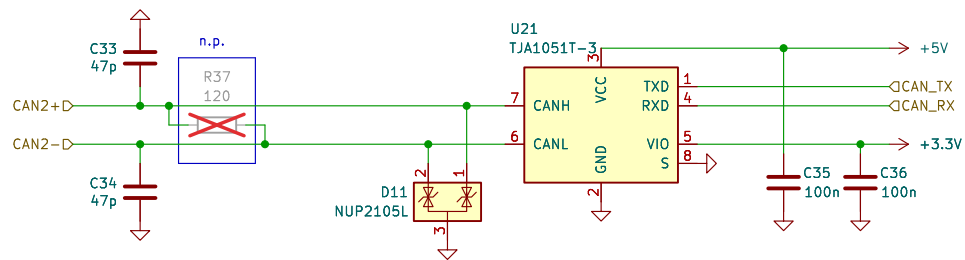
Size: A4

Date: 2023-11-12

Rev: v1.0

KiCad E.D.A. 9.0.3

Id: 7/10



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Sheet: /WBO-CAN-TRANS1/

File: wbo-can-trans.kicad_sch

Title: Polygonus Universal 88p

Size: A4

Date: 2023-11-12

Rev: v1.0

KiCad E.D.A. 9.0.3

Id: 10/10

