

Efficient elliptic curve arithmetic in zero-knowledge circuits

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Joint work with

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(Alpen Labs)

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ZKNOX team



Nicolas Bacca

20⁺ years experience (10⁺y web3)
Security and hardware specialist
Prev. Ledger cofounder/CTO



Renaud Dubois

20⁺ years experience (3⁺y web3)
Cryptographer
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Expertise and innovation to every challenge on the whole security chain:

- ▶ user end
(secure enclaves, hardware wallets),
- ▶ back end
(TEE, HSMs),
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(smart contracts).



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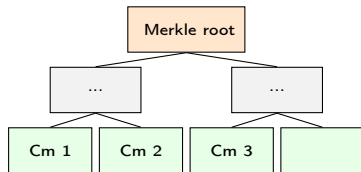
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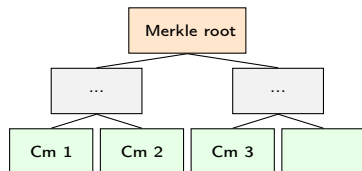
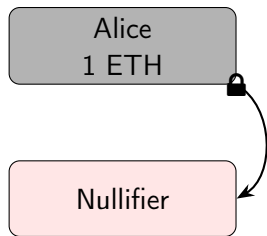
<https://zknox.eth.limo/>

<https://github.com/zknoxhq/>

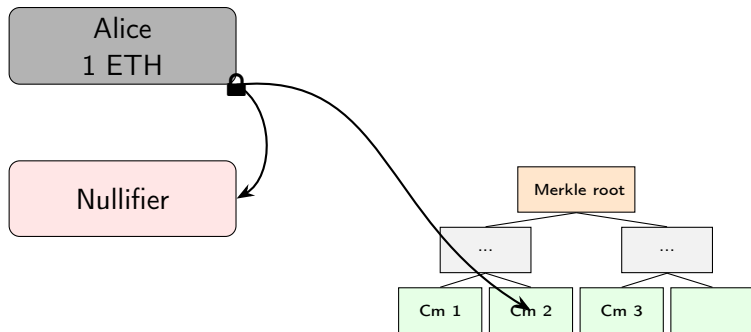
Introduction: privacy in Ethereum



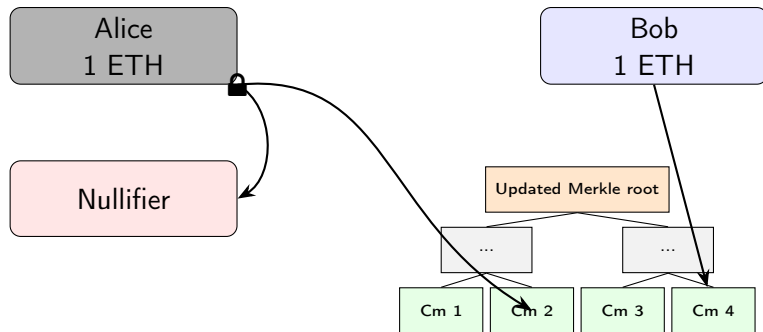
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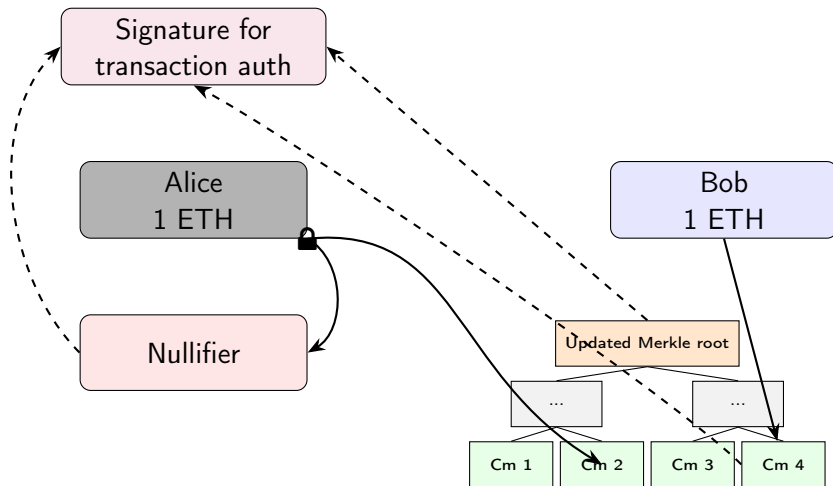
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A zero-knowledge proof that:

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- ▶ Proves the ownership of the note,
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The signature curve is chosen so that the authentication circuit is *compatible* with the nullifier and ownership circuits.

...but what is a circuit, exactly?

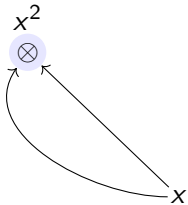
An example of circuit

Proving the knowledge of a solution ($x = 3$) of $x^3 + x + 5 = 35$:

x

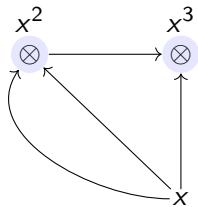
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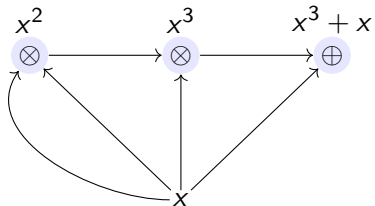
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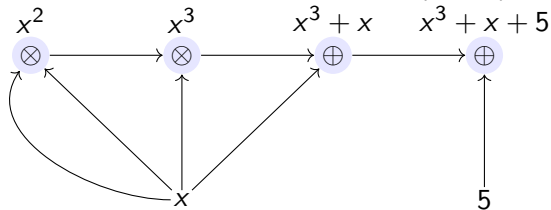
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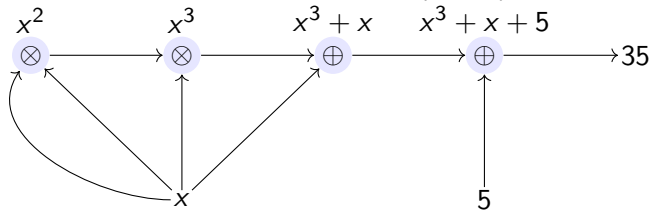
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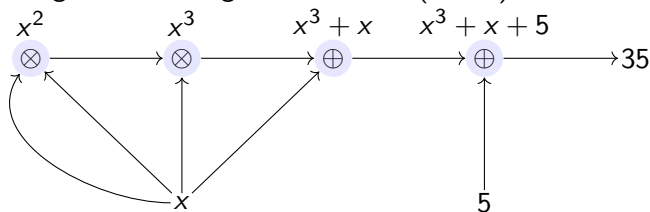
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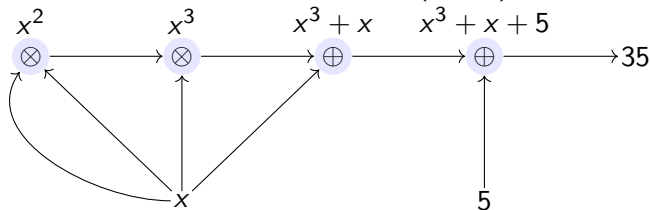
Proving the knowledge of a solution ($x = 3$) of $x^3 + x + 5 = 35$:



- ▶ (almost) Only additions and multiplications,
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 - ▶ Circom: BN254 scalar field,
 - ▶ Halo2: Pallas (or Vesta) scalar field,
 - ▶ Etc.

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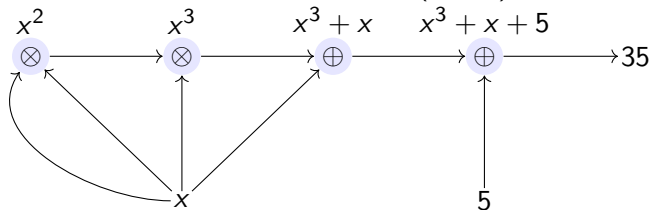


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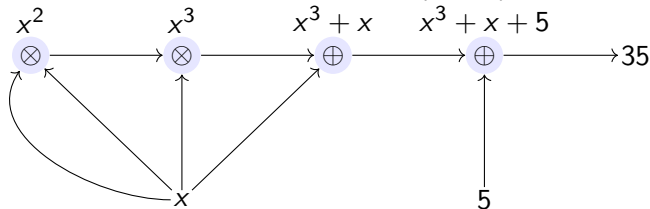
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The **authentication circuit** must be modulo q .

Select a curve whose base field is modulo q .

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Select a curve whose base field is modulo q .

(Railgun uses Circom; BabyJubjub defined over BN254 scalar field)

Elliptic curve in circuits

Signature verification: Elliptic curve scalar multiplications.

EdDSA in Wikipedia: $2^c SB \stackrel{?}{=} 2^c R + 2^c H(R||A||M)A$.

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1. How to compute $[10001101_2]P$?

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$$Q = [2^4]P + P$$

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In practice:

- ▶ Off-chain: very efficient (a few milliseconds),
- ▶ On-chain: non-native scalar multiplication ≈ 4 seconds.

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This work: reduce scalars using circuit hints:

- ▶ Smaller circuits for signature verification,
- ▶ Improved proof computation for circuits of elliptic curves.

Hinted scalar multiplication

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$\{x - kz = 0 \bmod r\}$ is a lattice of dimension 2:

$$\begin{pmatrix} r & 0 \\ k & 1 \end{pmatrix} = \begin{pmatrix} \square\square\square\square\square\square & 0 \\ \square\square\square\square\square\square & 1 \end{pmatrix}$$

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Apply lattice reduction (like LLL) to find a short vectors.

Expected size \sqrt{r} .

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$$[k]P = Q \iff [x]P - [z]Q = 0$$

- ▶ $[k]P = Q$: scalar of size 256,
- ▶ $[x]P - [z]Q = 0$: scalars of size 128. ✓

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$\{x_1 - k_1z = 0 \bmod r \text{ and } x_2 - k_2z = 0 \bmod r\}$ form a lattice of dimension 3:

$$\begin{pmatrix} r & 0 & 0 \\ 0 & r & 0 \\ k_1 & k_2 & 1 \end{pmatrix} = \begin{pmatrix} \square\square\square\square\square\square & 0 & 0 \\ 0 & \square\square\square\square\square\square & 0 \\ \square\square\square\square\square\square & \square\square\square\square\square\square & 1 \end{pmatrix}$$

Hinted double scalar multiplication

Consider the equation $[k_1]P_1 + [k_2]P_2 = Q$.

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Triple scalar multiplication with scalars of 171 bits. ✓

GLV hinted scalar multiplication

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$[k]P = [k_1]P + [k_2]\psi(P)$ where $\psi(P)$ is easy to compute, and k_1, k_2 halved size.

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$$[k]P = Q$$

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$$[x_1]P_1 + [y_1]\psi(P_1) + [x_2]P_2 + [y_2]\psi(P_2) \\ - [z]Q - [t]\psi(Q) = 0$$

Sextuple 86-bit scalar multiplication.

Practical results

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Circuit	R1CS			SCS		
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Non-native (P256)	157 685	78 940	50%	612 759	294 128	52%
Non-native GLV (secP256k1)	78 940	60 089	24%	385 461	223 188	42%
Native (Jubjub)	3 314	2 401	28%	5 863	4 549	22%
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- ▶ The scalar decomposition is not optimal yet (xgcd vs lll),
- ▶ The cost is implementation-dependent,
- ▶ Out-of-circuit considerations are also important,
- ▶ Double scalar multiplication not implemented yet.

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Thank you for your attention.