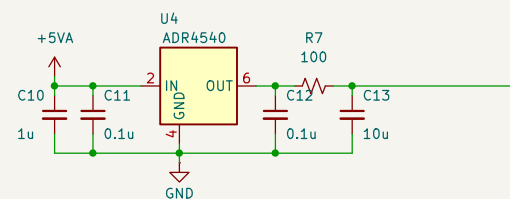


No Bypass Capacitors  
The data sheet mentions on page 28:  
Note that the VDD\_5V and the VDD\_1.8V supplies have internal 1  $\mu$ F bypass capacitors inside the package, while VIO has an internal 0.2  $\mu$ F bypass capacitor.

Reference:  
REF pin is an optional Unbuffered Reference Input. Drive REF with 4.096 V to 5 V (referred to ground). This pin has a 2  $\mu$ F bypass capacitor inside the package. When using the internal reference buffer, do not connect REF.



Buffer amplifier:  
What buffer amplifier do we want to use  
and do we want to use the REF or REFIN  
inputs on the ADC?



Sheet: /ADC/ File: ADC.kicad_sch		
Title: <b>3-channel ADC</b>		
Size: USLedger	Date: 2024-10-18	Rev:
KiCad E.D.A. 8.0.3		Id: 2/7