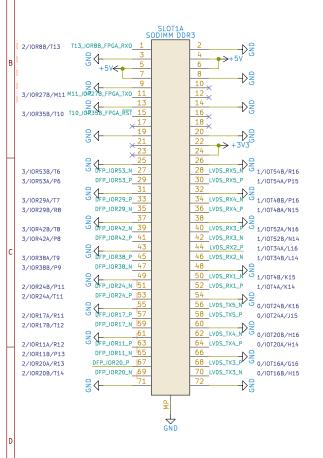
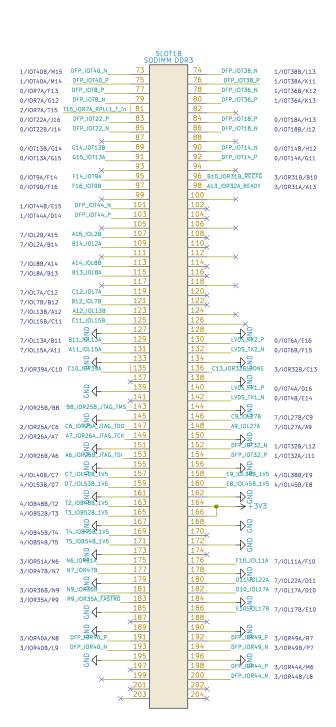
Tang Nano 20K Dock

Version His	Date	Change Note
7101	2022/02/21	First edition





2/I0R27B/M11 2/I0R8B/T13 2/I0R26A/A7 2/I0R25A/C6 2/I0R25B/B8	M11_IOR27B_FPGA_TXO T13_IOR8B_FPGA_RXO A7_IOR26A_TAG_TCK C6_IOR25A_TAG_TDO A6_IOR26B_TAG_TDI B8_IOR25B_TAG_TMS	P001_BI702 DBL702_UART1_RX DBL702_UART1_TX DBL702_JTAG_TCK DBL702_JTAG_TD0 DBL702_JTAG_TD1 DBL702_JTAG_TMS
0/IOT14A/G11 0/IOT14B/H12 0/IOT18B/H13 2/IOR20B/T14 2/IOR20A/R13 2/IOR11B/H13 2/IOR11A/R12 7/IOL11A/F10 1/IOT36A/K13 1/IOT36B/K12 1/IOT38A/K11 2/IOR7A/T15		File: P001_Bl702.kicad_sch P002_USB PHY USB_DATA0 USB_DATA1 USB_DATA2 USB_DATA3 USB_DATA4 USB_DATA6 USB_DATA6 USB_DATA7 USB_DATA7 USB_DATA7 USB_DATA7 USB_DATA7 USB_DATA7 USB_DATA7 USB_DATA7 USB_CLKOUT
		File: P002_USB PHY.kicad_sch

		003_Audio
/IOT48A/N15 /IOT54A/P15	LVDS_RX4_P LVDS_RX5_P LVDS_RX4_N	——————————————————————————————————————
/IOT48B/P16 /IOT54B/R16	LVDS_RX5_N	—— DHP_WS —— DPA_EN
		File: P003_Audio.kicad_sch

		P006_LCD
3/IOR40B/L9	DFP_IOR40_N	->RGB_LCD_R0
3/IOR40A/N8	DFP_IOR40_P	->RGB_LCD_R1
3/IOR36B/N9	N9_IOR36B	->RGB_LCD_R2
3/IOR47B/N7	N7_IOR47B	->RGB_LCD_R3
3/IOR51A/N6	N6_IOR51A	->RGB_LCD_R4
7/IOL22A/D11	D11_IOL22A	->RGB_LCD_G0
7/IOL15A/A11	A11_IOL15A	->RGB_LCD_G1
7/IOL13A/B11	B11_I0L13A	->RGB_LCD_G1
3/IOR49B/P7	DFP_IOR49_N	->RGB_LCD_G3
3/IOR49A/R7	DFP_IOR49_P	->RGB_LCD_G4
7/IOL17A/D10	D10_IOL17A	-DRGB_LCD_G5
7/IOL7B/B12	B12_IOL7B	->RGB_LCD_B0
7/IOL7A/C12	C12_IOL7A	-DRGB_LCD_B1
7/IOL8A/B13	B13_IOL8A	->RGB_LCD_B2
7/IOL8B/A14	A14_IOL8B	->RGB_LCD_B3
7/IOL2A/B14	B14_IOL2A	->RGB_LCD_B4
3/IOR35A/R9	R9_IOR35A_FASTRD	->RGB_LCD_CLK
7/IOL2B/A15	A15_IOL2B	->RGB_LCD_HSYNC
1/IOT44A/D14	DFP_IOT44_P	->RGB_LCD_VSYNC
1/IOT44B/E15	DFP_IOT44_N	->RGB_LCD_DE
7/IOL17B/E10	E10_I0L17B	->RGB_LCD_BL
0/IOT9A/F14	F14_IOT9A	->TP SDA
0/IOT9B/F16	F16_I0T9B	OTP_SCK
7/IOL15B/C11	C11_I0L15B	CITP INT
7/IOL13B/A12	A12_I0L13B	-DTP_RST
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		008_Carmera
2/IOR17B/T12	DFP_IOR17_N	DVP_D0
2/IOR24A/T11	DFP_IOR24_P	DVP_D1
2/IOR24B/P11	DFP_IOR24_N	DVP_D2
2/IOR17A/R11	DFP_IOR17_P	DVP_D3
1/IOT40B/M15	DFP_IOT40_N	DVP D4
1/IOT40A/M14	DFP_IOT40_P	DVP_D5
0/IOT22A/J16	DFP_IOT22_P	DVP_D6
0/IOT22B/J14	DFP_IOT22_N	DVP_D7
		F
0/IOT9A/F14	F14_IOT9A	DVP SCL
0/IOT9B/F16	F16_I0T9B	→DVP_SDA
0/IOR7A/F13	DFP_IOT8_P	—kjDVP_PCLK
0/IOR7A/G12	DFP_IOT8_N	DVP_XCLK
1/IOT38B/L13	DFP_IOT38_N	DVP_RST
0/IOT13A/G15	G15_IOT13A	TDVP VSYNC
3/IOR39A/C10	C10_IOR39A	DVP_PWDN
0/IOT13B/G14	G14_IOT13B	— KIDVP_HSYNC
		File: P008_Carmera.kica

		P004_HDMI
0/IOT20A/H14 0/IOT20B/H16 0/IOT24A/J15 0/IOT24B/K16 1/IOT30A/K14 1/IOT30B/K15 0/IOT16B/G16 0/IOT16B/H15	LVDS_TX4_P LVDS_TX4_N LVDS_TX5_P LVDS_TX5_N LVDS_RX1_P LVDS_RX1_N LVDS_TX3_P LVDS_TX3_P DFP_IOT32_P DFP_IOT32_N	DHDMLTXO_P DHDMLTX1_P DHDMLTX1_N DHDMLTX2_N DHDMLTX2_N DHDMLTX2_N DHDMLTXC_N DHDMLTXC_N DHDMLTXC_N
0/10T9A/F14 0/10T9B/F16	F14_IOT9A F16_IOT9B	——————————————————————————————————————
		File: P004_HDMI.kicad_sch
0/I0T4A/D16 0/I0T4B/E14 0/I0T6A/E16	LVDS_TX1_P LVDS_TX1_N LVDS_TX2_P F10_IOL114	ORTL_PHY_TXD0 ORTL_PHY_TXD1 ORTL_PHY_TXEN

>RTL_PHY_MDC >RTL_PHY_MDIO

>RTL_PHY_RXD1

>RTL_PHY_TXCLK

File: P005 Ethernet.kicad_sch

13

0/IOT9A/F14 F14_IOT9A

0/I0T9B/F16 F16_IOT9B 0/I0T6B/F15 LVDS_TX2_N

3/IOR44A/M6 DFP_IOR44_P

7/I0L27A/A9 A9_I0L27A

		007_Things
		007_IIIIIgs
3/IOR42A/P8	DFP_IOR42_P	->MIC_DATO
3/IOR42B/T8	DFP_IOR42_N	->MIC_DAT1
3/IOR29B/R8	DFP_IOR29_N	→MIC_DAT2
3/IOR29A/T7	DFP_IOR29_P	->MIC_DAT3
3/IOR53A/P6	DFP_IOR53_P	->MIC WS
3/IOR53B/T6	DFP_IOR53_N	->MIC_BCK
3/IOR38B/P9	DFP_IOR38_N	->MIC_LED_CLK
3/IOR38A/T9	DFP_IOR38_P	->WS2812-DAT
		T
3/IOR35B/T10	T10_IOR35B_FPGA_RST	->Silicone Key_1
4/I0B52B/T3	T3_I0B52B_1V5	->Silicone Key_2
4/IOB48B/T2	T2_I0B48B_1V5	->Silicone Key_3
4/IOL53B/D7	D7_I0L53B_1V5	-Silicone Key_4
4/IOL40B/C7	C7_I0L40B_1V5	->Silicone Key_5
		Difficult Rey_5
3/IOR32B/C13	C13_IOR32B_DONE	->Orange_LED0
3/IOR31A/A13	A13_IOR32A_READY	->Orange_LED1
1/IOT52A/N16	LVDS_RX3_P	-DOrange_LED3
1/IOT52B/N14	LVDS_RX3_N	-DOrange_LED2
1/I0T34B/L14	LVDS_RX2_N	-DOrange_LED4
1/IOT34A/L16	LVDS_RX2_P	-DOrange_LED5
•		Dolange_EEDJ
3/IOR31B/B10	B10_IOR31B_RECFG	-DSW1
4/IOL38B/E9	E9_I0L38B_1V5	-DSW2
4/IOL45B/E8	E8_I0L45B_1V5	-DSW3
4/10B45B/T4	T4_I0B45B_1V5	->5W4
4/IOB54B/T5	T5_I0B54B_1V5	-DSW5
., 10, 10		-D2M2

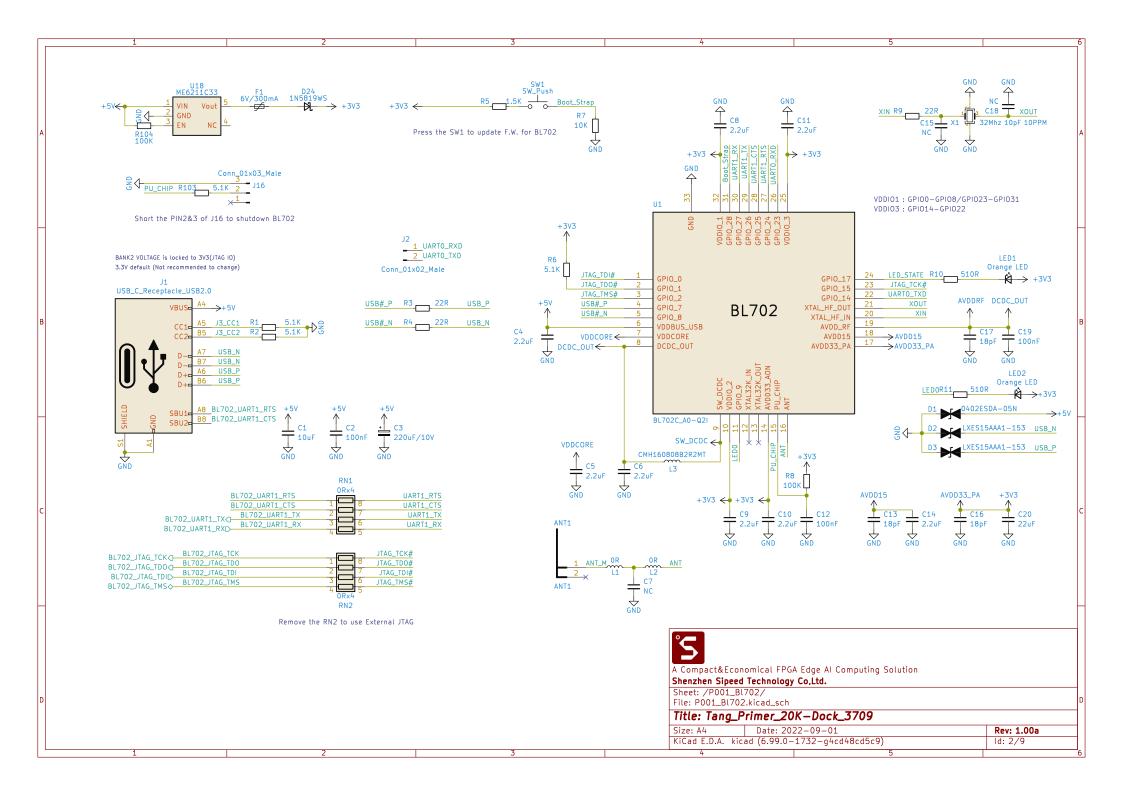
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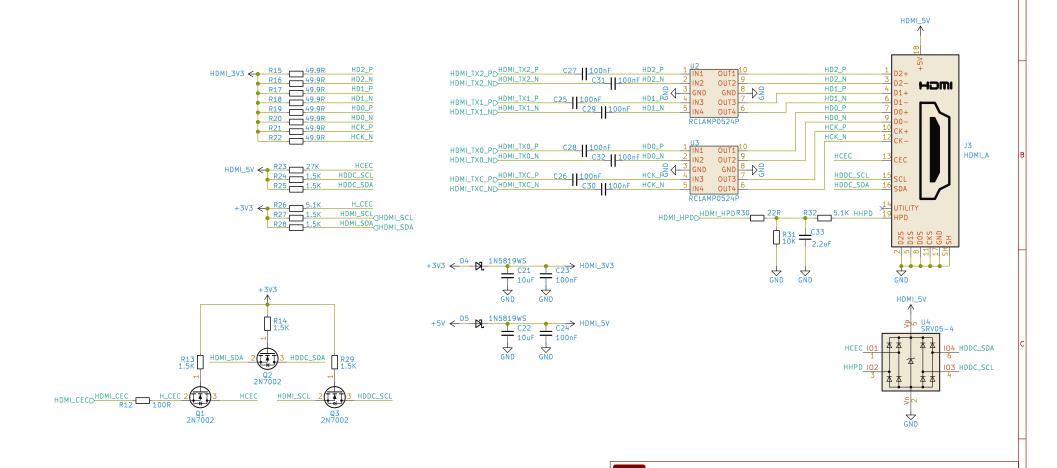
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Rev: 1.00a

ld: 5/9

 Title: Tang_Primer_20K – Dock_3709

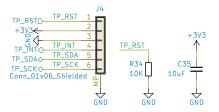
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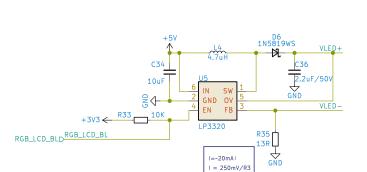
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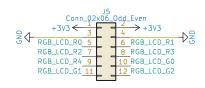
Shenzhen Sipeed Technology Co,Ltd.

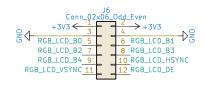
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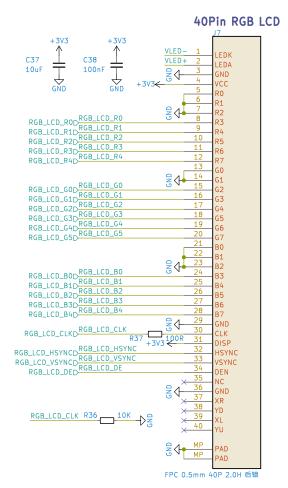
RGB LCD











S

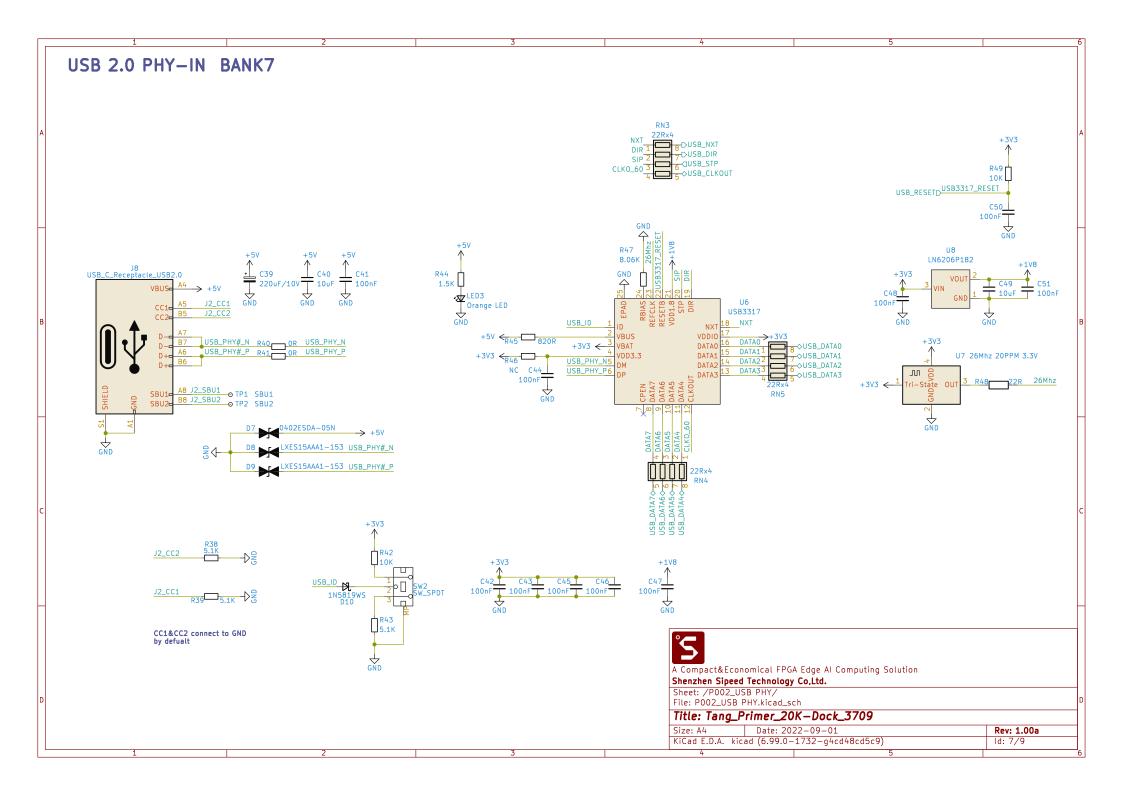
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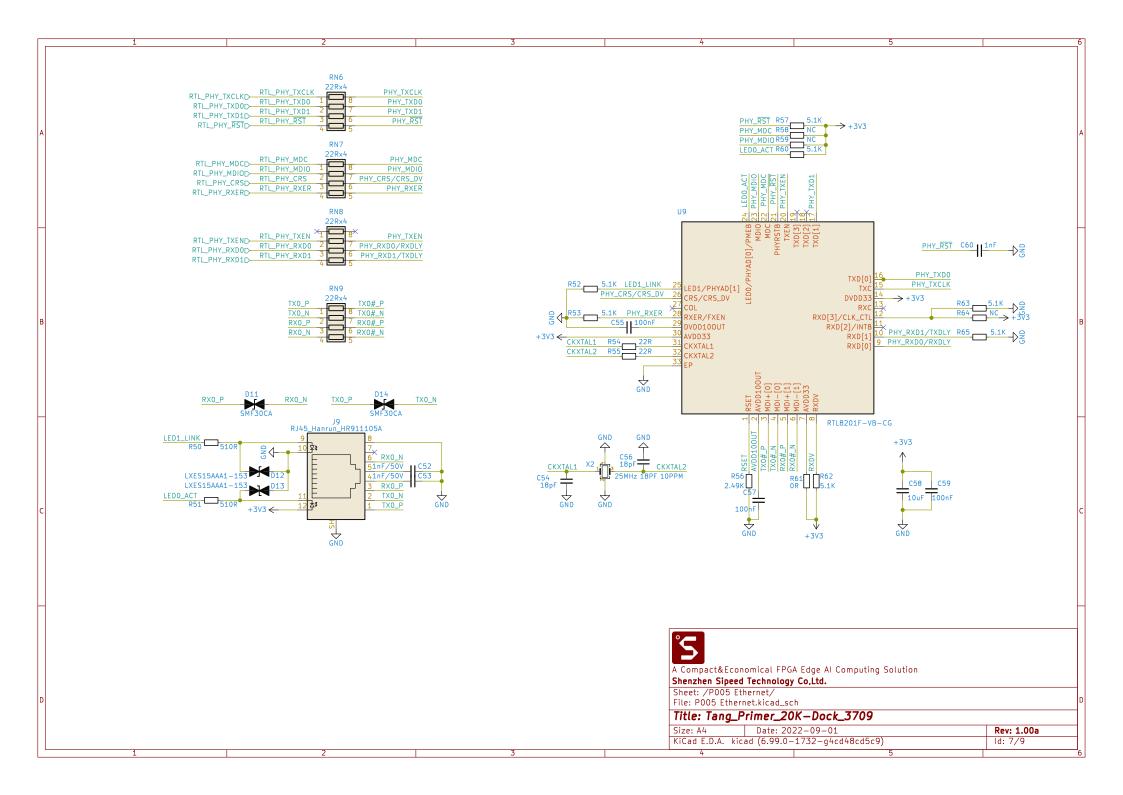
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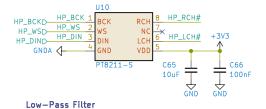
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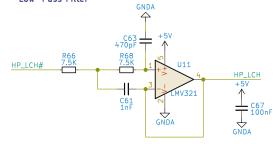


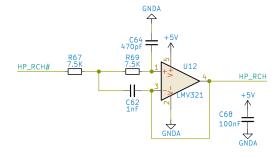


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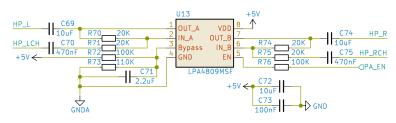
STEREO DAC





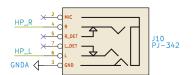


HEADPHONE AMP.





HEADPHONE AMP.





When the jack is unpluged, L/R_DET is connected to L/R_s .



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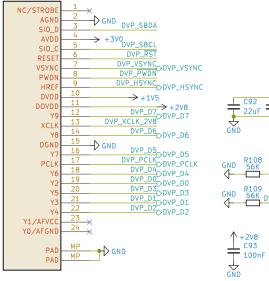
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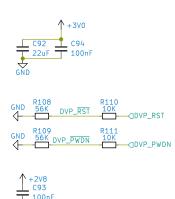
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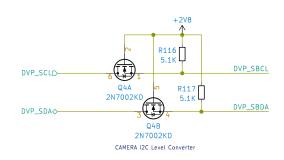
DVP Carmera

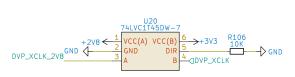


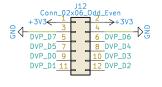


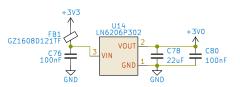
DOVDD refers to I/O Bank voltage

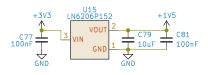


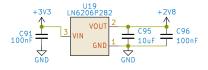














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Size: A4	Date: 2022-09-01		Rev: 1.00a
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