Lab 5 – UART Message State Machine By Zak Rowland and Hayden Hutsell tx_message.v Inputs: clk data_in go Outputs: data_out go = 0 / data_out = 1 Registers: count: 8 bits **START** go = 1 / data_out = 0 count != 8 / count += 1 STOP2 **SHIFT** data_out = data_out = 1 data_in[count] count = 8 / count = 0 STOP1 **PARITY** data_out = data_out = 1 ~^data_in

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