Lab 5 – UART Driver State Machine
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tx_driver.v

Inputs:
- clk
- switch
- message : 80 bits

Outputs:
- go
- tx_character

Registers:
- delay_count : 4 bits
- char_letter_count : 8 bits

~switch / go = 0

IDLE

char_letter_count = 88 /
char_letter_count = 0, go = 0

switch / -

char_letter_count != 88 / go = 0

DELAY

TX CHAR TRANSMIT

tx_character = 8'b message
char_letter_count += 8
delay_count = 0

delay_count != 12 / delay_count += 1

- / go = 1