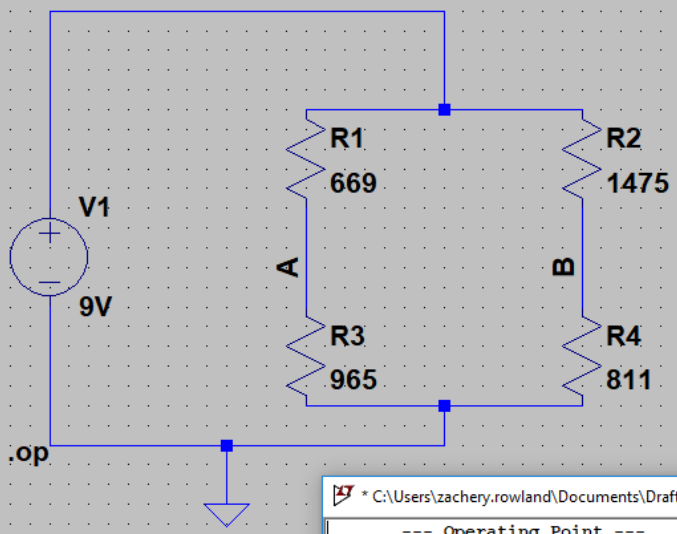
## Original circuit

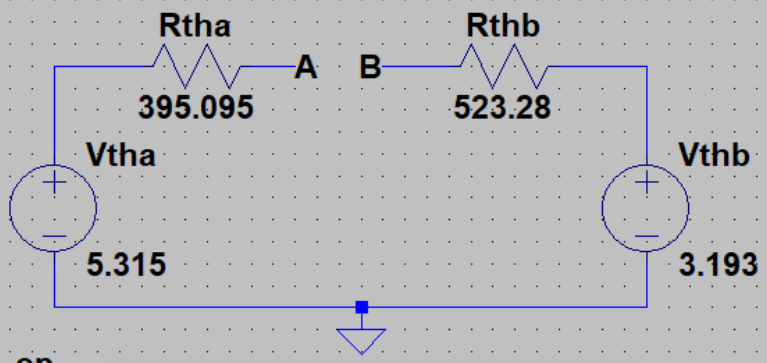



* C:\Users\zachery.rowland\Documents\Draft1.asc

```
        --- Operating Point ---

V(n001):        9               voltage
V(a):           5.31518         voltage
V(b):           3.19291         voltage
I(R4):          0.00393701      device_current
I(R2):          0.00393701      device_current
I(R3):          0.00550796      device_current
I(R1):          0.00550796      device_current
I(V1):          -0.00944496     device_current
```

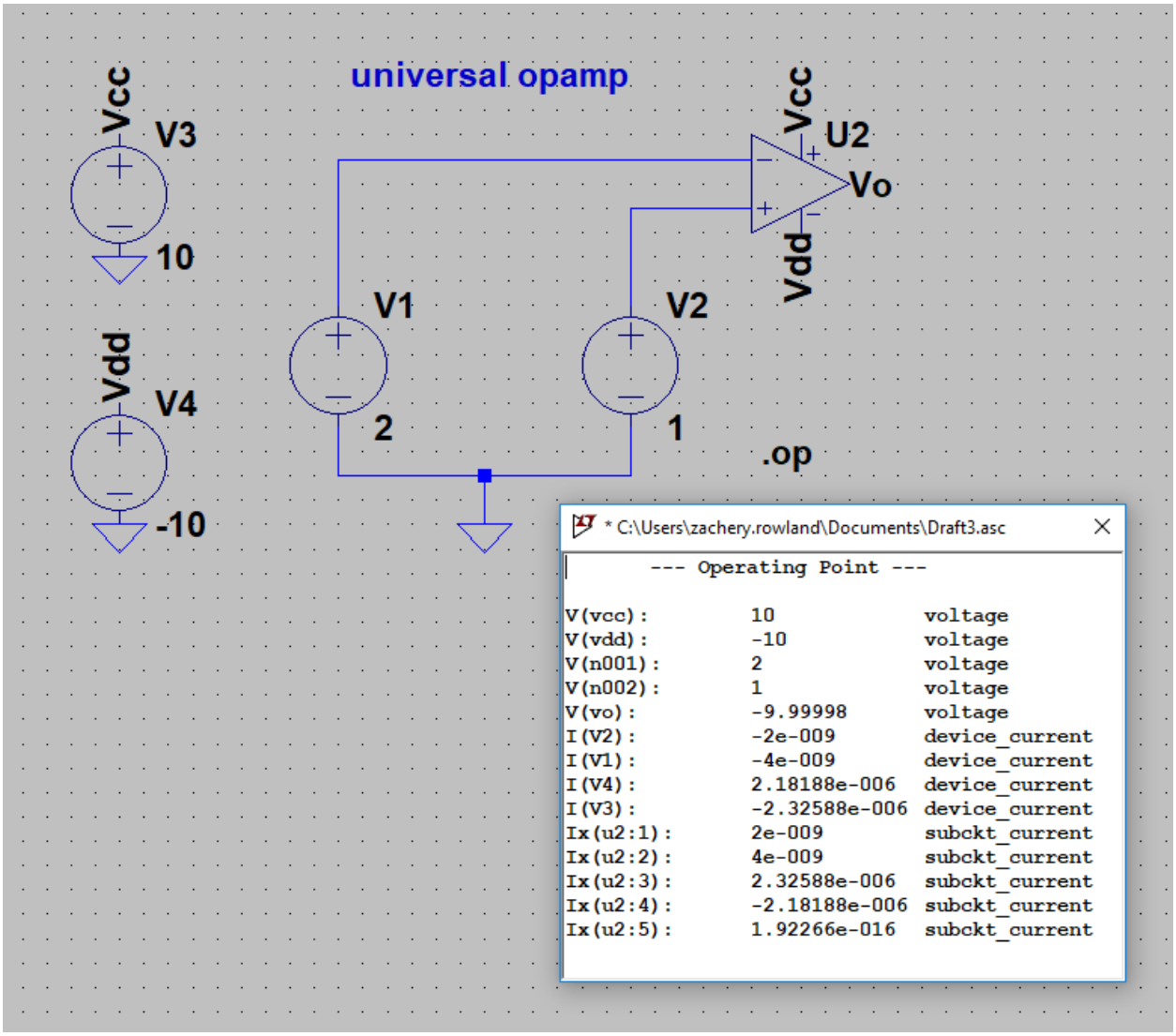
## Thevanin Equivalent




* C:\Users\zachery.rowland\Documents\Draft2.asc

```
        --- Operating Point ---

V(n001):        5.315           voltage
V(n002):        3.193           voltage
V(a):           5.315           voltage
V(b):           3.193           voltage
I(Rthb):        0               device_current
I(Rtha):        -2.24801e-018   device_current
I(Vthb):        0               device_current
I(Vtha):        -1.73472e-018   device_current
```


universal opamp
Vcc
V3
10
Vdd
V4
-10
V1
2
V2
1
Vcc
U2
Vo
Vdd
.op
* C:\Users\zachery.rowland\Documents\Draft3.asc
--- Operating Point ---
V(vcc): 10 voltage
V(vdd): -10 voltage
V(n001): 2 voltage
V(n002): 1 voltage
V(vo): -9.99998 voltage
I(V2): -2e-009 device_current
I(V1): -4e-009 device_current
I(V4): 2.18188e-006 device_current
I(V3): -2.32588e-006 device_current
Ix(u2:1): 2e-009 subckt_current
Ix(u2:2): 4e-009 subckt_current
Ix(u2:3): 2.32588e-006 subckt_current
Ix(u2:4): -2.18188e-006 subckt_current
Ix(u2:5): 1.92266e-016 subckt_current