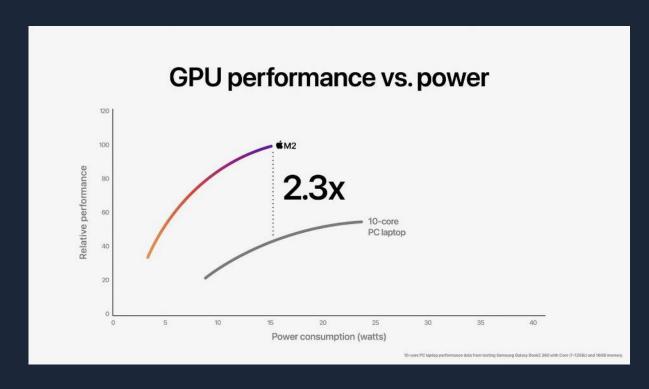
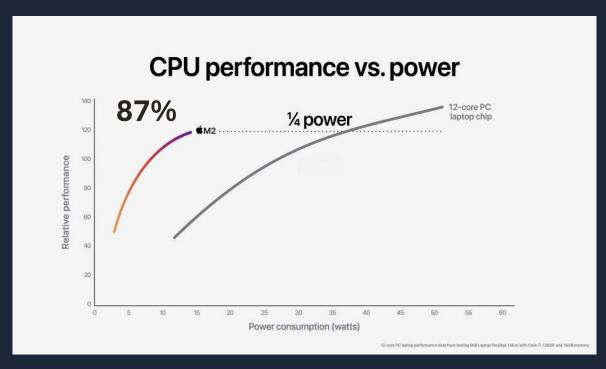
○☐ Imagination REVOLUTIONIZING RISC-Y ADOPTION **Integrated Solution** GPU + CPU + AI

NEW COMPUTE PLATFORMS

Proven benefit from system approach – Apple Silicon





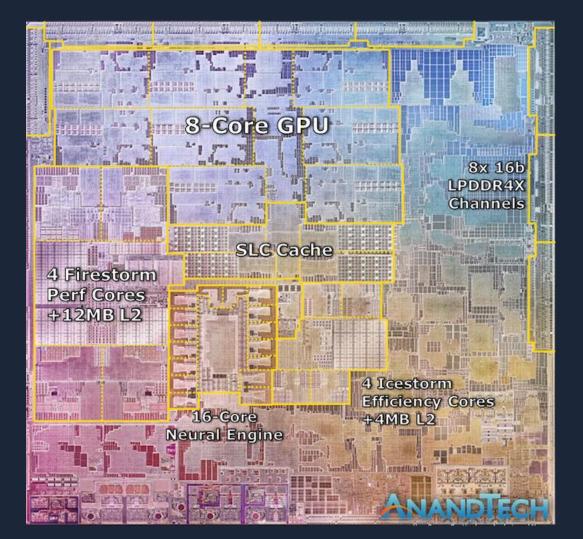
Efficient use of Memory – Scaling challenge

Optimisation at a system level – Bandwidth and system data congestion

System level design drives ecosystem – Faster adoption of RISC-V

NEW COMPUTE PLATFORMS

Proven benefit from system approach – Apple Silicon



M1 Chip	Area (^2 mm)	Percentage
Total Die	118	100%
Total CPU	21	18%
Total GPU	23.5	20%
Total AI	6.6	6%
SLC	6	5%

49%

COMPUTE SUBSYSTEM ACCOUNTS
FOR HALF OF THE SOC AREA



GRAPHICS IS CRITICAL TO COMPUTE PLATFORM

GPU IN 44% OF SEMICONDUCTOR TAM

RISC-Y WILL NOT DISRUPT THE ANDROID MARKET WITHOUT A CAPABLE AND CREDIBLE GPU

GPU IS CRITICAL TO A

GPU is the right architecture for Al

GPU IS THE PLATFORM FOR A1

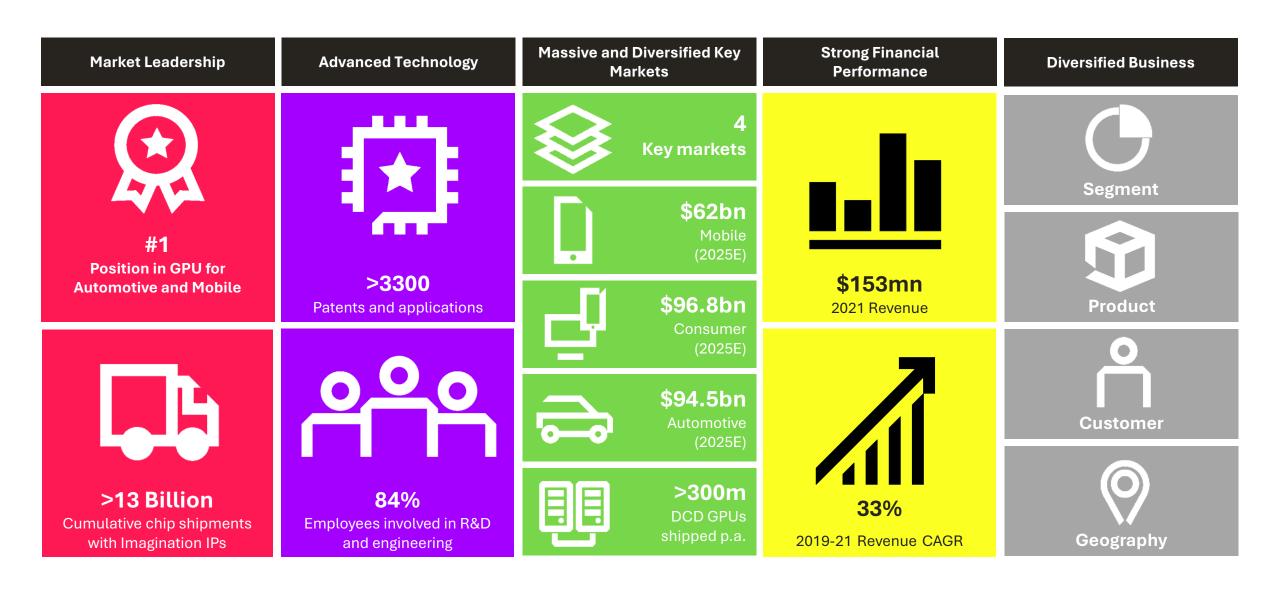
- CPU does not meet the level of compute density needed
- GPU programming model is established and deals well with varied parallel tasks (e.g. NVIDIA with CUDA, UXL and SYCL)
- > Algorithms move too quickly for dedicated hardware alone need some flexibility with a direct programming model
- Array-like approaches do not deliver a direct programming model and struggle with multiple workloads

C I Imagination

YOUR ONE-STOP SOLUTION FOR EMBEDDED COMPUTE

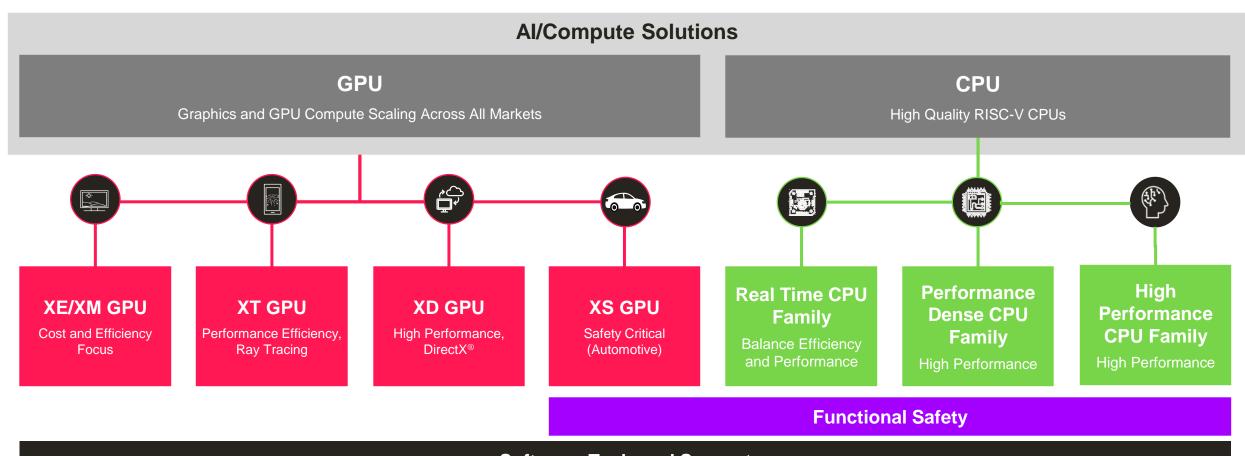
GPU, CPU and Al

IMAGINATION

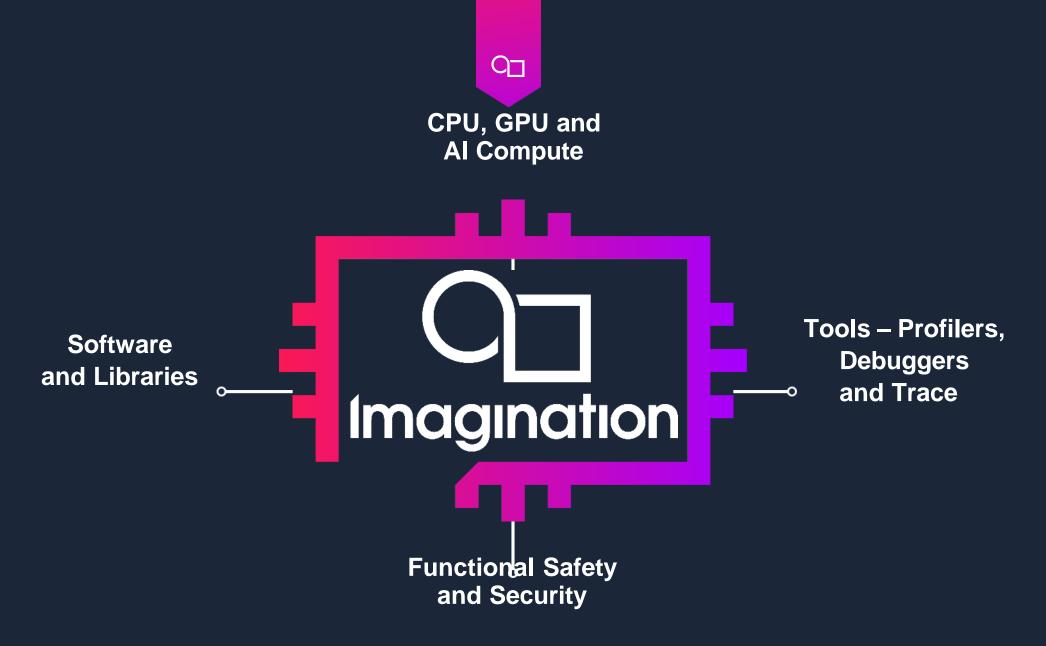


□ (magination

Industry-leading product portfolio: one-stop solution for embedded GPU, CPU and Al



Software, Tools and Support



IMAGINATION INTEGRATED SOLUTION





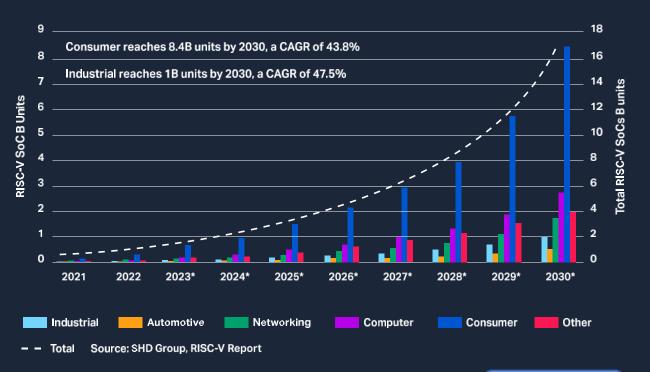
TRENDS IN CONSUMER AND 197

ENHANCED USER EXPERIENCE

- Pervasive deployment of sensors, notably in IoT, consumer applications
- Extensive deployment of Al
- Increasing complexity of user experiences driving the need for integrated graphics
- Silicon cost and efficiency very important



SIGNIFICANT GROWTH OF RISC-Y IN CONSUMER AND INDUSTRIAL APPLICATIONS

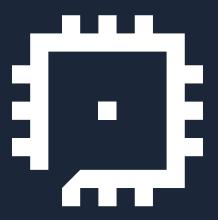




○☐ Imagination

MAGINATION APXM-6200 CPU





Imagination's first RISC-V application processor and the second CPU in our Catapult line of products

A secure CPU that delivers on every customer expectation of performance and trust.

APXM-6200 ADVANTAGES:

PERFORMANCE MEETS TRUST



Performance density



Integrated system (CPU + GPU + Software & Tools + Security)



Ease of migration from Arm based designs

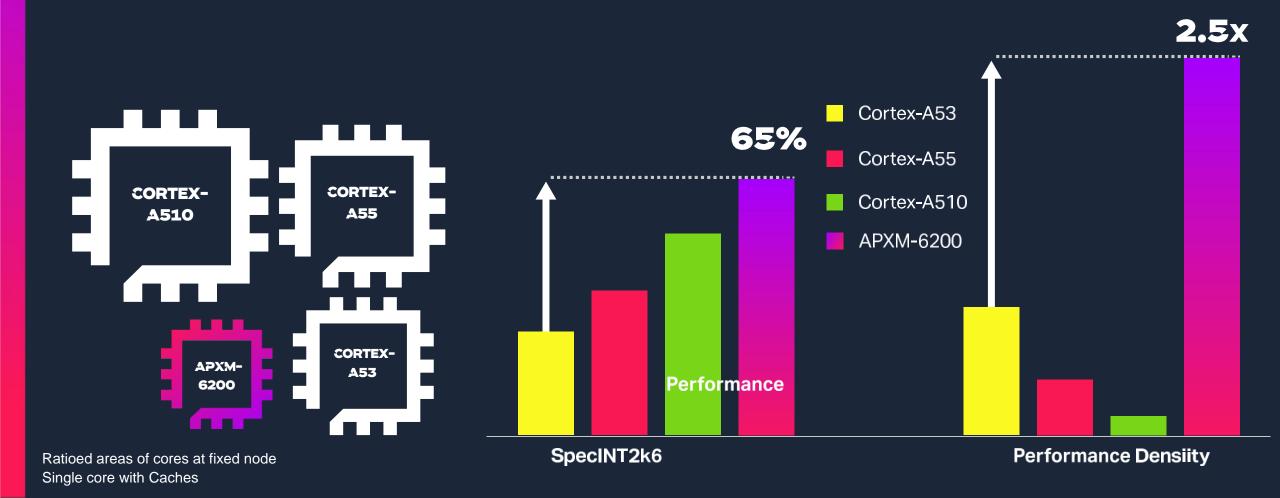


RISC-V profile compliance

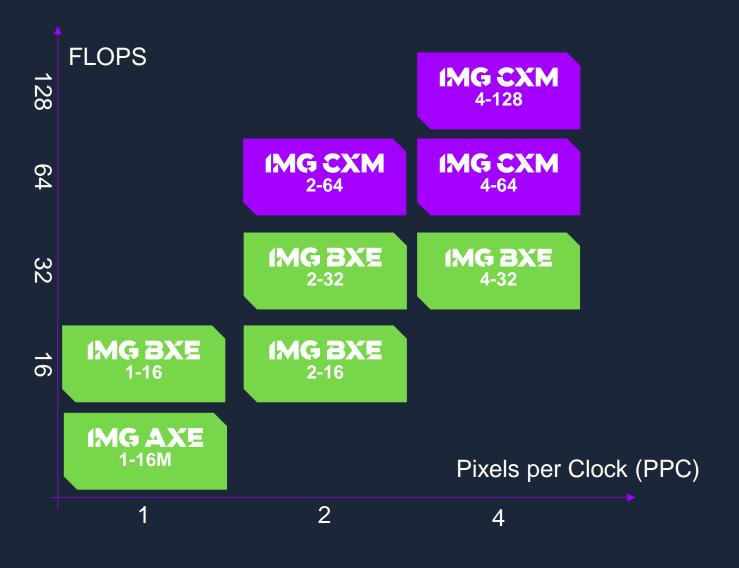
PERFORMANCE

TRUST

APXM-6200 - PROVIDING MARKET LEADING PERFORMANCE AT A FRACTION OF THE SILICON COST



RANGE OF EFFICIENT GPU FAMILY FROM IMAGINATION

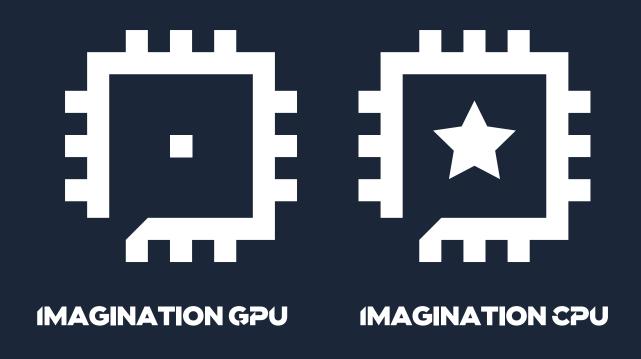


Full range performance points covering entry to flagship consumer /IOT devices

- Fine tuned base configuration for area efficiency
- Support for full HDR plus lossy/lossless compression
- Reduced memory bandwidth and footprint
- Power efficient and cost effective

Proven Android Experience

INTEGRATED CONSUMER & 107 SOLUTION





- ✓ Increase in bus utilisation
- ✓ Reduction in memory traffic



THANK HOLLS