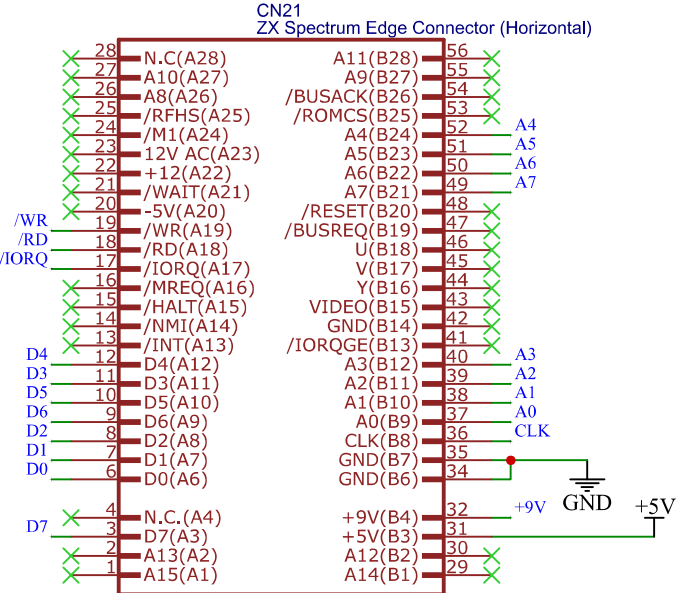
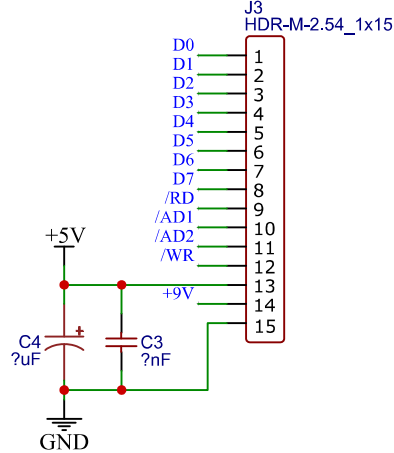


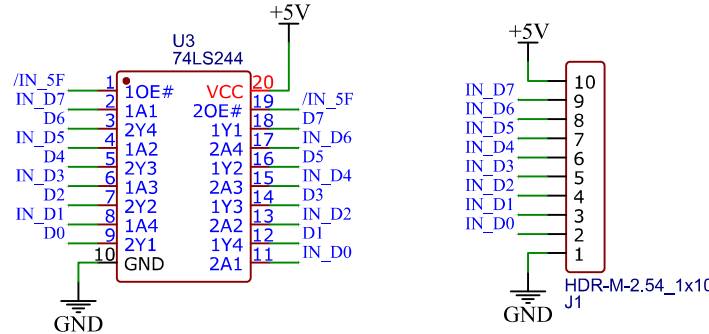
Spectrum Edge Connector



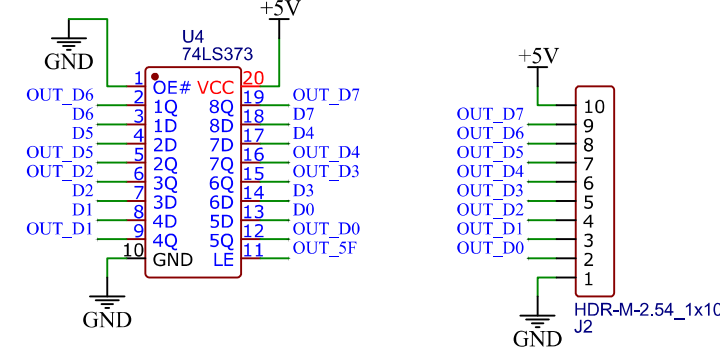
DCP Expansion Connector



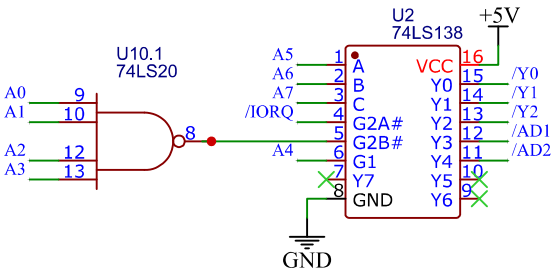
8 Bit Input Port



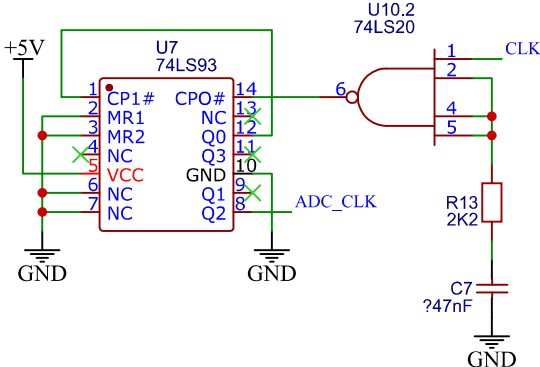
8 Bit Output Port



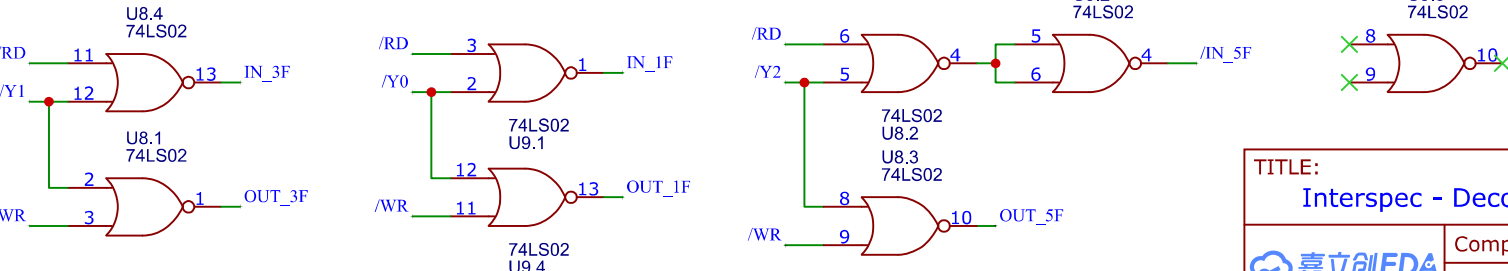
Address Decoder (incomplete)



ADC Clock Gen (incomplete)



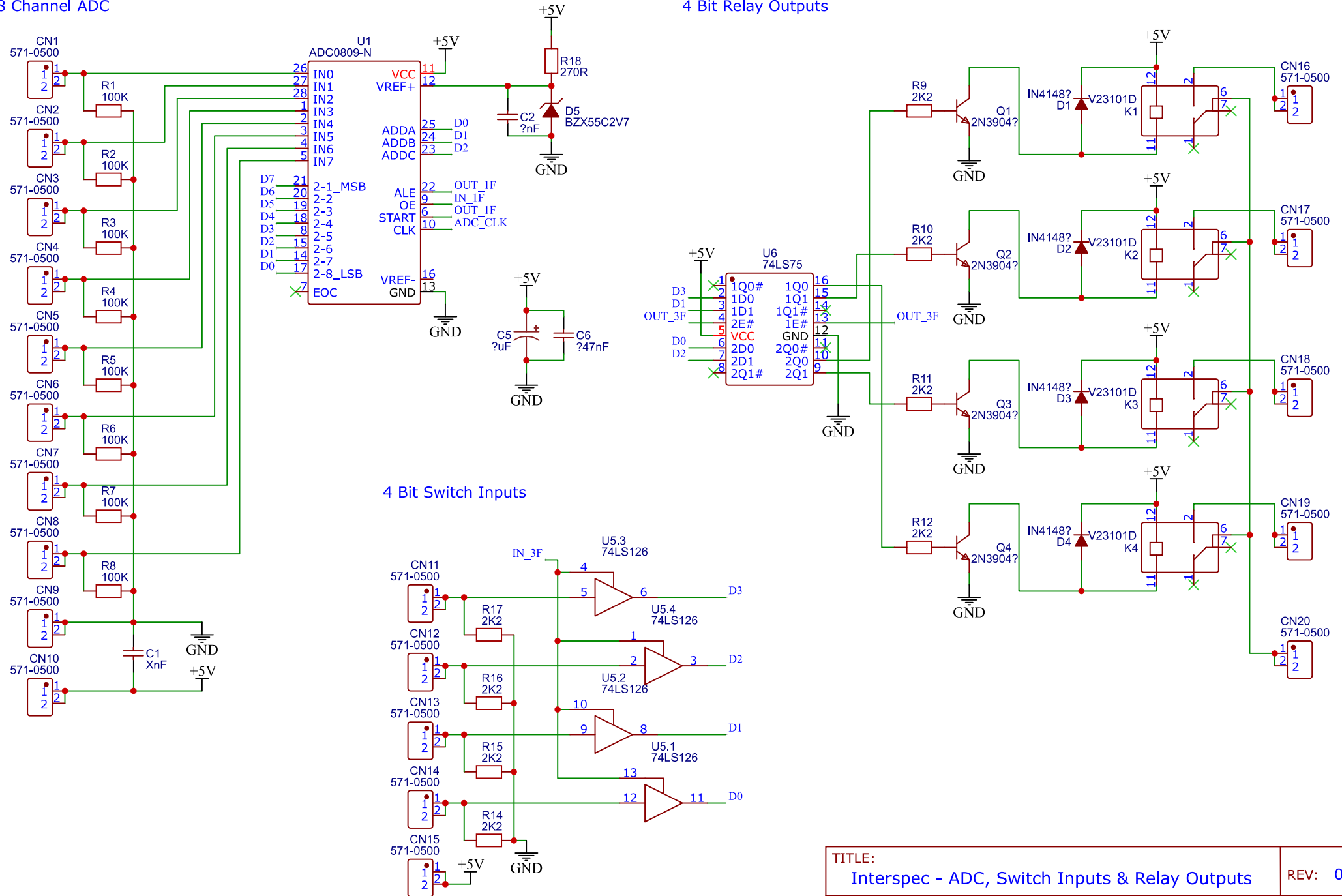
THIS SCHEMATIC IS UNTESTED AND INCOMPLETE



TITLE: Interspec - Decoder, Simple Ports & Clock Gen		REV: 0.1
Company:		Sheet: 1/2
Date: 27-01-2024		Drawn By:

## 8 Channel ADC

## 4 Bit Relay Outputs



**TITLE:**

### Interspec - ADC, Switch Inputs & Relay Outputs

REV: 0.1



Company:

Date: 27-01-2024 Drawn By:

Sheet: 2/2