

Description

GM6155 is an efficient linear voltage regulator with ultralow - noise output, very low dropout voltage (typically 17mV at light loads and 165mV at 50mA), and very low ground current (600 μ A at 100mA output). GM6155 provides better than 1% initial accuracy.

Designed especially for hand-held, battery-powered devices, GM6155 includes a CMOS or TTL compatible enable/ shutdown control input. When shutdown, power consumption drops nearly to zero. Regulator ground current increases only slightly in dropout, further prolonging battery life. Key features of GM6155 include a reference bypass pin to improve its excellent low-noise performance, reversed-battery protection, current limiting, and overtemperature shutdown.

The GM6155 is available in SOT-25 package.

Features

- ◆ Ultra low noise output
- ♦ High output voltage accuracy
- **♦** Extremely accurate output voltage
- ♦ Guaranteed 150mA output
- **♦** Low quiescent current
- **♦** Low dropout voltage
- ♦ Extremely tight load and line regulation
- **♦** Very low temperature coefficient
- Current and thermal limiting
- ♦ Reverse-battery protection
- ♦ "Zero" off-mode current
- ◆ Logic-controlled electronic enable

Application

Cellular telephones

Laptop, notebook, and palmtop computers

Battery-powered equipment

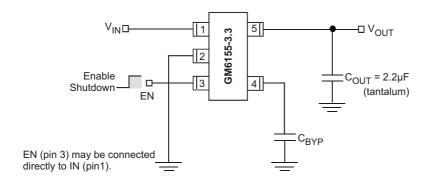
PCMCIA V_{CC} and V_{PP} regulation/ switching

Consumer/ personal electronics

SMPS post-regulator/ dc-to-dc modules

High-efficiency linear power supplies

TYPICAL APPLICATION CIRCUITS



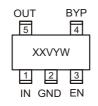
Low-Noise Operation: $C_{BYP} = 470pF, C_{OUT} \ge 2.2\mu F$

Basic Operation: C_{BYP} = not used, $C_{OUT} \ge 1 \mu F$



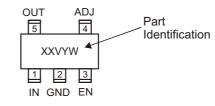
♦ MARKING INFORMATION & PIN CONFIGURATIONS (TOP VIEW)





Fixed Voltages

SOT-25 (SOT-23-5)



Adjustable Voltage

XX = Marking Code(**DA** = GM6155)

/ = Voltage Code

Y = Year W = Weekly

♦ ORDERING INFORMATION

Ordering Number	Output Voltage	Voltage Code	Package	Shipping
GM6155-AST25R	Adj	А	SOT-25	3,000 Units/Tape & Reel
GM6155-2.5ST25R	2.5V	G	SOT-25	3,000 Units/Tape & Reel
GM6155-2.7ST25R	2.7V	Т	SOT-25	3,000 Units/Tape & Reel
GM6155-2.8ST25R	2.8V	Н	SOT-25	3,000 Units/Tape & Reel
GM6155-2.9ST25R	2.9V	Х	SOT-25	3,000 Units/Tape & Reel
GM6155-3.0ST25R	3.0V	J	SOT-25	3,000 Units/Tape & Reel
GM6155-3.3ST25R	3.3V	K	SOT-25	3,000 Units/Tape & Reel
GM6155-3.6ST25R	3.6V	L	SOT-25	3,000 Units/Tape & Reel
GM6155-4.0ST25R	4.0V	М	SOT-25	3,000 Units/Tape & Reel
GM6155-4.2ST25R	4.2V	Y	SOT-25	3,000 Units/Tape & Reel
GM6155-5.0ST25R	5.0V	Q	SOT-25	3,000 Units/Tape & Reel

^{*} For detail Ordering Number identification, please see last page.

♦ PIN DESCRIPTION

Pin Number	Pin Name	Function
1	IN	Supply Input
2	GND	Ground
3	EN	Enable/ Shutdown(Input): COMS compatible input. Logic high = enable, logic low or open = shutdown.
4	ВҮР	Reference Bypass: Connect external 470pF capacitor to GND to reduce output noise. May be left open.
5	OUT	Regulator Output



♦ BLOCK DIAGRAM

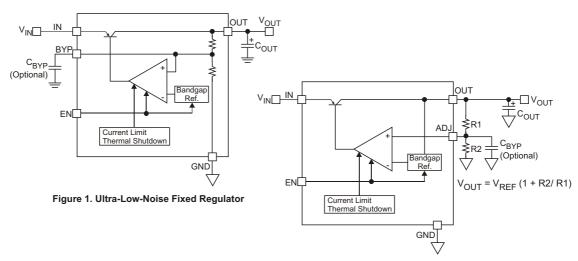


Figure 2. Ultra-Low-Noise Adjustable Regulator

♦ ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNITS
Supply Input Voltage	V _{IN}	-20 ~ + 20	V
Enable Input Voltage	V _{EN}	-20 ~ +20	V
Power Dissipation (Note 3)	P_{D}	Internally Limited	W
Junction Temperature	T _J	-40 ~ +125	°C
Lead Temperature (soldering, 5 seconds)	T _{LEAD}	260	°C
Storage Temperature	T _S	-60 ~ +150	°C

OPERATING RATINGS

PARAMETER	SYMBOL	RATINGS	UNITS
Supply Input Voltage	V _{IN}	+2.5 to +16	V
Enable Input Voltage	V _{EN}	0 to V _{IN}	V
Junction Temperature	T _J	-40 ~ + 125	°C
Thermal Resistance	JA	(Note 3)	°C/W



ELECTRICAL CHARACTERISTICS

 $(V_{IN} = V_{OUT} + 1V; \ I_L = 100 \mu A; \ C_L = 1.0 \mu F; \ V_{EN} \geq \ 2.0; \ T_J = 25 ^{\circ}C, \ \text{bold} \ \ \text{values indicate -40 } ^{\circ}C \leq \ T_J \leq \ +125 ^{\circ}C; \ \ \text{the proof of the proof of the$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit	
Output Voltage Accuracy	Vo	Variation from specified V _{OUT}	-1 -2		1 2	%	
Output Voltage Temperature Coefficient	V _O / T	(Note 4)		40		ppm/°	
Line Regulation	V _O /V _O	$V_{IN} = V_{OUT} + 1V \text{ to } 16V$		0.004	0.012/ 0.05	%/ V	
Load Regulation	V _O /V _O	I _L = 0.1mA to 150mA, (Note 5)		0.02	0.2/ 0.5	%	
		I _L = 100μΑ I _I = 50mA		10 110	50 70 150		
Dropout Voltage (Note 6)	V _{IN} - V _O	I _L = 100mA		140	230 250 300	mV	
		I _L = 150mA		165	275 350		
Quiescent Current	I _{GND}	$V_{EN} \le 0.4V$ (shutdown) $V_{EN} \le 0.18V$ (shutdown)		0.01	1 5	μΑ	
Ground Pin Current (Note 7)	I _{GND}	$V_{EN} \ge 2.0V$, $I_{L} = 0.1 \text{mA}$ $I_{L} = 50 \text{mA}$ $I_{L} = 100 \text{mA}$		350	125 150 600 800	μA	
		I _L = 150mA		1300	1000 1500 1900 2500		
Ripple Rejection	PSRR	f = 100Hz, I _L = 0.1mA		75		dB	
Current Limit	I _{LIMT}	V _{OUT} = 0V		320	500	mA	
Thermal Regulation	V _O / P _D	(Note 8)		0.05		%/ V	
Output Noise	e _{NO}	I_L = 50mA, C_L = 2.2 μ F, 470pF from BYP to GND		260		nV/√ŀ	
ENABLE Input							
Enable Input Logic-Low Voltage	V _{IL}	Regulator shutdown			0.4 0.18	V	
Enable Input Logic-High Voltage	V _{IH}	Regulator enabled	2.0			V	
Enable Input Current	I _{IL}	$V_{IL} \le 0.4V$ $V_{IL} \le 0.18V$		0.01	-1 -2	μΑ	
Enable Input Current	I _{IH}	$V_{IH} \le 2.0V$	2	5	20 25	μA	

- Note 1. Exceeding the absolute maximum rating may damage the device. Note 2. The device is not guaranteed to function outside its operating rating.
- Note 3: The maximum allowable power dissipation at any T_A (ambient temperature) is $P_D(max) = (T_J(max) T_A) \div_{JA}$. Exceeding the maximum allowable power dissipation will result in excessive die temperature, and the regulator will go into thermal shutdown.
- And 4: Output voltage temperature coefficient is defined as the worst case voltage change divided by the total temperature range.

 Note 4: Output voltage temperature coefficient is defined as the worst case voltage change divided by the total temperature range.

 Note 5: Regulation is measured at constant junction temperature using low duty cycle pulse testing. Parts are tested for load regulation in the load range from 0.1mA to 150mA. Changes in output voltage due to heating effects are covered by the thermal regulation specification.

 Note 6: Dropout Voltage is defined as the input to output differential at which the output voltage drops 2% below its nominal value measured at

- Note 7: Ground pin current is the regulator quiescent current plus pass transistor base current. The total current drawn from the supply is the sum of the load current plus the ground pin current.

 Note 8: Thermal regulation is defined as the change in output voltage at a time "t" after a change in power dissipation is applied, excluding load or line regulation effects. Specifications are for a 150mA load pulse at V_{IN} = 16V for t = 10ms.



◆ Applications Information

Enable/ Shutdown

Forcing EN (enable/ shutdown) high (>2V) enables the regulator. EN is compatible with CMOS logic gates. If enable shutdown feature is not required, connect EN (pin 3) to IN (supply input, pin 1). See Figure 3.

Input Capacitor

A $1\mu F$ capacitor should be placed from IN to GND if there is more than 10 inches of wire between the input and the acfilter capacitor or if a battery is used as the input .

Reference Bypass Capacitor

BYP (reference bypass) is connected to the internal voltage reference. A 470pF capacitor (C_{BYP}) connected from BYP to GND quiets this reference, providing a significant reduction in output noise. C_{BYP} reduces the regulator phase margin, when using C_{BYP} , output capacitors of 2.2 μ F or greater are generally required to maintain stability.

The start-up speed of GM6155 is inversely proportional to the size of the reference bypass capacitor. Applications requiring a slow ramp-up of output voltage should consider larger values of C_{BYP} . Likewise, if rapid turn-on is necessary, consider omitting C_{BYP} .

If output noise is not a major concern, omit $C_{\mbox{\footnotesize{BYP}}}$ and leave BYP open.

Output Capacitor

An output capacitor required between OUT and GND to prevent oscillation. The minimum size of the output capacitor is dependent upon whether a reference bypass capacitor is used. $1.0\mu F$ minimum is recommended when C_{BYP} is not used (see Figure 2). $2.2\mu F$ minimum is recommended when C_{BYP} is 470pF (see Figure 1). Larger values improve the regulator's transient response, the output capacitor value may be increased without limit.

The output capacitor should have an ESR (effective series resistance) of about 5 or less and a resonant frequency above 1MHz. Ultra-low-ESR capacitors can cause a low amplitude oscillation on the output and/ or underdamped transient response. Most tantalum or aluminum electrolytic capacitors are adequate; film types will work, but more expensive. Since many aluminum electrolytics have electrolytes that freeze at about -30°C, solid tantalums ate recommended for operation below -25°C.

At lower values for output current, less output capacitance is required for output stability. The capacitor can be reduced to $0.47\mu F$ for current below 10mA or $0.33\mu F$ for current below 1mA.

No-Load Stability

GM6155 will remain stable and in regulation with no load (other than the internal voltage divider) unlike many other voltage regulators. This is especially important in CMOS RAM keep-alive applications.

Thermal Considerations

Gm6155 is designed to provide 150mA of continuous current in a very small package. Maximum power dissipation can be calculated based on the output current and the voltage drop across the part. To determine the maximum power dissipation of the package, use the junction-to-ambient thermal resistance of the device and the following basic equation:

$$P_{D(max)} = \frac{(T_{J(max)} - T_A)}{R_{JA}}$$

 $T_{J(max)}$ is the maximum junction temperature of the die, 125°C, and T_A is the ambient operating temperature. R $_{JA}$ is layout dependent; Table 1 shows examples of junction-to-ambient thermal resistance for the GM6155.



Parameter	R _{JA} Recommended Minimum Footprint	R _{JA} 1" Square Copper Clad	R _{JC}
SOT-23-5	220°C/W	170°C/W	130°C/ W

Table 1. SOT-25 Thermal Resistance

The actual power dissipation of the regulator circuit can be determined by using the equation:

$$P_D = (V_{IN} - V_{OUT}) I_{OUT} + V_{IN} I_{GND}$$

Substituting $P_{D(max)}$ for P_D and solving for the operating conditions that are critical to the application will give the maximum operating conditions for the regulator circuit. For example, when operating Gm6155 at room temperature with a minimum footprint layout, the maximum input voltage for a set output current can be determined as follows:

$$P_{D(max)} = \frac{(125^{\circ}C - 25^{\circ}C)}{220^{\circ}C/W}$$

$$P_{D(max)} = 455mW$$

The junction-to-ambient thermal resistance for the minimum footprint is 220°C/W, from Table 1. The maximum power dissipation must not be exceeded for proper operation. Using the output voltage of 3.3V and an output current of 150mA, the maximum input voltage can be determined. From the Electrical Characteristics table, the maximum ground current for150mA output current is 2500µA or 2.5mA.

$$455\text{mW} = (V_{\text{IN}} - 3.3\text{V}) \ 150\text{mA} + V_{\text{IN}} \cdot 2.5\text{mA}$$

 $455\text{mW} = V_{\text{IN}} \cdot 150\text{mA} - 495\text{mA} + V_{\text{IN}} \cdot 2.5\text{mA}$
 $950\text{mW} = V_{\text{IN}} \cdot 152.5\text{mA}$
 $V_{\text{IN(max)}} = 6.23\text{V}$

Therefore, a 3.3V application at 150mA of output current can accept a maximum input voltage of 6.2V in a SOT-25 package. For a full discussion of heat sinking and thermal effects on voltage regulators.

Fixed Regulator Applications

Figure 3. Ultra-Low-Noise Fixed Voltage Application

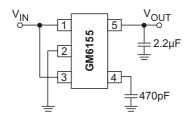


Figure 4. Low-Noise Fixed Voltage Application

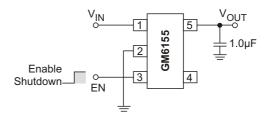


Figure 3. includes a 470pF capacitor for low-noise Figure 4. is an example of a low-noise configuration operation and shows EN (pin 3) connected to IN (pin 1) for an application where enable/ shutdown is not required. $C_{OUT} = 2.2 \mu F \text{ minimum}$.

where C_{BYP} is not required. $C_{OUT} = 1\mu F$ minimum.

Adjustable Regulator Applications

The GM6155 can be adjusted to a specific output voltage by using two externa resistors (Figure 5). The resistors set the output voltage based on the following equation:

$$V_{OUT} = 1.242V \times (\frac{R2}{R1} + 1)$$



This equation is correct due to the configuration of the bandgap reference. The bandgap voltage is relative to the output, as seen in the block diagram. Traditional regulators normally have the reference voltage relative to ground and have a different V_{OUT} equation.

Resistor values are not critical because ADJ(adjust) has a high input impedance, but for best results use resistors of 470k or less for best results. A capacitor from ADJ to ground provides greatly improved noise performance.

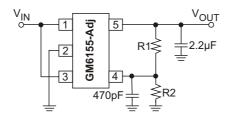


Figure 5. Ultra-Low- Noise Adjustable Voltage Application

Figure 5 includes the optional 470pF noise bypass capacitor from ADJ to GND to reduce output noise.

Dual-Supply Operation

When used in dual supply systems where the regulator load is returned to a negative supply, the output voltage

Typical Characteristics

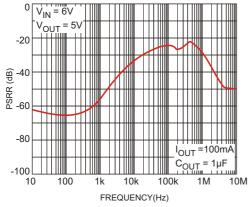


Figure 6. Power Supply Rejection Ratio

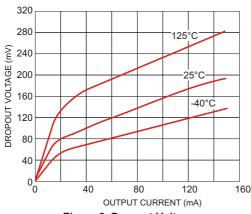


Figure 8. Dropout Voltage vs. **Output Current**

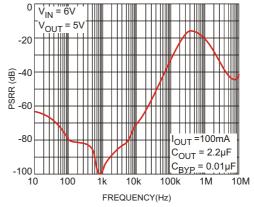


Figure 7. Power Supply Rejection Ratio

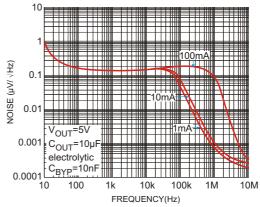
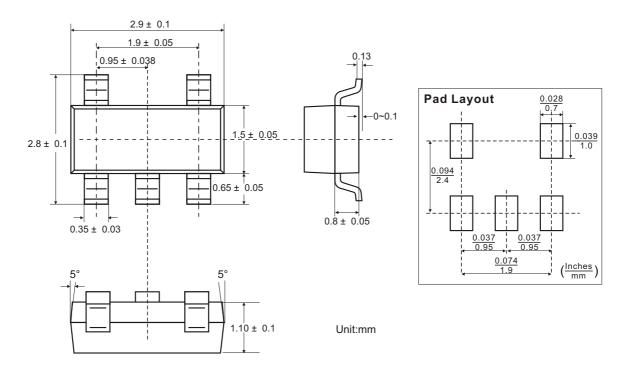


Figure 9. Noise Performance

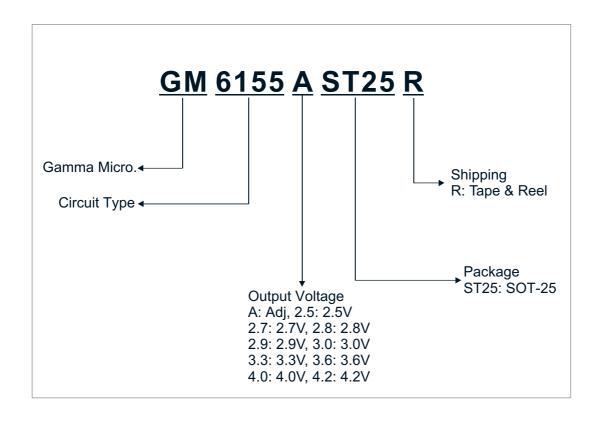




◆ SOT-25(SOT-23-5) PACKAGE OUTLINE DIMENSIONS



◆ ORDERING NUMBER



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