Zachary W. Walden

222 Prairie Ridge Dr. Bartlesville, Oklahoma 74006

(832)-294-9706 | ZachWWalden | zachary.walden@eagles.oc.edu

November 8, 2022

To whom it may concern,

As a recent graduate in Computer Engineering, I seeking a challenging position applying my knowledge and skills designing, building and testing circuit boards. The Design Engineer I position description matches well with my capabilities.

Per my attached resume, I earned a 4.0/4.0 grade point average and graduated summa cum laude. I know the value of hard work and determination.

Following my freshman year, I began working as a Java programmer at my Professor's startup VisuaALS Technology Solutions LLC. There I worked on a REST web API using the Spring Framework. The API was used to validate system purchases, by using storing the device unique IMEI number. I designed a SQL database to store hashed instances of those IMEIs. A remote management feature was also added to simplify the addition of new devices into the database.

During university, I was selected as a teaching assistant for a lower division course, and the sole teaching assistant for a series of two upper division courses. In that role, I aided students in debugging hardware and software issues they faced while working with microcontrollers alongside Digital and Analog peripherals.

I was chosen by a professor to experiment with an RGB laser assembly over winter break. I reverse engineered the included 7 color driver board. Using that knowledge, I found a suitable op-amp based current source for driving a grounded load, then built and tested that design using on hand components. Through testing, I found that the on hand components were not fast enough. I then developed a list of speed and power requirements, using them to source suitable components. Finally, I assembled and tested 2 PCB revisions designed in KiCad. The second of which added an adjustable current set point with a dual gang potentiometer, and incorporated the advanced knowledge in PCB design I acquired during the time inbetween revisions.

Please consider me for this position. I am available by phone or email, which are listed below and on my resume.

Sincerley, Zachary Walden

Zachary W. Walden

222 Prairie Ridge Dr. Bartlesville, Oklahoma 74006

☐ (832)-294-9706 | ⌨ ZachWWalden | ✉ zachary.walden@eagles.oc.edu

—Seeking a Challenging position designing, building, testing mixed signal systems in a team environment. A position with growth and leadership potential that uses my skills, knowledge and experience to provide excellence in product development and manufacturing. I will contribute to the success of projects and products that will expand and improve the product lines for Garmin.

Skills

–**Software**

   Languages—C/C++ (Embedded(RTOS, PIC, STM32, AVR), Linux, Windows), Python, Assembly (AVR) , Verilog (CocoTb) , MATLAB/Simulink, Java (General, Android) , Bash

   Other—GDB, Git, Qt GUI (Python & C++) , General Linux, UML, SYSML, LaTeX, Agile, Docker

–**Hardware**

   Design—Kicad(Schematic Capture, PCB layout), Cadence Design Entry CIS, Digital (Microcontrollers, FPGA, 7400 series), Analog (Op-Amps, Transisitors, Diodes)

   Verification/Validation—Cadence PSpice, Oscilloscopes, Function Generators, Multimeters, Logic Analyzers, Network Analyzers.

   Prototyping—Breadboard, Iron & Hot Air, SMT (Experience with all package types besides BGA, QFN, & Wafer Scale) , THT

Education

–**Oklahoma Christian University**

Bachelor of Science, Computer Engineering, GPA 4.0/4.0, Summa Cum Laude   *Aug. 2018 - Apr. 2022*

Honors & Awards

–**Oklahoma Christian University**
- Outstanding ECE Senior   *2022*
- Outstanding ECE Junior   *2021*
- Outstanding ECE Sophomore   *2020*
- The President's Scholarship   *2018 - 2022*

Experience

–**Oklahoma Christian University**

   Professor's Assistant—   *Dec. 2020 - Jul. 2021*

- Designed 2 revisions of a 3 channel adjustable full color laser diode driver board, using Kicad, to replace a 7 color driver board using an enhanced differential Howland Current Pump.
- Selected components to meet the frequency needs in excess of 12 MHz
- Reverse engineered 7-Color TTL driver board

   Teaching Assistant: CENG-3203, CENG-3213, ENGR-1122—   *Jan. 2020 - Mar. 2020, Aug. 2021 - Apr. 2022*

- Helped students troubleshoot hardware and software issues with 8-bit AVR Microcontrollers & 74 series logic used to control HD44780 LCD's, Keypad enocoders, and parallel DACs in order to control a laser scanner system.
- Instructed students on Assembly language concepts
- Guided students in constructing an autonomous robot
- Debugged Analog & Digital circuitry, and embedded C written for Microchip's 16-bit dsPIC line

–**VisuALS Technology Solutions LLC**

     May. 2019 - Aug. 2020

   Java Programmer—

- Designed a Spring REST API to authenticate a software purchase using a tablet's imei number
- Connected the authentication API to a SQL database that stored the hash of the imei
- Designed a SQL database, running on Google's Cloud
- Wrote dynamic Android U.I.
- Researched Machine Learning for text prediction (Tensorflow, Tensorflow Lite)

Projects

−8-bit 5-stage Pipeline RISC CPU *Jan. 2022 - Apr. 2022*
  – Designed and implemented 32-bit fixed length RISC instruction set comprising 42 instructions.
  – Architected and implemented, from scratch, an 8-bit mostly bypassed CPU with a 5 stage integer pipeline in Verilog and run on a Xilinx Spartan 7 board.
  – Implemented, and integrated a vga sync generator and a dumb framebuffer into the cpu using special instructions to write to the framebuffer.
  – Wrote a simple assembler for the instruction set in Pyhton along with helper scripts to convert the assembler output into Xilinx's coe format.
  – Debugged modules using testbenches written using the cocotb framework for Python.
  – Used Xilinx's Integrated Logic Analyzer IP in conjunction with an internal reset trigger circuit to troubleshoot on chip behaviour.

−DC/DC Boost Converter *Sep. 2020 - Nov. 2020*
  – Designed a discrete boost converter taking a 6V input and giving an adjustable 12V-24V output in steps of .1V using a PIC microcontroller for control.
  – The design achieved an average of .54% error relatice to the target output voltage.
  – Used PIC Datasheet and Family Reference Manual to write register level PWM and SPI drivers.
  – Wrote simple proportional-only control system to stabalize the output voltage, along with a bias adjustment to ensure the correct setpoint was reached.
  – Designed and assembled a printed circuit board for the project.
  – Led 3 person team.

−RGB Laser Scanner Controller
  –

References