Combinational Logic for Control Signals

- $LD\_D = S_3$
- $CL\_D = RST$
- $LD\_IR = S_1$
- $CL\_IR = RST$
- $LD\_AC = S_4 \cdot (ADD + SUB)$
- $CL\_AC = RST + S_2 \cdot CLEAR$
- $ADDORSUB = S_4 \cdot SUB$
- $R/W = S_4 \cdot STORE$
- $D/PC = [RST + S_4 \cdot (BNZ + STORE)]'$
- $LD\_PC = S_4 \cdot BNZ \cdot ZERO$
- $PC\_CNT = S_1 + S_4 \cdot [(ADD + SUB) + BNZ \cdot ZERO' + STORE]$
- $CL\_PC = RST$
- $MEM\_EN = S_1 + S_3 + S_4 \cdot STORE$