|  |
| --- |
| Zachary Boyle  (805) 350 - 8275  zackboyle@gmail.com · www.linkedin.com/in/zack-boyle · https://github.com/ZackBoyle |
| Junior **Electrical and Computer Engineering** student at Santa Clara University. Most experienced in electronic hardware design and the C language family. Has experience in both the corporate sphere and the academic research world. Interested in hardware/software co-design, hardware acceleration, and general hardware design. |

# Experience

|  |
| --- |
| JuL 2022 – Sep 2022Software Engineer Intern: Raytheon Technologies, Goleta, cA Developed modification/port of in-house SimView technology. Transitioned the program from a single-machine application to one which operates over a network of many computers and displays the simulation on a dummy terminal. Done using ASP.net framework in C# via gRPC and Google protobuf. |
| MAr 2023 – PResentResearch Assistant: Santa Clara University, Santa clara, ca Research assistant under Professor H. Yang in the Electrical Engineering department. Project title “Investigating Efficient Hardware/Software Codesign Techniques for Natural Language Processing and Large Language Models”. Investigated effective hardware acceleration methods for Transformer-based neural networks using Xilinx FPGAs and NVIDIA NVDLA technology. |

# Education

|  |
| --- |
| Graduating 2025Electrical and computer engineering, Santa Clara university Current Cumulative GPA: 3.565, Major GPA: 3.606. |
|  |

# RELEVANT Skills

|  |  |
| --- | --- |
| * C, C++, C# * GitHub and Agile Methodologies * Linux and Shell Scripting * Windows | * Vivado and Xilinx FPGA Design Flow * MATLAB * Verilog * Python, TensorFlow, and Keras |

# HOBBIES

|  |  |
| --- | --- |
| * Music – Bass Guitar * Hiking * Basketball | * Board Games * Classical Literature * History |