

1. Description

1.1. Project

Project Name	01_Project
Board Name	NUCLEO-F411RE
Generated with:	STM32CubeMX 6.9.2
Date	10/22/2023

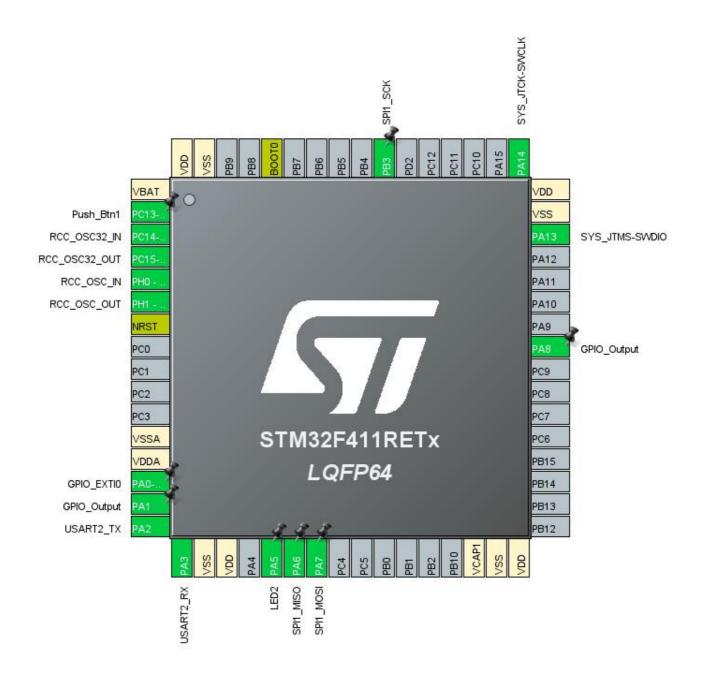
1.2. MCU

MCU Series	STM32F4
MCU Line	STM32F411
MCU name	STM32F411RETx
MCU Package	LQFP64
MCU Pin number	64

1.3. Core(s) information

Core(s)	Arm Cortex-M4	

2. Pinout Configuration

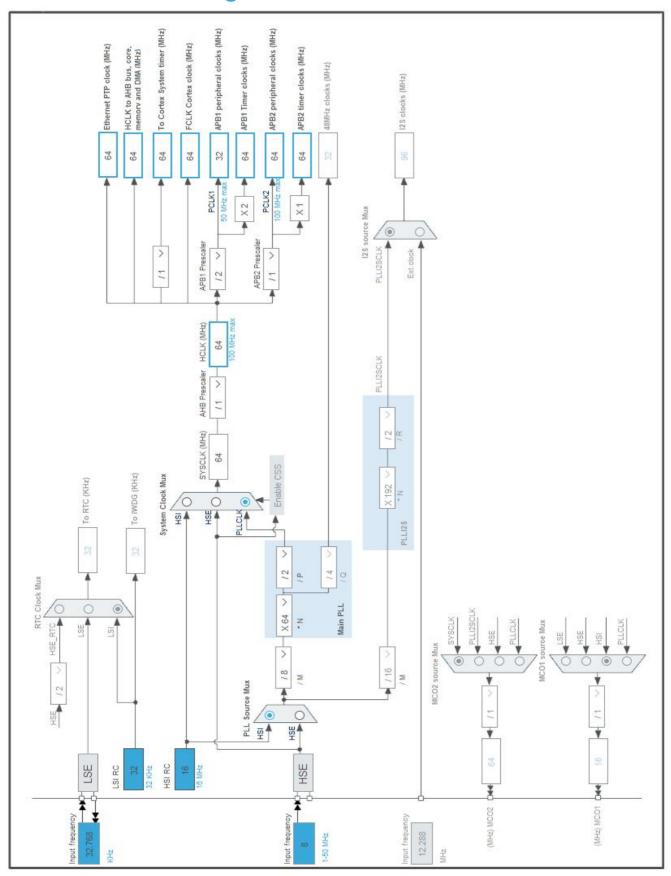


3. Pins Configuration

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
2	PC13-ANTI_TAMP	I/O	GPIO_EXTI13	Duch Ptn1
3	PC13-ANTI_TAMP	I/O	RCC_OSC32_IN	Push_Btn1
4	PC15-OSC32_IN	I/O	RCC_OSC32_IN	
5	PH0 - OSC_IN	1/0	RCC_OSC_IN	
6	PH1 - OSC_OUT	1/0	RCC_OSC_OUT	
			KCC_05C_001	
7 12	NRST	Reset		
	VSSA	Power		
13	VDDA	Power	CDIO EVTIO	
14	PA0-WKUP PA1 *	1/0	GPIO_EXTI0	
15		1/0	GPIO_Output	
16	PA2	1/0	USART2_TX	
17	PA3	I/O	USART2_RX	
18	VSS	Power		
19	VDD	Power	000 0 4 4	LEDO
21	PA5 *	1/0	GPIO_Output	LED2
22	PA6	1/0	SPI1_MISO	
23	PA7	I/O	SPI1_MOSI	
30	VCAP1	Power		
31	VSS	Power		
32	VDD	Power		
41	PA8 *	I/O	GPIO_Output	
46	PA13	I/O	SYS_JTMS-SWDIO	
47	VSS	Power		
48	VDD	Power		
49	PA14	I/O	SYS_JTCK-SWCLK	
55	PB3	I/O	SPI1_SCK	
60	BOOT0	Boot		
63	VSS	Power		
64	VDD	Power		

^{*} The pin is affected with an I/O function

4. Clock Tree Configuration



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5. Software Project

5.1. Project Settings

Name	Value	
Project Name	01_Project	
Project Folder	C:\Users\zafee\Documents\GitHub\Zafeer_BLE\STM32CUBEMX_Projects\01_Pr	
Toolchain / IDE	STM32CubeIDE	
Firmware Package Name and Version	STM32Cube FW_F4 V1.27.1	
Application Structure	Advanced	
Generate Under Root	Yes	
Do not generate the main()	No	
Minimum Heap Size	0x200	
Minimum Stack Size	0x400	

5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	
Enable Full Assert	No

5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name	
1	SystemClock_Config	RCC	
2	MX_GPIO_Init	GPIO	
3	MX_USART2_UART_Init	USART2	

1. Power Consumption Calculator report

1.1. Microcontroller Selection

Series	STM32F4
Line	STM32F411
MCU	STM32F411RETx
Datasheet	DS10314_Rev6

1.2. Parameter Selection

Temperature	25
Vdd	1.7

1.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

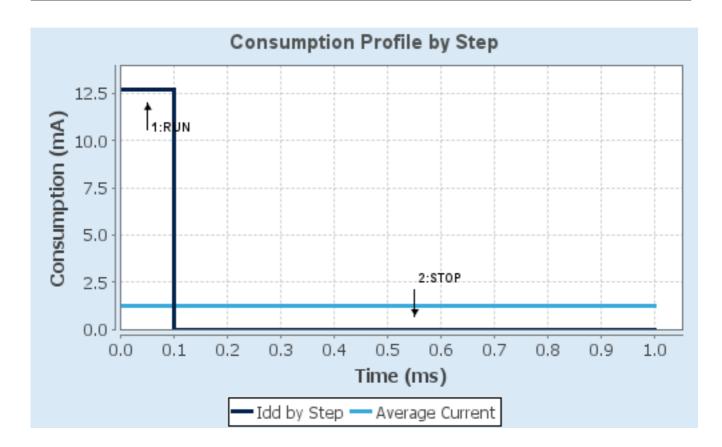
1.4. Sequence

_	_	_
Step	Step1	Step2
Mode	RUN	STOP
Vdd	1.7	1.7
Voltage Source	Battery	Battery
Range	Scale1-High	No Scale
Fetch Type	SRAM	n/a
CPU Frequency	100 MHz	0 Hz
Clock Configuration	HSE PLL	Regulator_LPLV Flash-
		PwrDwn
Clock Source Frequency	4 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	12.7 mA	9 μΑ
Duration	0.1 ms	0.9 ms
DMIPS	125.0	0.0
Ta Max	103.99	105
Category	In DS Table	In DS Table

1.5. Results

Sequence Time	1 ms	Average Current	1.28 mA
Battery Life	3 months, 19	Average DMIPS	125.0 DMIPS
	days, 6 hours	-	

1.6. Chart



2. Peripherals and Middlewares Configuration

2.1. RCC

High Speed Clock (HSE): BYPASS Clock Source

Low Speed Clock (LSE): Crystal/Ceramic Resonator

2.1.1. Parameter Settings:

System Parameters:

VDD voltage (V) 3.3
Instruction Cache Enabled
Prefetch Buffer Enabled
Data Cache Enabled

Flash Latency(WS) 2 WS (3 CPU cycle) *

RCC Parameters:

HSI Calibration Value 16

TIM Prescaler Selection Disabled
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

Power Parameters:

Power Regulator Voltage Scale Power Regulator Voltage Scale 1

2.2. SPI1

Mode: Full-Duplex Master

2.2.1. Parameter Settings:

Basic Parameters:

Frame Format Motorola

Data Size 8 Bits

First Bit MSB First

Clock Parameters:

Prescaler (for Baud Rate) 64 *

Baud Rate 1000.0 KBits/s *

Clock Polarity (CPOL) Low

Clock Phase (CPHA) 2 Edge *

Advanced Parameters:

CRC Calculation Disabled
NSS Signal Type Software

2.3. SYS

Debug: Serial Wire

2.4. **USART2**

Mode: Asynchronous

2.4.1. Parameter Settings:

Basic Parameters:

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

Advanced Parameters:

Data Direction Receive and Transmit

Over Sampling 16 Samples

2.5. STMicroelectronics.X-CUBE-BLE2.3.3.0

mode: WirelessJjBlueNRGAa2

2.5.1. Parameter Settings:

Log & Debug:

BLE2_DEBUG	0: No debug message

PRINT_CSV_FORMAT 0: .csv format message print disabled

BLUENRG2_DEBUG 0: No debug message

Basic Parameters:

HCI_READ_PACKET_SIZE 128
HCI_MAX_PAYLOAD_SIZE 128
HCI_READ_PACKET_NUM_MAX 10

Connection Parameters (for expert users):

 Scan Interval (SCAN_P)
 16384

 Scan Window (SCAN_L)
 16384

 Supervision Timeout (SUPERV_TIMEOUT)
 60

 Min Connection Period (CONN_P1)
 40

 Max Connection Period (CONN_P2)
 40

 Min Connection Length (CONN_L1)
 2000

 Max Connection Length (CONN_L2)
 2000

Advertising Type (ADV_DATA_TYPE)

Connectable Undirected Advertising

(ADV_IND)

Min Advertising Interval (ADV_INTERV_MIN)2048Max Advertising Interval (ADV_INTERV_MAX)4096Min Connection Event Interval (L2CAP_INTERV_MIN)9Max Connection Event Interval (L2CAP_INTERV_MAX)20Timeout Multiplier (L2CAP_TIMEOUT_MULTIPLIER)600

2.5.2. Platform Settings:

Exti Line PA0-WKUP
BUS IO driver SPI1
CS Line PA1
Reset Line PA8

* User modified value

3. System Configuration

3.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
RCC	PC14- OSC32_IN	RCC_OSC32_IN	n/a	n/a	n/a	
	PC15- OSC32_OU T	RCC_OSC32_O UT	n/a	n/a	n/a	
	PH0 - OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PH1 - OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SPI1	PA6	SPI1_MISO	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
	PA7	SPI1_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
	PB3	SPI1_SCK	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
SYS	PA13	SYS_JTMS- SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK- SWCLK	n/a	n/a	n/a	
USART2	PA2	USART2_TX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
	PA3	USART2_RX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
GPIO	PC13- ANTI_TAMP	GPIO_EXTI13	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	Push_Btn1
	PA0-WKUP	GPIO_EXTI0	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	
	PA1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PA5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LED2
	PA8	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	

3.2. DMA configuration

nothing configured in DMA service

3.3. NVIC configuration

3.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Pre-fetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
EXTI line0 interrupt	true	0	0
EXTI line[15:10] interrupts	true	0	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
SPI1 global interrupt	unused		
USART2 global interrupt	unused		
FPU global interrupt	unused		

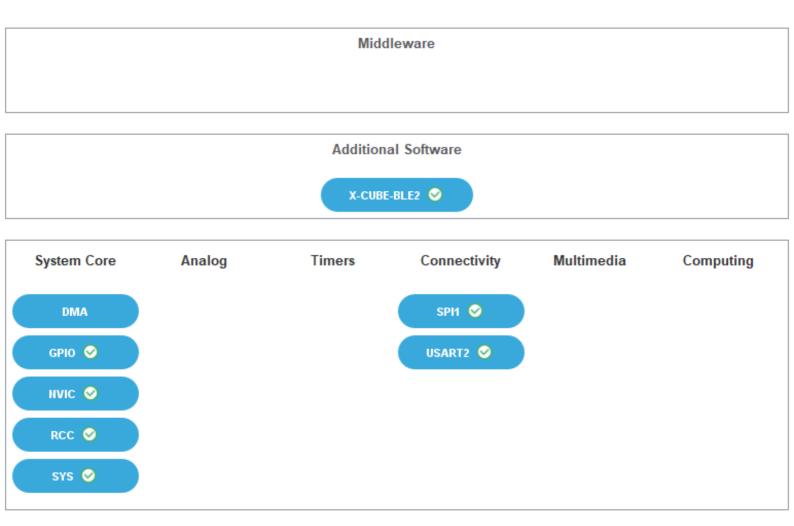
3.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Pre-fetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true
EXTI line0 interrupt	false	true	true
EXTI line[15:10] interrupts	false	true	true

* User modified value

4. System Views

- 4.1. Category view
- 4.1.1. Current



5. Software Pack Report

5.1. Software Pack selected

Vendor	Name	Version	Component
STMicroelectronic	X-CUBE-BLE2	3.3.0	Class : Wireless
s			Group :
			BlueNRG-2
			SubGroup :
			HCI_TL
			Variant : Basic
			Version : 3.3.0
			Class : Wireless
			Group :
			BlueNRG-2
			SubGroup :
			HCI_TL_INTERF
			ACE
			Variant :
			UserBoard
			Version: 3.3.0

6. Docs & Resources

Type Link

BSDL files https://www.st.com/resource/en/bsdl_model/stm32f411_bsdl.zip

IBIS models https://www.st.com/resource/en/ibis_model/stm32f411_ibis.zip

System View https://www.st.com/resource/en/svd/stm32f4_svd.zip

Description

Presentations https://www.st.com/resource/en/product_presentation/stm32-

stm8_embedded_software_solutions.pdf

Presentations https://www.st.com/resource/en/product_presentation/stm32_eval-

tools_portfolio.pdf

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onal-safety-packages.pdf

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stm8_software_development_tools.pdf

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and-smart-i-os.pdf

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discharge-sensitivity-measurement-stmicroelectronics.pdf

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guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf

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microcontroller-system-memory-boot-mode-stmicroelectronics.pdf

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recommendations-and-package-information-for-leadfree-ecopack-mcus-

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- Application Notes https://www.st.com/resource/en/application_note/an2945-stm8s-and-stm32-mcus-a-consistent-832bit-product-line-for-painless-migration-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3070-managing-the-driver-enable-signal-for-rs485-and-iolink-communications-with-the-stm32s-usart-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf
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- Application Notes https://www.st.com/resource/en/application_note/an4616-migrating-from-stm32f401-and-stm32f411-lines-to-stm32l4-series-and-stm32l4-series-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4646-peripheral-interconnections-on-stm32f401-and-stm32f411-lines-stmicroelectronics.pdf
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