

## CURRICULUM VITAE

**MR. JITENDRA KUMAR SHUKLA**  
**B. Tech (ECE), M. Tech (Digital Systems),**  
**Ph. D (Pursuing)**

### PERSONAL PROFILE

*Date of Birth* : 02-11-1991  
*Gender* : Male  
*Father's Name* : Late Mr. Shiva Chandra Shukla  
*Mother's Name* : Mrs. Kamla Shukla  
*Nationality* : Indian



*Linguistic* : Hindi, English

### CONTACT

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### ADDRESS

*Address*: Jitendra Shukla S/O  
Late Shiva Chandra Shukla  
Vill- Jokanai, Post- Pindi,  
Karchhana Prayagraj PIN- 212301

### ACADEMIC PROFILE

Course	Specialization	University/Board	Institution	Period	Performance
X <sup>th</sup> standard	-	U.P. Board	G.I.C. Allahabad (India)	2004-2005	<b>67.17%</b>
XII <sup>th</sup> standard	-	U.P. Board	G.I.C. Allahabad (India)	2006-2007	<b>73.60%</b>
B. Tech	Electronics and Comm. Engineering	Gautam Buddha Technical University U.P	Feroz Gandhi Institute of Engg. & Technology Raebareli Uttar Pradesh (India)	2008-2012	<b>77.50%</b>
M. Tech	Electronics and Comm. Engineering	Dr. APJ Abdul Kalam Technical University U.P	Madan Mohan Malviya Engg. College Gorakhpur Uttar Pradesh (India)	2012-2014	<b>80.75%</b>
P.hd (Pursuing)	--	Madan Mohan Malviya University of Technology Gorakhpur U.P	Madan Mohan Malviya University of Technology Gorakhpur U.P	2020 --	

### ACADEMIC ENDEAVOURS

#### Under Graduate

**Training** :1. EMBEDDED SYSTEMS from Cetpa Infotech Private Limited Lucknow.  
:2.Summer Training on Electronics Exchange, Microwave Networking ,Wireless Networking from Signal and Telecommunication center of North Central Railway Allahabad

**Project** :Automatic room light controller with bidirectional visitor counter

#### Post Graduate

**Thesis** : "Analysis of Square root raised cosine filter by variation of different parameters in WCDMA network"

### ACHIEVEMENTS

★ **ACADEMIC**

- ✓ **Qualified Gate in 2012, 2014 and 2022**
- ✓ **Qualified CBSE-UGC NET DEC-2014 & JUNE-2015 for Assistant Professor**

★ **EXTRA-CURRICULAR ACTIVITIES & INTERESTS**

- ✓ **Engaged as an Expert Panel Member (AE (T) Electronics & Telecommunication Engineering) for conduction of Interview of Assistant Engineer in UPPCL Lucknow in 2016**
- ✓ **Participated as a Technical expert in Technical committee for purchasing of shock baton defender by Uttar Pradesh Police Headquarter Allahabad in 2018**
- ✓ **Member in Anti Ragging Committee in IERT degree Division Prayagraj U.P**
- ✓ **Member in Time Table Committee of Electronics Engg. IERT degree Division Prayagraj**
- ✓ **Lab officer in Charge Analog Circuit Lab**

★ **FACULTY DEVELOPMENT PROGRAM**

- ✓ **Completed Four MOOCS courses through NPTEL**
- ✓ **Participated one week FDP on Recent Trends of Emerging Research Advances in Design Aspects and Innovative Modeling Techniques with Miniaturization for Electronics Devices and Circuits organized by IERT Prayagraj U.P sponsored by ATAL Academy New Delhi from 24-28 Jan 2022**
- ✓ **Coordinator for one week faculty development Program on Advancement in**

Electronics Device and circuits organized by IERT Prayagraj U.P sponsored by Dr. APJ Abdul Kalam Technical University Lucknow from (Feb 17-22, 2020)

- ✓ **Participated in one week faculty development Program on Internet of things in UCEM Prayagraj (May 7-11, 2019)**
- ✓ **Participated in one week faculty development Program on machine learning organized by IERT Prayagraj, UP sponsored by Dr. APJ Abdul Kalam Technical University Lucknow (Feb 26-March-02, 2019)**
- ✓ **Participated in AICTE sponsored one week Short term training Program on Advanced Next Generation wireless technique organized by Deptt. of ECE UCER Prayagraj U.P(20-25 July 2020)**
- ✓ **Participated in One Day Workshop On "NANO-SENSORS & THEIR APPLICATIONS organized by Madhav Institute of Technology and Science Gwalior M.P (31ST JULY 2020)**
- ✓ **Participated in the one week online workshop on "modern antennas for present and futuristic wireless communication technologies" (March 08-13, 2021)**
- ✓ **Development Program on "Advanced Control Strategies: Design and Applications organized by Rajkiya Engineering College Sonbhadra and sponsored by FET, AKTU Lucknow during 28th June to 2nd July 2021**

**PAPER PUBLISHED**

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★ **INTERNATIONAL JOURNALS**

- ✓ **Paper with title "Analysis of square root raised cosine filter by variation of different parameters in WCDMA network" is published in International Journal of Electronics, Electrical and Computational System (a Journal of Academic Science) ISSN 2348-117X Volume 3, Issue 3 May 2014.**
- ✓ **Paper with title "Comparative performance analysis of different pulse shaping filters for WCDMA system" is published in International Journal for Scientific Research and development ISSN (Online) -2321-0613.**

★ **NATIONAL CONFERENCE**

- ✓ **Paper with title "Effect of pulse shaping filter's parameter on BER of QAM modulated WCDMA based communication system" is published in National Conference on Advances in Computers Communication & Embedded System (ACCES-II, 2014) in 2014. ISBN: 978-93-83842-37-7**
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## COURSES TAUGHT AT UG LEVEL

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|--|--------------------------------|
| ✓ Analog Circuits                                | ✓ Digital Communication        |
| ✓ Fundamental of Electronic Devices and Circuits | ✓ Electromagnetic Field Theory |
| ✓ Information theory and Coding                  | ✓ Signals and systems          |
| ✓ Control Systems                                | ✓ Digital Signal Processing    |

## EXPERIENCE

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- ★ Two and half year Teaching experience (**14 July 2015 to 30 Jan 2018**) in **Shri Ramswaroop Memorial College of Engineering & Management (SRMCEM) Lucknow** Uttar Pradesh as an **Assistant Professor** in department of Electronics and Communication Engineering
  - ★ Worked as **Assistant Professor** in department of **Electronics Engineering** at **Institute of Engineering and Rural Technology (IERT) Prayagraj U.P** from **31 January 2018** to till date
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DATE: 13/04/2022

*Jitendra*

(JITENDRA KUMAR SHUKLA)