

NUTAN N. CHAVHAN

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Mrs. Nutan Chavhan

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Plot no.60/61, Above
Abhudaya Bank, Sect-18
Kamothe, Navi Mumbai
Pin 410209 (M. S.)

Academic Profile:***Master of Engineering (Electronics & Telecomm. Engineering): 2018***

ARMJET, Mumbai University, Maharashtra, India

Title: *FPGA Based Real Time Low Power Communication With Android*

Advisor: Dr. Mujib Tamboli CGPA: 7.75/10(First class)

Bachelor of Engineering (Electronics & Telecomm. Engineering): 2010

GECA, Aurangabad, Maharashtra, India

Research Topic: *Pick and Place robot using microcontroller*

Advisor: Prof. Sangita Joshi CGPA: 6.67/10(First class)

H.S.C: 2005

Shri Shivaji Jr. College, Washim, Amravati Board, Maharashtra

Total Marks: 80.17% (First class with Distinction)

S.S.C: 2003

Shri Shivaji Highschool, Washim, Amravati Board, Maharashtra

Total Marks: 81.60% (First class with Distinction)

Experiences:***Teaching Experience:***

- ✓ Working as a (August, 2019 to till date) Visiting Faculty in E&TC Engineering Department, Amity University, Mumbai, India
- ✓ Worked as an (June 2012 to May 2017) Asst. Professor in E&TC Engineering Department, A.C. Patil Engineering College, Kharghar, Navi Mumbai
- ✓ Worked as an Asst. Professor in E&TC Engineering Department for duration of one semester (August 2011- February 2012), Pratap Institute of Technology, Washim
- ✓ Worked as a Visiting Faculty for duration of two semesters (June 2010- June 2011) in Govt. Poly. Washim

Journal Publications:

National : 00
International : 02

- FPGA Based Real Time Low Power Bluetooth Communication with Android System organized by International Journal of Engineering Technology, Management and Applied Sciences, May 2017, Volume 5, Issue 5, ISSN 2349-4476 during May 2017
- Real Time Low Power Bluetooth Communication between an FPGA and Android System organized by ICRESMH-2017, ISBN: 978-81-932712-3-0

Conferences Publications:

International

- FPGA Based Real Time Low Power Bluetooth Communication with Android System organized by International Journal of Engineering Technology, Management and Applied Sciences, May 2017, Volume 5, Issue 5, ISSN 2349-4476 during May 2017
- Real Time Low Power Bluetooth Communication between an FPGA and Android System organized by India International Center, Max Muller Marg, Delhi, India during January 2017

Seminar/Workshops Attended:

- Attended Scilab Training at A. C. Patil Engineering College Kharghar during 27 JUNE 2016
- Attended Induction Program Phase-II by NITTTR, Bhopal at Bharti Vidyapith, Navi Mumbai during 05 TO 17 JUNE, 2016
- Attended Induction Program Phase-I by NITTTR, Bhopal at K.J. Somaiya Polytechnic, Mumbai during 16 TO 27 MAY, 2016
- Attended Workshop on PLC & SCADA by MSBTE at Govt. Poly. Bandra during 1 TO 5 February, 2016
- Attended Workshop on Electronics Instrumentation by Synergy India Ltd from 15 to 20 July 25.
- Attended Electronic Instrumentation & Measurement by MSBTE at St. Xavier's Polytechnic. Mahim during 13 TO 14 August 2015
- Attended Reliance Thermal Power Plant Dahanu by MSBTE at Reliance Thermal Power Plant Dahanu during 13 to 17 February 2015
- Attended one week workshop on Audio Video Engineering at Bharti Vidyapith Navi Mumbai by Prasara Bharti Mumbai.
- Attended orientation program for curricular development for various subjects by MSBTE.

Administrative and Academic Responsibilities:

- Convener & Member of Internal Academic Monitoring Committee for MSBTE from June 2014 to May 2017
- Controller of Examination for MSBTE Exam for one semester in May 2016
- Internal Vigilance Committee Member from April 2014 to May 2017
- Convener for student placement coordinator from June 2014 to December 2016
- Convener for institute industry partnership from June 2014 to December 2016
- Industrial Visit & Expert Lecture Coordinator
- Convener for apprenticeship training of students.
- Academic time table coordinator of the institute.
- Member of curricular development program for MSBTE.
- A Subject Expert for Electronics Devices & Circuits, MSBTE

Experimental Skills:

- ✓ Acquainted with undergraduate lab experiments and postgraduate laboratory courses
- ✓ Familiarity with Pspice, MATLAB, LAB VIEW, Orcad

Computing Skills:

- ✓ Conversant with operating systems: Windows and Linux

Personal Details:

- Date of Birth : 1st Janury 1988
- Father's Name : Mr. Namdeo F. Chavhan
- Marital Status : Married
- Nationality : Indian
- Languages known : Marathi, Hindi and English

Declaration:

I hereby declare that all the above information is true to the best of my knowledge and belief.

Date: - 08/03/2022

Place: - Navi Mumbai

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