

Dr. S. RADHA

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Assistant Professor - ECE

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Career Summary:

A doctorate in Wireless Communication and has over all 14 years of experience in the field of Teaching in Engineering colleges.

Objective:

Aspiring for an enthralling career in teaching profession especially in the field of **Electronics and Communication Engineering** apropos my capabilities and to give the best out of me.

Professional Experience:

- Assistant professor in Karunya University (**Deemed University**), Coimbatore from Jun 2012 to tilldate.
- Worked as Assistant Professor in TKRCET, Hyderabad from Dec 2011 to Apr 2012.
- Worked as Assistant Professor in TKRCET, Hyderabad from Oct 2006 to Dec 2009.
- Worked as Lecturer in GKCE, Sullurpet, Sriharikota, A.P. from Jun 2005 to May 2006.

Education Qualification:

- **Ph. D.** on Wireless Communications at Karunya University, Coimbatore in the year 2021.
 - **Course work** in PhD completed **with 90%** in the year 2013.
- **Master of Technology (M. Tech)** in **VLSI SYSTEM DESIGN** at **Teegala Krishna Reddy College of Engineering and Technology, JNT University, Hyderabad.** in the year 2011-
I class with distinction
 - **Topper with 82%**
- **Bachelor of Technology (B. Tech)** in Electronics and Communication Engineering from **Srikalahastheeswara Institute of Technology (SKIT), Srikalahasthi, JNT University, Anantapur, A.P.** in the year 2005 - I class with distinction **75.58%**.
- Intermediate from **Seven Hills Junior College, Tirupathi** (AP BOARD OF EDUCATION) in the year 2001 – I class with distinction **92.1%**
- **SSC** from **SSC BOARD OF EDUCATION, A.P.** in the year 1999 - I class with distinction **86.83%**
 - **Topper in Bangarupalyam mandal, Chittoor (Dt), A.P. with 521 marks.**

Academic Achievements (certificate of excellence / merit / Awards / Honors / Distinction):

- Qualified in **UGC - NET Jun 2020** (Electronics Science) conducted by NTA in Nov 2020.
- Qualified in **UGC – NET Dec 2019** (Electronics Science) conducted by NTA, India
- Qualified in **FET-2011**(Faculty Eligible Test-Conducted by JNTU, Hyderabad).

- Qualified in **GATE 2019** (conducted by **IIT, Madras**) with score 415 validity 17 March 2019 to 16 March 2022.
- Qualified in **GATE 2015** (conducted by **IIT, Kanpur**) with score 413 validity 27 March 2015 to 26 March 2018.
- Qualified in **GATE 2013** (conducted by **IIT, Bombay**) with score 449 validity 15 March 2013 to 14 March 2014 and **GATE 2013** with score 533 validity 15 March 2014 to 14 March 2015.
- Qualified in **GATE 2012** (conducted by **IIT, Delhi**) with 482 score validity 15 March 2012 to 14 March 2014.
- Qualified in **GATE 2009** (conducted by **IIT, Roorkee**) with **95%** score validity 15 March 2009 to 14 March 2010 and completed M.Tech in VLSI SYSTEM DESIGN with this GATE Score. Received **MHRD Scholarship of 8000/- per month** for a period of 24 months.
- Qualified in **GATE 2008** (conducted by **IISc, Bangalore**) with **87%** score validity 15 March 2008 to 14 March 2010.

- Qualified in **BEML (Govt. Sector)** Written Exam (**AIR-88**) for the post of **Executive Trainee (ET)** in 2011.
- **Topper in M.Tech** (82%) and received gold medal from college chairman in 2011.
- Selected for Interview in **OCES-DGFS BARC** (Bhabha Atomic Research Center) based on 2009 GATE Score.
- Qualified in **ISRO** Written Exam (**AIR-103**) for the post of **Scientist-B** in 2011.
- Qualified in **ISRO** Written Exam for the post of **Scientist-B** in 2008.
- **Topper in SSC** (in Bangaru Palyam mandal, Chittoor(Dt),A.P.) with 521 marks in the year 1999.
- Qualified in **Jawahar Navodaya Vidyalaya Entrance Exam** and completed class VI-IX education (in the year 1994 - 1998) from **Jawahar Navodaya Vidyalaya, Valasapalli**, Near MadanaPalli, Chittoor(Dt), **A.P.** under **Jawahar Navodaya Vidyalaya Samithi**. Based on merit - Complete education, sports, amenities are fully **sponsored by central government**.

Expertise in the subjects:

- **Control Systems, Digital Electronics**
- **Signals & Systems, Communications**
- **Electronic Devices and Circuits**

Professional Excellence Summary:

- Self-motivated, devoted and decisive management personnel.
- Have excellent analytical as well as mechanical features.
- Have ability in taking up new theories as well as responsibilities.
- Have in depth knowledge in the respective subject.

Software Skills :

Operating Systems : Windows 98 , 2000, Windows XP

Packages : MS-Word , MS-Excel , MS-Power Point

Software : **MAT Lab , VHDL , Verilog , MASM 5.1V/6.1V**

NPTEL / AICTE MOOC COURSES

S.No	Organizer	Type	Title
1	IISC, Bangalore	IIT online course Online certification exam Apr 2021 (silver medal)	NBA Accreditation and Teaching-Learning in Engineering (NATE) Jan 2021 – Apr 2021 with 12 assignments
2	IIT Madras	IIT online course Online certification exam Nov 2019 (Elite)	Deep Learning Jul 2019 – Nov 2019 with 12 assignments
3	IIT Madras	IIT online course Online certification exam 28 th Oct 2018 (Elite)	Switching Circuits and Logic Design Jul 2018 – Oct 2018 with 12 assignments
4	IIT Madras	IIT online course Online certification exam 22 nd Oct 2017 (Elite)	Control Engineering Jul 2017 – Oct 2017 with 12 assignments
5	IIT Kharagpur	IIT online course online certification exam 23 rd Apr 2017 (Elite)	Medical Image Analysis Feb 2017 – Mar 2017 with 4 assignments
6	IIT Madras	IIT online course online certification exam 26 th Mar 2017 (Elite)	Wireless Ad hoc and Sensor Networks Jan 2017 – Mar 2017 with 12 assignments
7	IIT Madras	IIT online course online certification exam 1 st Nov 2016 (Elite)	Discrete time signal processing Jul 2016 – Nov 2016 with 12 assignments
8	IIT Madras	IIT online course online certification exam 1 st Nov 2015 (Elite)	Networks and Systems Jul 2015 – Nov 2015 with 12 assignments
9	IIT Kanpur	IIT online course	Probability and Random Variables/

		online certification exam 8 th Nov 2015 (Elite)	Processes for Wireless Communications. Sep 2015 – Nov 2015 with 4 assignments
10	IIT Kanpur	IIT online course online certification exam 13 th Sep 2015 (Elite)	Principles of Modern CDMA/MIMO/OFDM Wireless Communications Jul 2015 – Aug 2015 with 8 assignments
11	IIT Madras	IIT online course online certification exam on 6 th Sep 2015	Basic Electrical Circuits Jul 2015 – Aug 2015 with 8 assignments

Journal Publications : (Total ScopusPublications-32 including conf, International Journals– 14)

(Sci Indexed – 7, Scopus – 4, NonScopus/UGC Listed – 3)

<p>Scopus ID: 24344889500, Web of Science Researcher ID: AAQ-7281-2020, URL of Scopus: https://www.scopus.com/authid/detail.uri?authorId=24344889500 URL of WoS: https://publons.com/researcher/3632847/s-radha/ ORCID ID: 0000-0003-4817-3683, Google Scholar ID: RWx8hwoAAAAJ, Scopus h-index: 5, Scopus Citations: 86, Google Scholar h-index: 5, Google Scholar i10-index: 2, Google Scholar citations: 115 NME-ICT Vidwan Profile URL: https://vidwan.inflibnet.ac.in//profile/232597/MjMyNTk3 Google Scholar URL: https://scholar.google.com/citations?hl=en&user=RWx8hwoAAAAJ Linked in URL: https://www.linkedin.com/in/radha-subramanyam-a510b0218/ Skype ID: nagabushanammooc@gmail.com Research gate URL: https://www.researchgate.net/profile/Radha-Subramanyam</p>
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Sci Indexed / Web of Science Publications:

- **Radha, S. ***, G.Josemin Bala, Nagabushanam, P., “Multilayer DS-MAC with Game theory Optimization”, Circuit World- July 2021. (ISSN: 0305-6120) (**Impact Factor – 0.875, Emerald Publishers**), DOI (10.1108/CW-08-2020-0197)
- **Radha, S. ***, G.Josemin Bala, Nagabushanam, P., E. Grace Mary Kanaga, “Energy Efficient MAC with Variable Duty Cycle for Wireless Sensor Networks”, International Journal of Electronics, Feb 2021 (**Impact Factor – 1.336, Taylor and Francis**)- DOI: 10.1080/00207217.2021.1892202, SJR- 0.26
- **Radha, S. ***, G.Josemin Bala, Nagabushanam, P., “FRAME routing with Game theory Optimization for wireless networks”, International Journal of Communication systems, Vol 34, Issue 6, Apr 2021. (ISSN: 1099-1131) (**Impact Factor – 2.047, John wiley**)
- Nagabushanam, P., Thomas George, S., **Radha, S.**, “EEG signal classification using LSTM and improved neural network algorithms”, Soft Comput 24, 9981–10003, July 2020. (ISSN: 1433-7479) (**Impact Factor – 3.643, Springer**)

- Nagabushanam, P., George , S. T., S. Raveena Judie Dolly, D., **Radha, S.***, “Artifact Cleaning of Motor Imagery EEG by Statistical Features Extraction using wavelet Families", International Journal of Circuit Theory and Applications, 2219-2241, Vol 48, Issue 12, 2020. (ISSN: 1097-007X) (**Impact Factor – 2.038, John wiley**)
- Vimukth; sam, shylu; **Radha, S.**; Paul, P.Sam; Samuel, Joel, “Design of a power efficient Kogge-Stone Adder by exploring new OR gate in 45nm CMOS Process" Circuit World, Mar 2020.(ISSN: 0305-6120) (**Impact Factor – 0.875, Emerald**)
- **Radha, S. ***, Shylu, D.S., Nagabushanam, P., “Power efficient low latency architecture for decoder: Breaking the ACS bottleneck”, International Journal of Circuit Theory and Applications, 1513–1528, Vol 47, Issue 9, 2019.(ISSN: 1097-007X) (**Impact Factor – 2.038, John wiley**)

Scopus Indexed Publications:

- **Radha, S.**, G.Josemin Bala, E. Grace Mary Kanaga, Nagabushanam, P. *, “Scheduling and Adaptive Listening Approaches in MAC for WSN Applications – A Survey”, Journal of High Speed Networks, vol. 26, no. 4, pp. 325-338, 2020 (ISSN: 1875-8940), **IOS press**.(Web of Science Journal- without Impact Factor)
- **Radha, S. ***, Shylu, D.S., Nagabushanam, P., Mathew, J., “Low noise amplifier with resistive and capacitive feedback for 2.4 GHz RF receiver front end”, Journal of High Speed Networks, 25(2), pp. 181-203, Jun 2019.(ISSN: 1875-8940), **IOS press**. (Web of ScienceJournal-without IF)
- Shylu Sam, D.S., **Radha, S.**, Jackuline Moni, D., Sam Paul, P., Jecintha, J., “Design of 1-V, 12-bit low power incremental delta sigma ADC for CMOS image sensor applications”, International Journal of Recent Technology and Engineering, 7(5), pp. 249-254, Feb 2019.
- **S. RADHA**, D.S.SHYLU, P.NAGABUSHANAM, J.SUNITHA KUMARI, “Design of RF LNA with resistive feedback and gain peaking for multi-standard application”, International Journal of Recent Technology and Engineering, pp 100-107 Volume 7 Issue 4, Nov 2018. (**Blue Eyes**)

Non-Scopus Publications/ UGC Listed Journals:

- Nagabushanam, P., George , S. T., S. Raveena Judie Dolly, D., **Radha, S.***, (2020). Convolution Neural Network Architectures for Motor Imagery EEG Signal Classification. International Journal of Artificial Intelligence and Machine Learning, Volume 11, Issue 1, 15-22, 2021, Jan (**IGI Global Publisher**). DOI: [10.4018/IJAIML.2021010102](https://doi.org/10.4018/IJAIML.2021010102)
- Nagabushanam, P., George, S. T., Subathra M S P., & **Radha, S. *** (2020) 1D CNN Architectures for EEG Classification with Motor Imagery input of eyes open and eyes closed conditions. Int. J. of Intelligence and Sustainable Computing. (**Inderscience Publisher**). DOI: [10.1504/IJISC.2020.10032562](https://doi.org/10.1504/IJISC.2020.10032562)
- **S. Radha**, P. Nagabushanam, “Survey on Networking architectures and protocols for green smart environment” Published in International Journal of Advanced Computer Technology (**IJACT**), ISSN: 2319-7900 Vol 3 Issue1 on 25 Feb 2014.

Reviewer in Journals:

1. Engineering Optimization (Sci indexed Journal, Impact Factor- 3.230, Taylor & Francis)

2. Frontiers in Neuroinformatics (Sci Indexed Journal, Impact Factor – 4.081)- ku email
3. Automatika: Journal for Control, Measurement, Electronics, Computing and Communications (Sci Indexed Journal, Impact Factor – 1.156, Taylor&Francis journal pub)- ku email
4. Journal of Circuits, Systems, and Computers (Sci Indexed Journal, Impact Factor – 1.333, world scientific journal pub)
5. Circuit World (Sci Indexed Journal, Impact Factor – 0.875, Emerald Pub.)
6. Transactions on Emerging Telecommunications Technologies (Sci Indexed Journal, Impact Factor – 1.258, John Wiley)
7. International Journal of Intelligent Information Technologies (IJIT) – Scopus Indexed
8. Intelligent Decision Technologies: an International Journal – Scopus Indexed
9. International Journal of Sensors, Wireless Communications and Control (SWCC)-Scopus Indexed
10. International Journal of Artificial Intelligence and Machine Learning (IJAIML)-Non Scopus Indexed

radha SUBRAMANYAM - Youtube Channel (Educational)

Contributed educational e-content in the link below

Link: <https://www.youtube.com/channel/UCrcsTiUiXhJ2X98tJQKqiQ> (click on **videos** button to view all the uploaded videos)

Youtube channel Status as on 18.04.2022 (click on **about** button to know the below details)

Number of Videos uploaded: 411

Number of Subscribers: 117

Number of views: 21,342

Logo:



Few Sample Videos - Link in youtube:

Subject	Topic	Youtube Links
Signals & Systems	Discrete time fourier series (DTFS)	https://www.youtube.com/watch?v=V3klIY_vdqQ&t=27s
Signals & Systems	GATE exam problems on Laplace transform	https://www.youtube.com/watch?v=URGIL6DTRY&t=28s
General Aptitude	Boats and streams	https://www.youtube.com/watch?v=iozbAbBscz8&t=41s

General Aptitude	Pipes and cisterns	https://www.youtube.com/watch?v=U8jlkBVnY_Y&t=30s
VLSI Design Lab	Layout of NAND GATE in Microwind software	https://www.youtube.com/watch?v=UV4lp3tQuVU&t=65s
VLSI Design Lab in LT Spice Software	3 bit Asynchronous Up Counter in LT Spice software	https://www.youtube.com/watch?v=nU-d0q-NcxU&t=4s
Digital Electronics	GATE exam Coaching – Problems on flipflops	https://www.youtube.com/watch?v=ilUmPbgBizU&t=7s
Digital Electronics	IES exam Coaching – Problems on flipflops	https://www.youtube.com/watch?v=jGyN7wNGs8Q

AICTE - ATAL Courses

S.No	Organizer	Title
1	ATAL Academy – IIT, Goa	Modulation, coding and multiple access techniques for wireless communication and storage systems 15 th Nov to 19 th Nov 2021
2	ATAL Academy – IIT, Goa	Signal Processing for 5G- Algorithms and Implementation 9 th Aug to 13 th Aug 2021
3	ATAL Academy - North-Eastern Hill University, Shillong	Sensors Technology 8 th Feb 2021 to 12 th Feb 2021
4	ATAL Academy - LBS College of Engineering, Kasaragod, Kerala	Internet of Things (IoT) 11 th Jan 2021 to 15 th Jan 2021
5	ATAL Academy - Thiagarajar Polytechnic college, Salem, Tamilnadu	Sensors Technology 7 th Dec 2020 to 11 th Dec 2020

Resource Person in:

1. **Subject Expert Category** - Incharge for preparing questions of general aptitude for **Karunya Entrance Examination (KEE) 2022** conducted by Karunya University, Coimbatore, Tamilnadu in May 2022.
2. Training Programme “Machine Learning and its applications using python programming (MLAPP 2020)” conducted by karunya university from 27th May 2020 to 6th Jun 2020.
3. **Reviewer** in **ICSPPC 2021** Conference conducted on 13th, 14th May 2021 in KITS, Coimbatore.
4. **Subject Expert Category** - Incharge for preparing questions of general aptitude for **Karunya Entrance Examination (KEE) 2021** conducted by Karunya University, Coimbatore, Tamilnadu in June 2021
5. **Reviewer** in **ICCDF 2021** Conference conducted on 3rd, 4th Apr 2021 in Northcap University, Gurugram.

Consultancy:

- 1. Subject Expert Category** - Prepared Question paper (Control systems subject) and submitted for AICTE Initiated - Student Learning Assessment (SLA) programme. Submitted 45 MCQ questions- Questions passed through plagiarism check and Blooms Taxonomy rules by AICTE. **Rs.200 will be paid for each successful question (Received Renumeration of Rs. 7215/- from AICTE ASLAP Project) – Feb to Apr 2021**
- 2. Subject Expert Category** - Prepared Question paper (wireless communications) and submitted for AICTE Initiated - Student Learning Assessment (SLA) programme. Submitted **54 MCQ questions**- All the questions passed through plagiarism check and Blooms Taxonomy rules by AICTE. Rs.200 will be paid for each successful question (Received Renumeration of **Rs. 9200/- from AICTE ASLAP Project**)– **May 2021**
- 3. Subject Expert Category** - Prepared Question paper (Analog and digital communication) and submitted for AICTE Initiated - Student Learning Assessment (SLA) programme. Submitted **21 MCQ questions**- All the questions passed through plagiarism check and Blooms Taxonomy rules by AICTE. Rs.200 will be paid for each successful question (Received Renumeration of **Rs. 4200/- from AICTE ASLAP Project**) – **Jun 2021**
- 4. Subject Expert Category** - Prepared Question paper (control systems) and submitted for AICTE Initiated - Student Learning Assessment (SLA) programme. Submitted **11 MCQ questions**- All the questions passed through plagiarism check and Blooms Taxonomy rules by AICTE. Rs.200 will be paid for each successful question (Received Renumeration of **Rs. 2200/- from AICTE ASLAP Project**) – **Jul 2021**

Seminars/Workshops/Conferences/Training Programmes attended:

S.No	Organizer	Type	Title
1.	Intellectual Property Office, India	IP awareness Training Program	National Intellectual Property awareness Mission (NIPAM) on 12 th Feb 2022
2.	Cadence Training course	Certified Badge exam	Behavioral modelling with Verilog Feb 2022
3.	SNM Institute of Management and Technology in association with Pantech E-Learning	webinar	Wireless Sensor Network Design and Validation 14 th Jun 2021
4.	GVR & S College of Engineering and Technology in association with Pantech E-Learning	webinar	IoT & Block Chain 11 th Jun 2021
5.	Kumaraguru College of Technology in association with Pantech E-Learning	webinar	Green Wireless Communication 1 st Jun 2021
6.	PET Engineering college in association with Pantech E-Learning	webinar	Antenna 31 st May 2021

7.	Pantech Prolabs India Pvt Ltd	5 days workshop	Image Processing using Matlab 12 th Apr 2021 to 16 th Apr 2021
8.	APSSDC (Andhra Pradesh State Skill Development Corporation) and Pantech Prolabs	30 days Master Class	30 days Master Class – Matlab Simulink 23 rd Nov 2020 to 22 Dec 2020
9.	IETE Mumbai and Pantech Prolabs India Pvt Ltd	Webinar series – 30 days	Matlab Master Class 2020 1 st Sep 2020 to 30 th Sep 2020
10.	Dr. M.G.R. Educational and Research Institute, Maduravoyal, Chennai	One day National level Webinar	I-STEM-A Gateway for Researchers stepping towards AATM NIRBHAR BHARAT 25 th Aug 2020
11.	Dayalbagh Educational Institute & STATCRAFT, Bangalore	Certificate Program	Data Analysis using STATCRAFT - 17 th August 2020
12.	Dr. M.G.R. Educational and Research Institute, Maduravoyal, Chennai	Two day National Level E-Workshop	Performing Statistical Data Analysis using MS EXCEL 18 th & 19 th Aug 2020
13.	Narasimha Reddy Engineering College, Hyderabad	Webinar	Revised Blooms Taxonomy-A Refresher Course 16 th Aug 2020
14.	Manipur University, Jaipur and IIDE (Indian Institute of Digital Education)	Webinar	Online Teaching & E-Content Development 10th and 11th August, 2020
15.	Adikavi Nannaya University, Rajamahendravaram, A.P. (Skill Development Center)	Webinar	Online Teaching Learning Methods 30 Jul 2020
16.	Kalasalingam Academy of Research and Education (KARE ACM Student Chapter)	Webinar	How to write a good research paper 27 th Jul 2020
17.			Exploring the nuam
18.	AIMIT ST. Aloysius college, Mangalore & Pantech Solutions	Webinar	Image Processing Applications 3 rd Jul 2020
19.	Dr. Shyama Prasad Mukherjee University & Pantech Solutions	Webinar	Internet of Things Using Aurdino 16Jun2020
20.	Adamas University & Pantech Solutions	Webinar	Application Design Of Brain Computer Interface (BCI) 13 th Jun 2020
21.	IETE Mumbai & Pantech Solutions	Webinar	Development of Deep Learning Architecture 25 May 2020
22.	IETE Mumbai & Pantech Solutions	Webinar	Future of AI 20 May 2020
23.	Karunya Institute of Technology and Sciences & IEEE EDS Chapter , Coimbatore	Distinguished Lecture	Advanced III-N Devices for 5G and Beyond 19 May 2020
24.	Kongu Engineering College, Erode	Hands on Workshop in Association with IIP Cell	Fundamentals of Machine Learning for Image Processing using PYTHON 30.8.19 to 31.8.19
25.	Mepco engg college , Virudhunagar, TN	5 days	Deep learning Techniques in healthcare applications 13-17 th may 2019
26.	Electronics & ICT Academy in collaboration with IIT, Guwahati	One-week Faculty Development Programme (Conducted in PSG Tech, CBE)	VLSI Design at Deep Submicron Node 4 th Feb to 8 th Feb 2019.
27.	Karunya Institute of Technology	Two days National	National Workshop On Device Modeling

	and Sciences, Coimbatore	Workshop	using Synopsys Sentaurus TCAD – 12 th & 13 th Nov 2018
28.	Kongu Engineering College, Erode	Two days National Workshop	Investigations on Morphological Feature Extraction of Organs from MR Images for Detection and Classification of Brain Tumors” 30.10.2018 & 31.10.2018
29.	Karunya Institute of Technology and Sciences, Coimbatore	Training programme - NITTR	Research Paper Writing 16 th May to 18 th May 2018
30.	Karunya Institute of Technology and Sciences, Coimbatore	National Conference	Participation and Role of Women in Water, Sanitation and Hygiene 27 th Mar 2018
31.	Karunya Institute of Technology and Sciences, Coimbatore	Two Days Workshop CoreEL Technologies, Bangalore	IC Design and Implementation using Mentor Graphics EDA Tool 1 st and 2 nd Mar 2018
32.	Karunya Institute of Technology and Sciences, Coimbatore	5 Days Short Term Certified Course	Fundamentals of Digital Design & Verification (Bridging concepts to practice) using CADENCE TOOL FLOW 29 th Jan 2018 -2 nd Feb. 2018
33.	Kongu Engineering College, Erode	One day National Workshop	Neural Networks in Engineering Applications using MATLAB 24 th Jan 2018
34.	Karunya Institute of Technology and Sciences, Coimbatore	5 Days Short Term Certified Course	Analog IC Design-Circuit and Layout Design Methodologies using CADENCE ANALOG DESIGN FLOW 11 th -15 th December 2017
35.	Kumaraguru College of Technology, Coimbatore	Three days IEEE ES sponsored Short Term Training Programme	Model based VLSI System Design using Matlab Simulink 29 th Nov – 1 st Dec 2017
36.	Faculty Development Club, Karunya Institute of Technology and Sciences, Coimbatore	Faculty Development Programme	Faculty Development Programme on “Professional Ethics and Integrity” 18 th Nov 2017
37.	Kongu Engineering College, Erode	National Level Workshop	Computational Methods in Bayesian Statistics and Machine Learning -Monte Carlo for Statistical Inference Using R 6 th – 7 th Oct 2017
38.	Karunya University, Coimbatore	National Workshop	Application of Graph theory in Engineering 28 th Apr 2017
39.	Karunya University, Coimbatore	Poster Presentation	Colloquium on Research Practices 8 th Apr 2017
40.	Karunya University, Coimbatore	One day Workshop	Lecture capturing By Dove Apr 2017
41.	Karunya University, Coimbatore	One day Workshop	Workshop on Biological Signal Processing WBSP’17 24th Feb 2017
42.	Kongu Engineering College, Erode	Three days Workshop	Hands on Training Programme on Network Simulator (NS2) 5 th to 7 th Aug 2016
43.	Kongu Engineering College, Erode	Two days Workshop	Workshop on Network Simulators NS2 and NS3 2 nd to 3 rd Sep 2016
44.	Karunya University, Coimbatore	One Day Seminar	Recent Advances in Antenna Technologies & Applications 8 th Jul 2016
45.	Karunya University, Coimbatore	Faculty Development Programme (FDP)	Effective Teaching Learning Methodology for Engineers

			23 rd Jun 2016
46.	SSN College of Engineering, Chennai	7 days - STTP	VLSI for Wireless Communication System Design 13 th to 19 th June 2016
47.	Karunya University	National Workshop	ANALOG AND MIXED SIGNAL DESIGN USING CADENCE TOOL 7 th & 8 th Apr 2016.
48.	Kongu Engineering College, Erode	IETE Workshop	Open Hardware for Biomedical Signal Acquisition on Android Mobile Phone with Real Time Filtering and Signal Processing using MATLAB 9 th Mar 2016
49.	Karunya University, Coimbatore	Two days Workshop	Computer Vision and Image Analysis - 12-13 Feb 2016
50.	Kongu Engineering College, Erode	One day Hands on Training programme	LATEX (Art of Research Paper Publication) – 6 th Feb 2016
51.	Kongu Engineering College, Erode	One day National Seminar	Writing Effective Research Papers for SCI journals and Preparing Project Proposals – 8 th Jan 2016
52.	Kongu Engineering College, Erode	DBT Sponsored National Seminar	Analysis of Signal and Image Processing Techniques for the Diagnosis of Neuro and Cardiac Abnormalities – 16-17 oct 2015
53.	Rathinam College of Arts and Science, Coimbatore	One day National Level workshop	Network Simulator(NS2) NLWNS - 29 th Aug 2015
54.	PSG College of Technology IETE Students Chapter	One day workshop	Wireless sensor Network-A cognitive Perspective using NetSim 22 nd Aug 2015
55.	Karunya University & Texas Instrument & Starcom Information Technology Limited	AnalogMaker Competition	Analog Maker Competition 16.02.2015 – 14.03.2015
56.	Karunya University, Coimbatore	IEEE Step Sponsored National Seminar	Sustainable Solid Waste Landfill Management 12 th Dec 2014
57.	Vidyanikethan Engg College, Tirupathi	Two Day FDP under TEQIP-II (Technical Education Quality Improvement Programme)	FDP on “Emerging Trends in wireless Networks” 29 th Mar – 30 th Mar 2014
58.	Vidyanikethan Engg College, Tirupathi	Two Day FDP under TEQIP-II (Technical Education Quality Improvement Programme)	FDP on Biomedical Signal Processing 21 st Jun – 22 nd Jun 2013
59.	IIT Kharagpur in 200 centers My Center-PSG Tech, Coimbatore	Two Week National workshop	Workshop on Analog Electronics 4 th Jun – 14 th Jun 2013
60.	Karunya University, CBE	One day National Workshop	ASIC-System on Chip Design Methodologies 2 nd Feb 2013
61.	Stairway Engineering PVT.LTD	2 days Hands on Training	AVR Microcontroller Programming and Interfacing 13-14 Dec 2012
62.	ETI NSS – Avinashilingam University, Coimbatore	One Week Orientation Training by Empanelled Training Institution-NSS	NSS Orientation Training 05 – 11 Sep 2012
63.	Guru Nanak Engineering	International	“Emerging Trends in signal processing

	college, Hyd	Conference tutorial	and VLSI Design” 11-13 June 2010
64.	Vardhaman College of Engineering, Shamshabad, Hyd	Two week Short term Training Programme	VLSI Design Methodologies & Testing Principles 24Aug-4Sep 2009
65.	RRS College of Engineering and Technology, Medak(Dt), A.P.	National Level Refresher Course	Electronic Devices and Circuits 16-17 sep 2008
66.	TKRCET, Hyd	Two Day National Level workshop	Embedded Systems Design 4-5 Jan2008
67.	JNTU, HYD	Train the Trainer (One Week)	Soft Skills Train the Trainer 23-27 july 2007

Seminars/Workshops/Conferences/Training Programmes Organized :

S.No	Organizer	Type	Title
1	Karunya University, Coimbatore	Online mode	Training on “An effective project report preparation” 6 th - 7 th Apr 2022
2	Karunya University, Coimbatore	Hybrid mode session [offline-classroom and via gmeet]	orientation program on "GATE (Graduate Aptitude Test for Engineering) Exam" on 8 th Mar 2022
3	Karunya University, Coimbatore	Online Quiz Competition	National Science Day 2022 - Online Quiz Competition on 28 th Feb 2022
4	Karunya University, Coimbatore	2 days FDP	Digital and analog design flow in cadence 2 nd – 3 rd Dec 2021
5	Karunya University, Coimbatore	Webinar	Awareness program - Opportunities After Engineering Through GATE & ESE on 29.09.2021
6	Karunya University, Coimbatore	Webinar	Opportunities after Engineering through GATE & ESE 18 th Mar 2021
7	Karunya University, Coimbatore	Webinar	Career Guidance – Lecture Series 23 and 30 May 2020
8	Karunya University, Coimbatore	One day workshop	One day workshop on Data Analysis using R Software 11 th Mar 2019
9	Karunya Institute of Technology and Sciences, Coimbatore	Half a day workshop	Orientation Program for GATE EXAM 7 th Mar 2019
10	Karunya Institute of Technology and Sciences, Coimbatore	Two days National Workshop	National Workshop On Device Modeling using Synopsys Sentaurus TCAD – 12 th & 13 th Nov 2018
11	Karunya Institute of Technology and Sciences, Coimbatore	One day workshop	Implementation of Digital Logic Design in FPGA Kit 18.9.2018
12	Karunya Institute of Technology and Sciences, Coimbatore	Half a day workshop	Orientation Program for GATE EXAM 12 th Jul 2018
13	Karunya University, Coimbatore	One day workshop	One day workshop on Data Analysis using R Software 15 th Mar 2018
14	Karunya University, Coimbatore	Two days workshop	Two days workshop on VLSI chip design & verification using Mentor Graphics EDA tool 1 st

			– 2 nd Mar 2018
15	Karunya University, Coimbatore	5 Days Short Term Certified Course	Fundamentals of Digital Design & Verification (Bridging concepts to practice) using CADENCE TOOL FLOW 29 th Jan 2018 -2 nd Feb 2018
16	Karunya University, Coimbatore	5 Days Short Term Certified Course	Analog IC Design-Circuit and Layout Design Methodologies using CADENCE ANALOG DESIGN FLOW 11 th -15 th December 2017
17	Karunya University	National Workshop	ANALOG AND MIXED SIGNAL DESIGN USING CADENCE TOOL 7 th & 8 th Apr 2016.

M.Tech / PG Projects Guided:

Details of Post-Graduate (ME/M.Tech) Scholars Guided						
Sl. No.	Students Name	Date of Viva-voce	Joining Date	Thesis Title	Software used	Any Outcome/ Recognition of significance
1	Rajesh	May 2021	Dec 2020	Galios Field Multiplier using Verilog	Open EDA tool	completed
2	R. Nancy	May 2019	Dec 2018	PSO Optimization for WSN using NS2	NS2	Conference Publication
3	P. Manoj	May 2018	Dec 2017	Multi-channel MAC for WSN Using NS2	NS2	Conference Publication
4	Jisha Mathew	May 2017	Dec 2016	Linearization Op-Amps using Cadence	Cadence	Conference Publication
5	Maldona Sahaai	May 2016	Dec 2015	Multiplier Design using VHDL	Cadence	Conference Publication
6	Siva Karthikeyan	May	Dec 2013	Protocols for WSN using NS2	NS2	Conference Publication

B.Tech Projects Guided

S. No	Name of the Project	Completion	Tool Used
1	Security based Electronic Voting Machine using Xilinx tool	Nov 2021	Xilinx, VHDL
2	64-bit Multi-match Priority Encoder (MPE64) for high performance VLSI architectures	Nov 2021	Xilinx, VHDL
3	Analysis of diabetes patients using ML and DL classification	May 2021	Python,

	algorithm		anaconda
4	Energy efficient co-operative MAC for single-hop and multi-hop wireless networks	May 2021	NS2
5	Artifact removal using elliptic filter and classification using 1D-CNN for EEG signals	March 2020	Matlab, Python anaconda
6	Power optimization in MAC protocols for WSN	April 2019	NS2
7	Transformed Coupled Injection Based Differential VCO using 180nm Technology in Cadence Tool	April 2019	Cadence
8	Scp protocol for MAC in WSN	April 2019	NS2
9	Cluster based routing protocol for underwater sensor networks	April 2017	NS2
10	Packet size optimization in MAC protocol for underwater wireless sensor networks	April 2017	NS2
11	Transmission Distance in MAC protocol for Wireless Sensor Networks	April 2016	NS2 (C++)
12	Gabor Filter based Tonsillitis Analysis using VHDL	April 2016	VHDL, Xilinx
13	Six DOF simulation for saclos guidance using matlab simulation	April 2016	Matlab
14	Analysis of attacks on AODV routing protocol in MANET	April 2016	NS2
15	An efficient denoising method for removal of impulse noise	April 2015	Matlab
16	Design and Implementation of FIR Band Pass Filter in virtex-4 FPGA		MatLab

Paper Presentations in National/International conferences and conference publications:

(National Conf-12; International Conf-28; **Scopus Indexed conference publications - 21**)

- Reddy, G. S., **Radha, S.**, Taufiq, K. T., Reddy, K. D. S., Reddy, K. P. K., & Nagabushanam, P. (2022, January). Security based Electronic Voting Machine using Xilinx tool. In 2022 2nd International Conference on Power Electronics & IoT Applications in Renewable Energy and its Control (PARC) (pp. 1-4). IEEE.
- Pradeep, A., **Radha, S.**, Sujay, N., Ram, P. V. J., Ganesh, G. V., & Nagabushanam, P. (2022, January). 64-bit Multi-match Priority Encoder (MPE64) for High Performance VLSI Architectures. In 2022 2nd International Conference on Power Electronics & IoT Applications in Renewable Energy and its Control (PARC) (pp. 1-5). IEEE.
- J Joe Abinas, Hari Venkata Krishna Chandolu, P. Nagabushanam, **S. Radha**, Vempalli Murali Krishna, “Analysis of Diabetes Patients using Classification algorithms”, presented in 2021 10th IEEE International Conference on Communication Systems and Network Technologies (CSNT), 18th to 19th Jun 2021 in Bhopal, India.

- **S. Radha**, Basti Rishitha Shalini, Suram Anagalakshmi, Vundela Rajitha Reddy, P. Nagabushanam, “SYNCHRONOUS DUTY CYCLE IN MAC PROTOCOL FOR WIRELESS APPLICATIONS”, presented in 3rd International Conference on Signal Processing and Communication (ICPSC), 13th to 14th May 2021 in Karunya University, CBE.
- P. Nagabushanam, Neema C Jayan, C. Antony Joel, **S. Radha**, “CNN Architecture for Diabetes Classification”, presented in 3rd International Conference on Signal Processing and Communication (ICPSC), 13th to 14th May 2021 in Karunya University, CBE.
- P. Nagabushanam, S. Thomas George, Praharsha Davu, P. Bincy, Meghana Naidu, **S. Radha***, “Artifact Removal using Elliptic Filter and Classification using 1D-CNN for EEG signals”, presented in **IEEE Sponsored** – International Conference on Advanced Computing & Communication Systems (ICACCS 2020) organized by Sri Eshwar college of Engineering , CBE on 6th & 7th Mar 2020.
- **S. Radha**, G. JoseminBala, S. Priya Lakshmi Khannaa, J. Reshma, PMP. Chrisstoja, P. Nagabushanam*, “Contention window adjustment in MAC Protocol for WSN Applications”, presented in **IEEE Sponsored** – International Conference on Advanced Computing & Communication Systems (ICACCS 2020) organized by Sri Eshwar college of Engineering, CBE on 6th & 7th Mar 2020.
- **S. Radha**, Gadde Krishna, Kurra Megha Sai Sumanth, Yangareddy Sasidhar Reddy, Kondala Varun Kumar Reddy, P. Nagabushanam*, “Double Balanced Mixer with noise reduction for RF Receiver using Cadence 180nm Technology”, presented in **IEEE Sponsored** – International Conference on Advanced Computing & Communication Systems (ICACCS 2020) organized by Sri Eshwar college of Engineering , CBE on 6th & 7th Mar 2020.
- **S.Radha**, G.Josemin Bala, Stephy. V, Dominic Mary Princy, Divya. J, Sherene Johny Rithinika. P, “POWER OPTIMIZATION IN MAC PROTOCOLS FOR WSN”, 2nd IEEE International Conference on Signal Processing and Communication (ICSPC'19), 29th to 30th MARCH 2019 in Karunya University, CBE.
- **S.Radha**, D.S. Shylu, Sanchana Priya M.S., Christobel Dianna, Aishwarya T, Hepziba S Selvamani , “Transformer Coupled Voltage Controlled Oscillator Using 180nm Technology in Cadence Tool”, 2nd IEEE International Conference on Signal Processing and Communication (ICSPC'19), 29th to 30th MARCH 2019 in Karunya University, CBE.
- D.S.Shylu, **S.Radha**, P.Sam Paul, Parakati Sarah Sudeepa, “Design of low power 4-bit Flash ADC in 90nm CMOS Process”, 2nd IEEE International Conference on Signal Processing and Communication (ICSPC'19), 29th to 30th MARCH 2019 in Karunya University, CBE.
- **S. Radha**, Wilson Vineetha, Darshana. S, G.Josemin Bala, P. Nagabushanam, “ScP Protocol for Wireless Sensor Networks” presented in **IEEE Sponsored** – International Conference on

Advanced Computing & Communication Systems (ICACCS 2019) organized by Sri Eshwar college of Engineering , CBE on 15th & 16th Mar 2019.

- P. Nagabushanam, Praharsha Davu, S. Thomas George, **S. Radha**, “Morphological operations on EEG Signals for Spike Detection” presented in **IEEE Sponsored** – International Conference on Advanced Computing & Communication Systems (ICACCS 2019) organized by Sri Eshwar college of Engineering , CBE on 15th & 16th Mar 2019.
- **S. Radha**, G. Josemin Bala, P. Nagabushanam “Multilayer MAC with Adaptive listening for WSN” presented in **IEEE Sponsored** - International 3rd International Conference on Inventive Systems and Control (ICISC 2019) organized by JCT college of Engineering , CBE on 10th & 11th Jan 2019.
- P. Nagabushanam, S. Thomas George, D.S. Shylu, **S. Radha** “GABOR FILTER BASED TONSILLITIS ANALYSIS USING VHDL” presented in **Springer** - International Conference on Artificial Intelligence, Smart Grid and Smart City Applications (AISGSC 2019) organized by PSG college of Technology , CBE on 3rd to 5th Jan 2019- Book chapter edition: 2020
 - ISBN 978-3-030-24050-9 ISBN 978-3-030-24051-6 (eBook)
 - <https://doi.org/10.1007/978-3-030-24051-6>
- **S.Radha**, Nancy.R, Aiswarya Rajendran, Kunkanuru Supriya “Multi Layer MAC with Adaptive Listening for Wireless Sensor Network” presented in **IEEE Sponsored** - International 3rd International Conference on Engineering and Technology (ICETECH 2018) organized by Rathinam Technical Campus , CBE on 30th & 31st August 2018.
- **S.Radha**, P.Nagabushanam, RB Hari Krishna, T.Samuel Franklin “Transformed Coupled Injection Based Differential VCO using 180nm Technology in Cadence Tool” presented in **IEEE Sponsored** - International 3rd International Conference on Engineering and Technology (ICETECH 2018) organized by Rathinam Technical Campus , CBE on 30th & 31st August 2018.
- **S.Radha**, P.Nagabushanam, K.Nikhil, Bitty Jose, Litty Jose, “High Speed Op-Amp for Video Applications” presented in **IEEE Sponsored** - International Conference on Intelligent Computing and Control Systems (ICICCS 2018) organized by Vaigai College of Engineering, Madurai on 14th & 15th Jun 2018.
- P.Nagabushanam, **S.Radha**, Sushmita Selvadass, Kezia Kanishka Joseph, “Gabor filter based Image segmentation for Disease Detection using VHDL” presented in 2nd International Conference on Inventive Communication and Computational Technologies (ICICCT 2018)organized by Rangannath Engineering College, Coimbatore on 20th & 21st Apr 2018.

- **S.Radha**, P.Nagabushanam, Thanam Jaideep Reddy, B.Sachin, “Transmission Distance in MAC protocol for Wireless Sensor Networks” presented in 2nd International Conference on Inventive Communication and Computational Technologies (ICICCT 2018) organized by Rangannath Engineering College, Coimbatore on 20th & 21st Apr 2018.
- **S.Radha**, RB Hari Krishna, N.Paramasiva Pandi, Sneha Varghese, P.Nagabushanam, “Floor Planning of 16 bit Counter design for health care applications Using 180nm Technology in Cadence Tool” presented in 2nd International conference on Electronics, Communication and Aerospace Technology (ICECA 2018) organized by RVS Technical Campus, Coimbatore on 29th & 30th Mar 2018.
- **S.Radha**, G.Josemin Bala, “Multichannel MAC for Wireless Sensor Network applications” presented in 3rd International Conference on Communication and Electronics Systems (ICCES) organized by RVS Technical Campus, Coimbatore on 15-16 Oct. 2018.
- **S.Radha**, G.Josemin Bala, “Flooding in Static and Free Deployment for Underwater Wireless Sensor Networks” IEEE International Conference on Signal Processing and Communication (ICSPPC’17) on 28th and 29th Jul 2017 in Karunya University, CBE.
- P.Nagabushanam, S.Thomas George, **S.RADHA**, “Brain Disorders by Analyzing EEG Signals – A Review” presented in 2017 **IOSRD - International Conference - ICDSME 2017** organized by International Organization of Scientific Research and Development, Coimbatore on 14th & 15th Apr 2017.
- Junias M, **S. Radha**, “Power Aware Dynamic Ram Using Clock Gating Techniques” National Conference on Innovations in Future Communication Technologies - March 30-31 2017.
- Lavanya A, **S. Radha**, “Power delay product optimization in Wallace tree multiplier” National Conference on Innovations in Future Communication Technologies - March 30-31 2017.
- Rohit.S Aashish Nelson, **S.Radha**, “Implementation Of Flooding Based Routing In Static And Mobile Nodes For Underwater Wireless Communications” National Conference on Innovations in Future Communication Technologies - March 30-31 2017.
- Reshma Shibi, Ashlin George Mathew, **S. Radha**, “Energy Efficient Routing in Underwater Wireless Sensor Network by Static Node Deployment “ presented in 4th National Conference on Computational and Inter-Networking Information Technology - **NCIIT’17**, organized by Anna University, Coimbatore on 30th & 31st March 2017.
- Vinita Sara Jacob, Daniya Dthomas, **S. Radha**, “Medium Access Control Protocol for Underwater Wireless Sensor Networks “ presented in 4th National Conference on Computational and Inter-Networking Information Technology - **NCIIT’17**, organized by Anna University, Coimbatore on 30th & 31st March 2017.

- P. M. Prem Thomas, R. Rishanth, **S.Radha**, “Implementation of Flooding in Protocols for Underwater Wireless Sensor Network “ presented in 9th National Conference(**IEEE Sponsored**) on Signal Processing, Communication and VLSI Design (NCSCV’ 17) , organized by Anna University, Coimbatore on 24th & 25th March 2017.
- **S.Radha**, Raechel Alexander, Jeenu John, “Energy Model of Reactive Protocols for Underwater Wireless Sensor Networks “ presented in 9th National Conference(**IEEE Sponsored**) on Signal Processing, Communication and VLSI Design (NCSCV’ 17) , organized by Anna University, Coimbatore on 24th & 25th March 2017.
- **S.Radha**, Nova Ann Jacob, Ann Shettilda S, “PACKET SIZE OPTIMIZATION IN PROTOCOLS FOR UNDERWATER WIRELESS SENSOR NETWORK “ presented in 9th National Conference (**IEEE Sponsored**) on Signal Processing, Communication and VLSI Design (NCSCV’ 17) , organized by Anna University, Coimbatore on 24th & 25th March 2017.
- **S.RADHA**, Jisha Mathew, “Linearization of Low Noise Amplifier for Wireless Sensor Networks” presented in 2017 **IEEE** sponsored **International Conference - ICISC 2017** organized by JCT College of Engineering and Technology, Coimbatore on 19th & 20th Jan 2017.
- Madona Sahaai, **S.RADHA**, “High Speed Energy Efficient Cell based Adder for VLSI Design” presented in 2016 **IEEE** sponsored **International Conference - ICETECH 2016** organized by Rathinam Technical Campus, Coimbatore on 17th & 18th Mar 2016.
- **S.RADHA**, G. Josemin Bala, “Reactive Protocols for Underwater Wireless Sensor Networks” presented in National Conference **NCACM’16** organized by Anna University, Regional Center, Coimbatore on 15th Mar 2016.
- K. Siva Karthikeyan, **S.RADHA**, “Design of Energy Efficient DMM-MAC Routing Protocol for Underwater Sensor Networks” Presented in **International Conference ICE²-RTRI 2015** Organized by Electrical Sciences Dept, Sri Krishna College of Technology, and Coimbatore on 2nd, 3rd January 2015.
- **S.RADHA**, G. Josemin Bala, “Survey on Underwater Acoustic Wireless Communication” Presented in National Conference **NCSCV’14** Organized by ECE Dept, Anna University Regional Center, Coimbatore on 9,10th May 2014.
- **S.RADHA**, “Implementation of Vedic Multiplier with Reduced Power Consumption” Presented in National Conference **NEPECC’14** Organized by EEE Dept, Karunya University, Coimbatore, TN on 3rd & 4th Apr 2014.

- **S.RADHA**, “Parallel Add-Compare-Select Unit Structure for high throughput trellis Decoder” Presented in **International conference ICSIE** Organized by Jawahar Engineering College, Chennai, TN on 6th Apr 2013.
- **S.RADHA**, “Improved Pipeline for Add-Compare-Select Unit Structure of Viterbi Decoder” Presented in **International conference ICAET** Organized by Dr.MGR Educational and Research Institute University, Chennai, TN on 5th & 6th Apr 2013.
- **S.RADHA**, “Power Efficient Low Latency Architecture for Decoder:Breaking The ACS-Bottleneck” Presented in **International conference ICHIOS** Organized by E&I Dept, Karunya University, Coimbatore, TN on 1st & 2nd Mar 2013.
- **S.RADHA**, “A High Speed Low Power Pipelined Decoder –Breaking Through ACS Bottleneck” (2010 IEEE Paper Implemented) Presented in National Conference **NCECS** Organized by Vanjari Seethaiah College, Patancheru, Hyd on 12-13 Oct 2011.
- The above Paper is published in the conference Proceedings Souvenir.
- **S.RADHA**, “Physical challenges on nano-CMOS” Presented in National Paper Presentation **PRATHIBHA 2K12** Organized by TKRCET on 14Mar 2012
- **S.RADHA**, “BAKER ADC” Presented in National Paper Presentation **PRATHIBHA 2K12** Organized by TKRCET on 14Mar 2012.

Awards /Honours/any other academic distinction

S.No	Name of the award/honour/distiction	Awarded by	year
1.	Qualified in UGC-NET Exam (Electronics Science)	NTA, India	UGC-NET Jun 2020 UGC-NET Dec 2019
2.	NSS State Award	Govt of TN	2016-17
3.	Qualified in GATE Exam-6 times	MHRD	GATE2019, GATE2015, GATE2013, GATE2012, GATE2009, GATE2008
4.	Topper in M.Tech with 82%	TKRCET	2011
5.	Qualified in FET-2011 (Faculty Eligibility Test)	JNT University	FET-2011
6	Topper in SSC (in Bangarupalyam mandal, Chittoor Dt, A.P.)	Royal Public School	1999
7	Qualified in Jawahar Navodaya Entrance Exam	Jawahar Navodaya Vidyalaya	1994

Administrative Experience :

Name of Organization	Designation	Type of work
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KU (Deemed University), Coimbatore	GATE (Graduate Aptitude Test for Engineering) exam Coaching- Co-ordinator (Jun 2013-till date)	Responsible for Scheduling the date and time for GATE coaching. Also handle few classes in different subjects
KU (Deemed University), Coimbatore	Technical Club Co-ordinator(ECE Dept)-Electronics, Robotics and Embedded Clubs Jun 2015-Jun 2016	Responsible for Planning and Scheduling Training Classes in different software's related to ECE and conducting competitions in each semester for UG students.
KU (Deemed University), Coimbatore	Coordinator-TECH-EXPO(in QUASAR 2014,2015) Mar 6 th -7 th 2014	School level exhibition to be conducted
KU (Deemed University), Coimbatore	UG IV Year Project Co-ordinator Jun 2013-Jun 2018	Responsible for deciding Panel dates and arranging panel members
KU (Deemed University), Coimbatore	Time table Co-ordinator Jun 2013-till date	Responsible for making timetable for 60 staff members and 12 sections class timetables(4sections of II,III,IV Year each) in the Dept.
KU (Deemed University), Coimbatore	Editorial Team-International Conference 2013(IEEE Sponsored) ICSIPR'13 Feb7-8, 2013 and ICSPC'17 28th-29th Jul 2017.	Responsible for Conference Proceedings, Selected Papers to be published in IEEE, Special edition
TKRCET, Hyd	Coordinator- PRATHIBA 2012	Organizing and conducting National level Paper Presentations and Project Expo.
TKRCET, Hyd	Worked for two NBA Accreditations -once in 2008 & second Accreditation in 2012	Course File Maintenance for all subjects, Placement & Higher Studies File Maintenance.
TKRCET, Hyd	Coordinator- ECTA(ECE Technical Association)	Organizing and conducting Quiz, Debate, cycle tests.
TKRCET, Hyd	Coordinator- PRATHIBA 2007	Organizing and conducting Various events and workshop.

Participation in Extension work/Community services/Sports

S.No	Name	of	the	Participatory Level	Period	Place
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	Sport/Work/Service		/Year	Obtained
1	NSS Special Camp (21-28 Feb 2017)	Theme-O Youth! For National Integration and Social Harmony	2016-17	Program Officer
2	Tennikoit, Cricket, Tug off war in STAFF CHRISTMAS TOURNAMENT 2013,KU	Captain Member Member	2013-14	1 st place 2 nd place 2 nd place
3	NSS Special Camp (3-9 Feb 2014)	Incharge for Alandurai Camp site	2013-14	Incharge for Alandurai Camp site
4	Throw Ball in STAFF CHRISTMAS TOURNAMENT 2012	Member	2012-2013	2 nd Place
5	NSS CLUB	Program Officer	2012-2017	P.O.
6	XLIII National School Games, Bhopal	Member	1997-98	Participation
7	VIII National Meet for Group 'A' Games, Agra(U.P.)	Member	1997-98	Winner
8	XXVIII A.P. State Rural Sports Tournament	Member	1997-98	Second
9	XXVII A.P. State Rural Sports Tournament	Member	1996-97	Winner
10	XXVI A.P. State Rural Sports Tournament	Member	1995-96	Winner

Participation in corporate Life(Contribution to the institution served in the matter of co-curricular activities,enrichment of campus,Student's welfare etc.)

S.No	Name of the activity	Participatory Level	Period/Year
1	Arrear Coaching – Signals and systems, KU	Tutor	2013-14, 2015-16
2	Gate Coaching Classes, KU	Co-ordinator	2012-2017, tilldate
3	Arrear Coaching – MPMC Subject (for UG IV Year), KU	Tutor	2012-2013

4.	Living Star Karunya Ladies Hostel, KU	SRA (Senior Residence Advisor)	2012-2013
5.	NSS CLUB, KU	Program officer	Jun 2012 to tilldate
6	ECTA (ECE Technical ASSOCIATION), TKRCET	Co-ordinator	2010-2011

Intra College Cluster in JNT University Activities:

S.No	Name of the activity	College	Name of the Lab	Period/Year
1.	External Lab Examiner	RVR Institue of Engg&Tech, Hyderabad	MPI	31.03.2011 to 02.04.2011
2.	External Lab Examiner	PujyaShri Madhavanji Collegeof Engg&Tech, Hyderabad	IC Applications &ECAD Lab	10.11.2010
3.	External Lab Examiner	Bhoj Reddy Engg College for Women, Hyderabad	AC Lab	06.04.2009 to 07.04.2009

Subjects Handled in the college :

1. Control systems(4 times)
2. Signals and systems(5 times)
3. Switching Theory and Logic Design / Digital Electronics
4. Analog Communications
5. Digital Communications
6. Linear IC Applications
7. Electronic circuits(2 times)
8. DSP Architectures and Processors
9. Communication systems
10. Electronic Devices and Circuits
11. Pulse and Digital Circuits
12. Modern Digital Communications
13. Information Coding Theory
14. Basic Electronics

Professional Memberships :

1. The Society of Digital Information and Wireless Communications (SDIWC) – **Id: 11444**

2. International Association of Engineers(IAENG)- Id: **141006**
3. International Association of Computer Science and Information Technology(IACSIT)
4. International Society for Research and Development(ISRD)- Id: **M4150900841**

Personal Details:

Date of Birth	03.04.1983
Husband's Name	P. Nagabushanam
Gender	Female
Category	OBC
Marital Status	Married
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Youtube channel	https://www.youtube.com/channel/UCILGC842BznD3RVX3CgUAOg (For Mental and Physical wellness)
Address for Correspondence	S.RADHA, W/O P. Nagabushanam G3, Siva Priya Apartments, Chappani Madai Road 1 st left, Karunya Nagar, Coimbatore, TN. Pincode - 641114

(S.RADHA)