

Curriculum-Vitae



Name : Dr. SANTOSH KUMAR DWIVEDI
Father's Name : Shri T. D. Dwivedi
Date of Birth : 10-09-1977
Category : General
Discipline : Electronics & Communication Engineering
Marital Status : Married
Address for Communication : 54, Radhakunj colony, UPSIDC road, Sikandara, Agra (UP)
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Teaching Experience: *(Total 20+ Years of teaching experience)*

- Working as Associate Professor in Department of Electronics and Communication Engineering, Ch. Beeri Singh College of Engineering and Management, Agra (UP), INDIA, since July 01, 2020 to till date.
- Worked as Associate Professor in Department of Electrical and Electronics Engineering, Aditya College of Technology and Science, Satna (MP), INDIA, since July 23, 2018 to June, 30, 2020.
- Worked as **Assistant Professor and Head**, in Department of Electronics & Communication Engineering, Hindustan Institute of Technology & Management (formerly B. M. A. S. Engineering College), a **SHARDA GROUP OF INSTITUTIONS (SGI) Agra (UP)**, INDIA, since July 15, 2008 to July 13, 2018.
- Worked as **Assistant Professor and Head**, in Department of Electronics & Communication Engineering, Vindhya Institute of Technology and Science, Satna (MP), INDIA, since August 01, 2007 to July 12, 2008.
- Worked as Senior Lecturer in Department of Electronics & Communication Engineering, Vindhya Institute of Technology and Science, Satna (MP), INDIA, since August 01, 2004 to July 31, 2007.
- Worked as Lecturer in Department of Electronics & Communication Engineering, Shree Institute of Science & Technology, Bhopal (MP), INDIA, since June 01, 2002 to July 31, 2004.

- Worked as Lecturer in Department of Electronics & Communication Engineering, Mandsaur Institute of Technology, Mandsaur (MP), INDIA, since August 01, 2000 to May 31, 2002.

Professional Qualification:

- Awarded **Doctor of Philosophy (Ph.D.)** in the field of **Radio Frequency Communication and Microwave in Electronics Engineering** department from **Rajasthan Technical University, Kota (Rajasthan), in 2021**. My research title “**Design of Metamaterial Inspired Reconfigurable Multiband Antenna for Wireless Applications**”
- **M. Tech.** (Microwave & Millimeter Wave Engineering) with 8.59 CGPA from **Maulana Azad National Institute of Technology (MANIT), Bhopal (M.P.)** in year 2007.
- B.E. (Electronics & Telecommunication Engineering) from Pt. Ravishankar Shukla University, Raipur (CG) with 67% in year 2000.
- Higher secondary from M.P. Board, Bhopal (M.P.) with 70.68% in year 1995.
- High school from M.P. Board, Bhopal (M.P.) with 81.6% in year 1993.

Research Experience: (10+ Years of research experience)

Research paper reviewer:

- Reviewer for Wireless Personal Communication, Springer. A SCI International journal reporting research on RF design, and properties of wireless communication.
- Reviewer for International Journal of Wireless Communications and Mobile Computing.
- Reviewer and session chair for IEEE International Conference on Communication, Control and Intelligent Systems 2015 (CCIS 2015) will be held at GLA University Mathura (UP), INDIA during 07th November 2015 to 08th November 2015.
- Reviewer and member of technical program committee for IEEE International Conference on Computational Intelligence and Communication Networks 2018 (CICN 2018) held at Esbjerg, Denmark during August 17, 2018 to August 19, 2018.
- Reviewer for the 25th World Congress on Engineering (WCE 2018) organized by International Association of Engineers (IAENG) held on London, U.K. during July 4-6 2018.

Research Paper published / accepted/ communicated:

International Journal:

- **Santosh K. Dwivedi, M. Kumar and L. Tharani, (2020)** “A Metamaterial Loaded Hybrid Fractal Hepta-Band Antenna for Wireless Applications with Reconfigurability

Characteristics”, *Progress In Electromagnetics Research C*, Vol. 100, 187–203, 2020. doi: 10.2528/PIERC20011203. **Current status: Published.**

- **Santosh K. Dwivedi**, M. Kumar and L. Tharani, (2018) “A Rectangular SRR Switched Slotted Microstrip Patch for Frequency Diversity Application,” *Wireless Personal Comm.*, 103, pp. 2863–2875, 2018. doi:10.1007/s11277-018-5967-z in **Springer**.
Current status: Published.

International Conferences:

- **S K Dwivedi**, M Kumar, L Tharani, (2015) “**A novel probe-fed tunable broadband antenna using stub loaded configuration**”, Proc. in IEEE International Conference on Communication, Control and Intelligent Systems (CCIS 2015), at GLA University, Mathura (UP) during 7-8 Nov. 2015, proceedings published online in **IEEE-Xplore**.
- Sachin Dubey, **S K Dwivedi**, and B N Gupta (2012) “**Design and Simulation of High level low power 7T SRAM cell using various process and circuit technique**”, Proc. in International Conference on Advances in Electronics, Electrical and Computer Science Engineering (EEC 2012) held at Uttarakhand Technological University, Dehradun, during 07-09 July 2012. Proceedings published online in Institute of Research Engineers and Doctors.
Website: <http://theired.org/eec>.

National Conferences:

- **Santosh Kumar Dwivedi**, Rahul Saraswat (2013): “Analysis and application of Biquad filter for sinusoidal signal: A review”, Proc. National Conference on Globalized Leading Edge Technologies in Engineering (GLETE- 2013), November 15-16, 2013, organized by Department of Electronics & Communication Engineering, Eshan College of Engineering, Mathura (UP), INDIA. Web Site: www.eshancollege.com.
- **Santosh Kumar Dwivedi**, Rahul Saraswat (2013): “A model for dynamic and short circuit power of CMOS inverter with RC load”, Proc. National Conference on Challenges for Efficient Energy Technology for Clean Energy - 21st Century (CEETCE- 2013) May 23-24, 2013, Eshan College of Engineering, Mathura (UP), INDIA.
Web Site: www.eshancollege.com.
- **Santosh Kumar Dwivedi**, Manish Gupta, Rahul Saraswat and Shashank Sharma (2013): “A Comprehensive Analysis for Digital Public Key Watermarking System: A Survey”, Proc. National Conference on Global Innovation in Computer Science and Electronics (Cybertronics-2013), March 08-09, 2013, organized by Department of Computer Science & Engineering and Electronics & Communication Engineering, Eshan College of Engineering, Mathura (UP), INDIA. Web Site: www.eshancollege.com.
- Rahul Saraswat, Manish Gupta, **Santosh Kumar Dwivedi** and Shashank Sharma (2013): “Digital Watermarking for Ownership Issues: A Review ”, Proc. National Conference on

Global Innovation in Computer Science and Electronics (Cybertronics-2013), March 08-09, 2013, organized by department of Computer Science & Engineering and Electronics and Communication Engineering, Eshan College of Engineering, Mathura (UP), INDIA.
Web Site: www.eshancollege.com.

Seminar, Courses, Industrial Training, Conferences and Workshop Organized:

- Workshop Coordinator for one day webinar on “**Mine Ventilation and Fire Hazards in Underground Mines**” conducted by TEQIP, RGPV, Bhopal at Aditya College of Technology and Science, Satna on June 12, 2020.
- Workshop Coordinator for one day webinar on “**Introduction to Machine Learning with Trends and Challenges**” conducted by TEQIP, RGPV, Bhopal at Aditya College of Technology and Science, Satna on June 08, 2020.
- Workshop Coordinator for one day webinar on “**Vertical Transport System in Underground Water**” conducted by TEQIP, RGPV, Bhopal at Aditya College of Technology and Science, Satna on June 03, 2020.
- Workshop Coordinator for three days online workshop on “**Employability Skills after Covid 19**” conducted by TEQIP, RGPV, Bhopal at Aditya College of Technology and Science, Satna during May 29 to May 31, 2020.
- Workshop Coordinator for one day webinar on “**Understanding Pedestrian Flow Characteristics**” conducted by TEQIP, RGPV, Bhopal at Aditya College of Technology and Science, Satna on May 26, 2020.
- Workshop Coordinator for three days STTP on “**Employability Skills**” conducted by TEQIP, RGPV, Bhopal at Aditya College of Technology and Science, Satna during November 28 to November 30, 2019.
- Workshop Coordinator for two days STTP on “**Mine Ventilation and Mines safety**” conducted by TEQIP, RGPV, Bhopal at Aditya College of Technology and Science, Satna during September 19 to September 20, 2019.
- Workshop Coordinator for One day workshop on “**Object Based Evaluation for NBA Accreditation**” conducted by TEQIP, RGPV, Bhopal at Aditya College of Technology and Science, Satna on August 03, 2019
- Organized a one day induction program for first year students at Aditya College of Technology and Science, Satna on August 24, 2019.
- Organized a one day national seminar / talk on “**MATLAB**” conducted by DUCAT Technologies Noida at Hindustan Institute of Technology & Management, Agra on dated 20 November 2017.
- Organized an Industrial Visit for approximately 50 students of department of Electronics and Communication Engineering of Hindustan Institute of Technology & Management, Agra at MACAS Automation LTD. Faridabad (Haryana) in November 7, 2017.

- Workshop Coordinator for One week ISTE STTP on “CMOS Mixed Signal and Radio Frequency VLSI Design” conducted by IIT Kharagpur at Hindustan Institute of Technology & Management, Agra during Jan. 30 to Feb. 04, 2017.
- Convener of Technical Poster Competition in “Eureka 2016 (Annual fest)”, at Hindustan Institute of Technology & Management, Agra, during 08-09 March 2016.
- Organized a national seminar on “Embedded System”, conducted by DUCAT Technologies Noida and Department of Electronics & Communication Engineering, Hindustan Institute of Technology & Management, Agra on 20 February 2016.
- Organized an Industrial visit for 78 students from department of electronics & communication engineering of Hindustan Institute of Technology & Management, Agra at D-Link Networks Pvt. Ltd. Goa during 20 September 2015 to 26 September 2015.
- Organized a national seminar / talk on “Signals and Systems” at Hindustan Institute of Technology & Management, Agra on November 08 2014.
- Organized an Industrial visit for 28 students of B. Tech(EC), 2nd year from Department of Electronics and Communication Engineering, Hindustan Institute of Technology and Management, Agra, at Marson’s Electrical Industries, Agra (UP) on November 01, 2014.
- Teaching Assistant for two week ISTE workshop on “Signals and Systems” conducted by IIT Kharagpur through NMEICT at Hindustan Institute of Technology & Management, Agra during January 2-12, 2014. Organized one week National Workshop on “Wireless and Mobile Communication” by Department of electronics and Communication Engineering, at Hindustan Institute of Technology & Management, Agra during 16-20 September 2013 in collaboration with NITTTR Chandigarh.
- Organizes a Guest Lecture on “MEMS Technology” by Dr. Mithilesh Kumar, Professor, Department of Electronics Engineering, RTU, Kota at Hindustan Institute of Technology & Management, Agra on September 07 2013.
- Workshop Coordinator for two week ISTE workshop on “Analog Electronics”, conducted by IIT Kharagpur through NMEICT at Hindustan Institute of Technology & Management, Agra during 04-14 June 2013.
- Organizing Secretary of two days workshop on “ROBOTRYST-2013”, organized by Robosapiens Technologies and Department of Electronics and Communication Engineering at Hindustan Institute of Technology & Management, Agra during Feb 19-20, 2013.
- Organized a Guest Lecture on “Digital Communication” by Dr. D.K. Shrivastava, Professor, Department of Electronics and Communication Engineering, BIET, Jhansi at Hindustan Institute of Technology and Management, Agra on Feb 09, 2013.
- Organizing Committee member of two days national workshop on “Advances in Microwave Integrated Circuit (AMIC-2012)” jointly organized by Department of Electronics and Communication Engineering and IEEE_MTTTS students branch chapter at Hindustan Institute of Technology & Management, Agra during February 11-12, 2012.

- Co-convenor of 1st ISTE Student State Annual Convention AADHAAR-2011 (Theme : Latest Trends in Engineering & Technology) of Uttar Pradesh and Uttarakhand region supported by ISTE student chapter HITM, Agra at Hindustan Institute of Technology & Management, Agra during 19-20 August 2011.
- Organize a Guest Lecture on “Microstrip Patch Antenna Design” by Dr. P.K. Singhal, Professor in Department of Electronics and Communication, MITS, Gwalior (MP) on August 11, 2010 at Hindustan Institute of Technology and Management, Agra.

Courses & Workshop Attended:

- Attended for one day workshop on “**Energy Audit**” organized by Department of Electrical and Electronics Engineering, University Institute of Technology, RGPV, Bhopal (MP) on January 25, 2020.
- Attended for two days workshop on “Introduction to Robotics” organized by Indian Institute of Technology, Bombay (e-yantra) on December 27 to December 28, 2019.
- Attended for two days Saptahant Shikshak Prashikshan workshop on “Introduction to Machine Learning” under the National Mission on Education through ICT, conducted by IIT Kharagpur, at Hindustan Institute of Technology & Management, Agra, during 16 December 2017 to 17 December 2017.
- Attended for one day workshop on “Organic and Polymeric Ferroelectrics for Flexible Electronics” organized by Nano and Molecular Society India, at Hindustan College of Science and Technology, Mathura, on December 11, 2017.
- Attended for two weeks ISTE workshop on “Electric Power System” under the National Mission on Education through ICT, conducted by IIT Kharagpur, held at Hindustan Institute of Technology & Management, Agra, one week full-time equivalent work online (online activities started from 12th June to 15th July 2017) and the face to face interaction through ICT, 25th May, 2015 to 30th May, 2015.
- Attended one week ISTE STTP Coordinators workshop on “CMOS, Mixed Signal and Radio Frequency VLSI Design” under the National Mission on Education through ICT conducted by IIT, Kharagpur, during 19th September, 2016 to 23rd September, 2016.
- Attended one day workshop on “Parallel Computing with GPUs”, conducted by GPU Centre of Excellence (GCOE), IIT Bombay organized by Department of Electronics Engineering, Rajasthan Technical University, Kota dated March 5, 2016.
- Attended one week AICTE sponsored short term course on “VLSI Design” through ICT, conducted by Department of Electronics and Communication Engineering, NITTTR, Chandigarh held at Hindustan Institute of Technology & Management, Agra during 05 October 2015 to 09 October 2015.
- Attended for two week ISTE STTP on “Introduction to Design of Algorithms” under the National Mission on Education through ICT, conducted by IIT Kharagpur, held at Hindustan Institute of Technology & Management, Agra, one week full-time equivalent

work online (online activities started from 27th April to 24th May 2015) and face to face interaction through ICT, during 25 May 2015 to 30 May 2015.

- Attended two week ISTE workshop on “Control Systems” under the National Mission on Education through ICT, conducted by IIT Kharagpur, held at Hindustan Institute of Technology & Management, Agra during 02 December 2014 to 12 December 2014.
- Attended for two weeks ISTE workshop on “Computer Programming”, under the National Mission on Education through ICT, conducted by IIT Bombay, held at Hindustan Institute of Technology & Management, Agra, one week full-time equivalent work online (online activities started from 20th May to 15th June 2014) and face to face interaction through ICT, during 16 June 2014 to 21 June 2014.
- Attended one week AICTE recognized short term course on “Network Security and Firewalls”, through ICT, conducted by Department of Computer Science, NITTTR, Chandigarh, held at Hindustan Institute of Technology & Management, Agra during 18 November 2013 to 22 November 2013.
- Attended one week AICTE recognized ICT program on “Solid State Control of Electric Drives”, conducted by NITTTR, Chandigarh, held at Hindustan Institute of Technology and Management, Agra, during 02-06 September 2013.
- Attended one week ISTE workshop for Coordinators on “Analog Electronics”, under the National Mission on Education through ICT, conducted by MHRD, held at IIT Kharagpur, during 01-05 April 2013.
- Attended two day ISTE workshop on “Aakash for Education”, under the National Mission on Education through ICT, conducted by IIT Bombay, held at Hindustan Institute of Technology & Management, Agra during 10-11 November 2012.
- Attended two days Workshop on “Microsoft WOWZAPP 2012 Worldwide Hackathon for Windows” organized by Department of Computer Science and Engineering, at Hindustan Institute of Technology & Management, Agra, during 09-10 November 2012.
- Attended two-week ISTE workshop on “Introduction to Research Methodologies” under the National Mission on Education through ICT, conducted by IIT Bombay, held at Hindustan Institute of Technology and Management, Agra, during 25th June to 04th July, 2012.
- Attended one week ISTE workshop on “Mission 10X”, conducted by WIPRO applying thought, held at Anand Engineering College, Agra, during Feb 27, 2012 to March 03, 2012.
- Attended two day National Workshop on “Advances in Microwave Integrated Circuit (AMIC-2012)” jointly conducted by Department of Electronics and Communication Engineering and IEEE-MTTS student branch chapter HITM, Agra, at Hindustan Institute of Technology and Management, Agra during 11-12 February 2012.
- Attended two days national workshop on “Labview, MATLAB and Their Applications” under the National Mission on Education through ICT, organized by Dayalbagh

Educational Institute (Deemed University), Agra (UP), during March 25,2011 to March 26, 2011.

- Attended one day workshop on “Virtual Labs”, under the National Mission on Education through ICT Project, conducted by Virtual Teaching and Learning Labs (VITAL), Dayalbagh Educational Institute, Agra on dated 22nd January, 2011.
- Attended two days workshop on “Data Analysis using SPSS” jointly organized by SPSS South Asia and Hindustan Institute of Management & Computer Studies, held at HIMCS, Mathura (UP) from 14th May, 2010 to 15th May, 2010.
- Attended one-day workshop on “Human Values and Professional Ethics”, conducted by Value Education Cell, Uttar Pradesh Technical University (UPTU), Lucknow, at Hindustan Institute of Technology and Management, Agra (UP), on dated 12th April, 2010.
- Attended two days technical seminar on “Mobile Networking and Computing”, organized by Department of Electronics and Communication Engineering, Mepco Schlenk Engineering College, Sivakasi, TamilNadu on January 29-30, 2010.
- Attended one-week workshop on “Recent Advances in Nanotechnology” held at School of nanotechnology, Rajiv Gandhi Proudhyogiki Vishwavidyalaya (RGPV), Bhopal (MP) during 2nd -6th June, 2008.
- Attended four days workshop on “Computer Fundamentals”, organized by Department of Computer Science & engineering and Information Technology, held at Vindhya Institute of Technology and Science, Satna (MP) from 26 November 2007 to 29 November 2007.
- Attended two days workshop on “VLSI Technology”, under Technical Education Quality Improvement Programme (TEQIP), organized by Department of Electronics and Communication Engineering, held at Ujjain Engineering College, Ujjain (MP) from 24-25 February, 2007.

Computer Skills:

Operating System: Windows XP / 2000 server

Other utility : MATLAB, MS-office, Latex, Code Composer studio, VHDL, CST

Professional Membership:

Life Member of ISTE (LM 64964)

Area of Interest:

Digital Signal Processing

Microwave Engineering

Antenna and Wave Propagation

Electronics Engineering

Signals and Systems

Communication System

Optical Communication

Nature of Job / Duties:

(i) Academic:

Teaching various subjects related to Electronics and Communication Engineering at undergraduate level. Supervise the technical project undertaken by students of B.Tech and guided research paper during their courses of training. Facilitate the modernized laboratory to the students as per requirement.

(ii) Awards/ Achievements:

The “**Shreshtha shikshak award**” of Sharda Group of Institutions is achieved in year 2009 at Hindustan Institute of Technology and Management, Agra (UP).

(iii) Administrative Work:

- Worked as **Dean Academics** since 23rd July 2018 to 20th June, 2020 at Aditya College of Technology and Science, Satna (MP).
- Worked as **Head of the Department** of Electronics and Communication Engineering, since 25th July 2008 to 13th July, 2018 at Hindustan Institute of Technology & Management, Agra.
- Working as a **member of Grievance Redressal Cell** for Faculty at Hindustan Institute of Technology & Management, Agra.
- Worked as **Additional Dean Academics** at Hindustan Institute of Technology & Management, Agra.
- Worked as a **President of college Library committee** at Hindustan Institute of Technology & Management, Agra.
- Established VSAT center for interactive teaching through ICT mode in collaboration with RGPV, Bhopal at Vindhya Institute of Technology and Science, Satna (MP).
- Worked as **Training and Placement Coordinator** for B. Tech. (EC) students at Hindustan Institute of Technology & Management, Agra.
- Worked as **Head of Department** of Electronics & Communication Engineering at Vindhya Institute of Technology and Science, Satna (MP).
- Worked as **Head of Department** of Electronics & Communication Engineering at Shree Institute of Science and Technology, Bhopal (MP).
- Worked as **Deputy Registrar** at Shree Institute of Science and Technology, Bhopal (MP).
- Establishment of various labs of the Department likes Analog Communication lab, Microwave & Optical Communication lab, Measurement & Instrumentation lab etc. at SIST, Bhopal (MP), VITS, Satna (MP) and HITM, Agra (UP).
- Procurement of various measuring instruments for all departmental labs at SIST, Bhopal (MP), VITS, Satna (MP) and HITM, Agra (UP).
- Appointed as an External Practical Examiner of Uttar Pradesh Technical University, Lucknow (formerly Mahamaya Technical University, Noida and Gautam Buddh Technical University, Lucknow) and Rajiv Gandhi Proudyogiki Vishwavidyalaya, Bhopal.

- Appointed as an **Observer / member of Flying squad** for end semester theory examinations conducted by Uttar Pradesh Technical University, Lucknow (formerly Mahamaya Technical University, Noida and Gautam Buddh Technical University, Lucknow).
- Appointed as an **Observer / Centre Controller** for Uttar Pradesh State Entrance Examination (UP-SEE) conducted by Uttar Pradesh Technical University, Lucknow (formerly Mahamaya Technical University, Noida and Gautam Buddh Technical University, Lucknow).
- Worked as **Head Examiner** for end Semester theory examination, Uttar Pradesh Technical University, Lucknow (formerly Gautam Buddh Technical University, Lucknow) at university central evaluation center (Hindustan Institute of Technology & Management, Agra).
- Worked as an **Assistant Center Superintendent (ACS)** for end Semester theory examination, Rajiv Gandhi Technological University, Bhopal (MP), at Shree Institute of Science and Technology, Bhopal (MP).
- Worked as **Liaisoning officer** for RGPV and DTE, Bhopal at Shree Institute of Science and Technology, Bhopal (MP), INDIA.
- Worked as **member of Proctorial Board** at Hindustan Institute of Technology and Management, Agra (UP).
- Worked as **Admission Cell Coordinator** for B.Tech/MBA students at Vindhya Institute of Technology and Science, Satna (MP).
- Completed various Academic and Administrative works assigned to me time to time.

I hereby declare that all the information furnished above is true to the best of my knowledge and belief.

(Santosh Kumar Dwivedi)