

## Sarla More

sarlamore@gmail.com

Department of Computer Science and Applications

Dr. Harisingh Gour University, Sagar MP INDIA, 470003

+91 6265419159

[https://scholar.google.com/citations?user=Abbf\\_CMAAAAJ](https://scholar.google.com/citations?user=Abbf_CMAAAAJ)

### EDUCATION

- 2016- Present**      **Doctor of Philosophy** (expected defence in August 2022)  
Department of Computer Science and Applications, Dr. Harisingh Gour University, Sagar, MP, India  
Thesis title:    Some New Methods for Ready Queue Processing Time Estimation in Multiprocessor Environment  
Supervisor:    Prof. Diwakar Shukla
- 2009-2011**      **Master of Technology** (First Class honors)  
School of Information Technology, Rajiv Gandhi Proudyogiki Vishwavidyalaya Bhopal, MP, India  
Thesis title:    Multi-relation Image Retrieval and Annotation based on Holistic Approach and Decision Tree  
Supervisor:    Dr. Nishchol Mishra
- 2004-2008**      **Bachelor of Engineering** (First Class)  
Department of IT, Shri Govindram Seksaria Institute of Technology and Science Indore, MP, India
- 2001-2004**      **Diploma in IT** (First Class honors)  
Department of IT, Govt. Women's Polytechnic College, Burhanpur, MP, INDIA

### PATENT

Diwakar Shukla and Sarla More. "A System for Lottery Scheduling for Ready Queue Mean Time Estimation in Multiprocessor Environment and Method thereof." Indian Patent Application 202121015734A, filed April 02, 2021, publication 09/04/2021

<https://patentscope.wipo.int/search/en/detail.jsf?docId=IN321837155>

### RESEARCH INTERESTS

Process Scheduling, Computer Architecture, Image Processing

### SELECTED HONOURS

- 2020      GATE-2020 Qualified  
2018      UGC/NTA NET Qualified  
2008      GATE-2008 Qualified  
2021      IMA Data Analytics & Visualization Fundamentals Certificate

## MEMBERSHIP

**2021-present** Professional Member, (ACM) Association for Computing Machinery

## TEACHING INTERESTS

- Graduate-level courses in Operating System, Computer Architecture
- Undergraduate-level courses in Computer Education

## TEACHING EXPERIENCE

March 2020-present	<b>Guest Faculty</b> Department of Computer Science and Applications, Dr. Harisingh Gour University, Sagar, MP <ul style="list-style-type: none"><li>• Taught Operating System, Computer Organization and Architecture, Computer Education-I &amp; II, PC Software</li></ul>
Aug.2014-Aug.2016	<b>Assistant Professor</b> Department of Computer Science, Sha-Shib Institute of Technology, Bhopal, MP <ul style="list-style-type: none"><li>• Taught Operating System, Java Programming</li></ul>
Aug.2012-May 2014	<b>Assistant Professor</b> Department of Computer Science, Thakur Shivkumar Singh Memorial Engineering college, Burhanpur, MP <ul style="list-style-type: none"><li>• Taught Software Engineering, Computer Organization, Dot Net Technology</li></ul>
Aug.2011-July 2012	<b>Assistant Professor</b> Department of Information Technology, Truba Institute of Engineering and Information Technology, Bhopal, MP <ul style="list-style-type: none"><li>• Taught Embedded System, Data Mining and Warehousing</li></ul>
Nov.2009-May 2010	<b>Lecturer</b> Under Earn while you learn scheme at UIT RGPV Bhopal, MP <ul style="list-style-type: none"><li>• Taught Java Programming</li></ul>
July 2008-Sep.2009	<b>Lecturer</b> Department of Computer Science, Acropolis institute of Technology and Research, Indore, MP <ul style="list-style-type: none"><li>• Taught Data Structures and algorithms, Operating System, Data Base Management System</li></ul>

## PUBLICATIONS

### Publication in refereed journals

- More, S., and Shukla, D. (2021). Sampled Ready Queue Processing Time Estimation Using Size Measure Information in Multiprocessor Environment. Reliability: Theory and Applications (*RT&A*) Vol. 16, Issue 3, <https://doi.org/10.24412/1932-2321-2021-363-63-80>
- Shukla, D., and More, S. (2020). Modified Group Lottery Scheduling Algorithm for Ready Queue Mean Time Estimation in Multiprocessor Environment. Reliability: Theory and

Applications (*RT&A*), Vol. 15, Issue 4, <https://doi.org/10.24411/1932-2321-2020-14008>

- More, S., and Shukla, D. (2018). A Review on Ready Queue Processing Time Estimation Problem and Methodologies used in Multiprocessor Environment. *International Journal of Computer Sciences and Engineering*, 6(5), 1186–1191. <https://doi.org/10.26438/ijcse/v6i5.11861191>

### **Paper Submitted for publication in refereed journals**

- More, S., and Shukla, D. (2022). A Generalized Approach For The RQRPTTE Algorithms in Multiprocessor Environment under Risk of Sudden Failure using Regression Type Estimator with Cost Analysis. Submitted to *Reliability: Theory and Applications (RT&A)*
- More, S., and Shukla, D. (2021). Sampled Ready Queue Remaining Time Estimation using Lottery Scheduling and Imputation at Multiprocessor Environment. Submitted to *Control & Cybernetics*.
- More, S., and Shukla, D. (2021). Ready Queue Remaining Processing Time Estimation in Multiprocessor Environment under Risk of Sudden Failure using Regression Type Estimator with Cost Analysis. Submitted to *Reliability: Theory and Applications (RT&A)*

### **Publications in refereed conference proceedings**

- More, S., and Shukla, D. (2020). Some New Methods for Ready Queue Processing Time Estimation Problem in Multiprocessing Environment. *Lecture Notes in Networks and Systems*, 100, 661–670. [https://doi.org/10.1007/978-981-15-2071-6\\_54](https://doi.org/10.1007/978-981-15-2071-6_54)
- More, S., and Shukla, D. (2019). Review on Internet Traffic Sharing Using Markov Chain Model in Computer Network. *Lecture Notes on Data Engineering and Communications Technologies*, 16, 81–98. [https://doi.org/10.1007/978-981-10-7641-1\\_7](https://doi.org/10.1007/978-981-10-7641-1_7)
- More, S., and Shukla, D. (2019). Analysis and Extension of Methods in Ready Queue Processing Time Estimation in Multiprocessor Environment. *SSRN Electronic Journal*, 1558–1563. <https://doi.org/10.2139/ssrn.3356312>
- More, S., and Mishra, N. (2012). Multi-Relation Image Retrieval and Annotation based on Holistic Approach and Decision Tree. *International Journal of Computer Applications*, 39(7), 39–44. <https://doi.org/10.5120/4836-7096>
- More, S., and Mishra, D. K. (2012). 4G revolution: WiMAX technology. *Asian Himalayas International Conference on Internet*. <https://doi.org/10.1109/AHICI.2012.6408433>
- More, S., and Mishra, D. K. (2012). Multimedia data mining: A Survey. *International Journal of Science, Spirituality, Business and Technology (IJSSBT)*, Vol. 1, No 1, pp 49-55

### **Non-refereed papers**

- Sarla More and Diwakar Shukla (2020). Ready Queue Mean Time Estimation Using Extended Lottery Scheduling Scheme in Multiprocessor Environment. *International Conference on Innovative Research in Science, Management and Technology (ICIRSMT - 2020)* organized by Department of CSA, Bilaspur University, Bilaspur (C.G.), India
- Sarla More and Diwakar Shukla (2020). Lottery Scheduling Approach for Ready Queue Time Estimation in Multiprocessors. *8<sup>th</sup> International Conference Innovative Research Practices in Pharma, Science Management, Agriculture and Technology*, organized by Swami Vivekanand University, Sagar, MP, INDIA

- Sarla More and Diwakar Shukla (2020). Optimization of Ready Queue Mean Time Estimation using MGLS Algorithm in Multi processing Environment. *National Conference on Big Data and Intelligent Systems*, organized by Department of Computer Science & Applications, LNCT University Bhopal, MP, INDIA
- Sarla More and Diwakar Shukla (2019). Group Systematic Lottery (GSL) Scheduling Scheme for Ready Queue Processing Time Estimation in Multi-processor Environment. *International Conference ICSISCET*, organized by Madhav Institute of Technology & Science, Gwalior, MP, INDIA
- Sarla More and Diwakar Shukla (2019). Mean Time Estimation in Ready Queue under Multiprocessor Environment. *International Conference on Recent Trends in Mathematical, Physical and Computational Sciences (ICRTMPCS)* organized by Department of Mathematics and Statistics, Dr. Harisingh Gour Vishwavidyalaya, Sagar, MP, INDIA

### **Poster Presentations**

Sarla More and Diwakar Shukla (2018). A study on ready queue processing time estimation methodologies in multiprocessor environment. *6th International conference on population and its control through agriculture pharmacy science and technology*, organized by Swami Vivekanand University, Sagar, MP, INDIA

### **WORKSHOP ATTENDED**

- ACM India Winter school on Natural Language Processing (for women only) Hosted by IRNLP Lab, DA-IICT during 4th-15th January 2021
- ACM India Summer School 2021 on Program Execution Hosted by the Indian Institute of Technology Goa, Sponsored by Hewlett Packard Enterprise (HPE) from 21st June-2nd July 2021
- ACM India Winter School on Natural Language Processing (for woman only) hosted by IRNLP Lab, DA-IICT during 4th-15th January 2021
- AICTE Training and Learning (ATAL) Academy online FDP on Artificial Intelligence at Federation of Indian Chambers of Commerce and Industry (FICCI) on 15th-19th June 2020
- Using ICT in Online Teaching Learning Process on 31st August - 05th September 2020
- AICTE Sponsored Six-Day Short Term Training Program (STTP) under AICTE Quality Improvement Schemes (AQIS) on Data Science: Unfolding Layers of Knowledge (Phase-I) Organized by Department of Computer Science & Engineering, Acropolis Institute of Technology and Research Indore on 23rd-28th Nov. 2020
- GIAN course on Epidemiological Research Methods and Analysis (174046G06) organized by population research centre, Department of general and applied geography, Dr. Harisingh Gour Vishwavidyalaya, Sagar (MP), with grade B and credit 1 on 19th -24th February 2018
- A Practical Approach Towards Data Science and Artificial Intelligence Organized by School of Information Technology, UTD, RGPV, Bhopal under TEQIP-III during 29th may- 2nd June 2018
- UGC sponsored Short Term Course on Research Methodology from UGC-Human Resource Development Centre, Dr. Harisingh Gour Vishwavidyalaya, Sagar, India, during 31st July to 6th August 2017.