

# RESUME

**SUBJECT NAME:** OPTOELECTRONICS AND COMMUNICATION SYSTEM

**REGISTRATION NUMBER:** 20-17-03

**SPECIALIZATION:** OPTICAL COMMUNICATION AND PHOTONICS

**NAME:** UMAKANT RAJARAM DHARTAK

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## EDUCATION QUALIFICATION

| EXAMINATIONS | BOARD /UNIVERSITY                                    | YEAR | SPECIALIZATION                                  | %TAGE/<br>CGPA | DIVISION                     |
|--------------|--|------|---|----------------|------------------------------|
| S.S.C        | Maharashtra State Board                              | 2009 |   | 79.69          | FIRST                        |
| H.S.C.       | Maharashtra State Board                              | 2011 | Science   | 63.83          | FIRST                        |
| B.E.         | PUNE UNIVERSITY                                      | 2016 | Electronics & Telecommunication Engg.           | 65.94          | FIRST                        |
| M.TECH       | DEFENCE INSTITUTE OF ADVANCE TECHNOLOGY<br>PUNE DRDO | 2022 | Optoelectronics and Communication System (OCP). | 6.92           | FIRST                        |
| GATE         |  | 2020 | Electronics and Communication Engineering       | MARKS-32.33    | RANK- 8984<br>GATE SCORE-393 |

## TECHNICAL SKILLS:

- ❖ *DESIGNING SOFTWARE KNOWLEDGE:* NI Multisim Xilinx & Proteus, ISE 14.7, VIVADO.
- ❖ *PROGRAMMING SOFTWARE KNOWLEDGE:* C LANGUAGE, DATA STRUCTURE, MATLAB, FPGA, NI LABVIEW, Assembly Language: 8051, Embedded C, Python, VHDL, Verilog.
- ❖ I have worked on a 6-month SPARTON 3E FPGA Board by using ISE 14.7 software, I have also worked on a 6-month Basys 3 FPGA Board by using VIVADO software.

## PROJECTS:

- Designed a project on “Wire Battery Charger” in the third year as a mini project in the field of power electronics and microcontrollers to measure the voltage of the battery on LCD.
- Design a project on “Cursor Control Using Pupil Tracking” in the final year as a major project in the field of digital image processing which aims to for Handicap persons using eye pupils.

- M. Tech “thesis” work on “Development Of FPGA Based Autocorrelator card for single photon detection”.

### **Extra Co-Curricular Activities:**

- Worked as a member of a karamveer bhaurao Patil ‘earn and learn’ committee working 3 hours per day in SAOE library.
- Successfully Completed Certification Course in MS-CIT.

### **STRENGTHS:**

- Positive Attitude Towards Work.
- Adaptable And Ability To Do hard Work.
- Having Good Patience And Smiley Nature.
- Good Listener & Good Team- Leader.
- Eager To Know New Things.

### **Personal Detail:**

**Nam:** Umakant Rajaram Dhatark

**Date of birth:** 13<sup>th</sup> June 1993

**Gender:** Male

**Nationality:** Indian

**Marital Status:** Single

**Permanent Address** : Post- Bhokar, Tal- Bhokar, DIST- Nanded Pin-431801

**Language Known:** English, Hindi, Marathi.

### **Hobbies:**

- Playing cricket
- Reading
- Teaching
- Making friends

I hereby declare that the above information given by me is true and correct to the best of my knowledge.

**DATE**

Your Faithfully

**(Umakant Rajaram Dhatrak)**