RESUME



# Ms. Mridul Saxena [Mobile: +91 8745953523](mailto:kori.shivu@mailcity.com)

## PERSONAL PROFILE

Name : Ms. Mridul Saxena

Date of Birth : 1st November 1979

Nationality : Indian

Address : 304,3rd Floor, Kumar Mohalla, Badarpur, New Delhi-44 Marital Status : Married

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[sri.mridul@rediffmail.com](mailto:sri.mridul@rediffmail.com)

## EDUCATIONAL QUALIFICATIONS

|  |  |  |  |
| --- | --- | --- | --- |
| **Qualification** | **Institute/University** | **Division/%** | **Year** |
| M.Tech (Electronics and Comm.) | Delhi College of Engineering Technology and Management, Haryana  (YMCA University, Faridabad) | First Class 74 % | 2020 |
| B. E (Elec. & Comm. Engg.) | Jamia Millia Islamia, New Delhi | First Class 62 % | 2017 |
| Diploma ( Elec. & Comm Engg.) | Govt Girls Polytechnic, Lucknow (Board of Technical of Education,  UP) | First Class 75 % | 2003 |

**AREAS OF RESEARCH**

Basic of Electronics, Digital Electronics, Analog Electronics and Analog Integrated Circuits. Title of M.Tech Dissertation : Implementation of Passive Filter Design

## SERVICES

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| **S. No.** | **Institute** | **Period of service in**  **each post** | **Designation** | **Nature of work and level of**  **responsibilities** |
| 1. | Manav Rachna University, Faridabad | April 2017  -  Till date | **Sr.Tech** | Teaching and Research |
| 2. | Manav Rachna University, Faridabad | Sept. 2013 to  March 2017 | Lab Instructor | Teaching and demonstrate Practical of Student first year and second year and maintain all data of student. |
| 3. | Northern India Eng.  College LKO (Unit of Babu Banrsai Das Regional  Engg.College,LKO) | Sep 2007  to  Dec 2010 | Lab Instructor | Teaching and demonstrate practical of students. |
| 4. | Sagar Inst. of Tech. and  Mang. LKO | July 2005 To  Aug 2007 | Lab Instructor | Teaching and demonstrate practical of students. Lab Incharge |
| 5. | Electro-Med Pvt. Ltd | July 2004 To  June 2005 | Trainee | Hardware and software testing |

**SUBJECTS TAUGHT FOR UG**

.Analog & Digital electronics, Electronic Devices and Circuit , Analog Int Circuit, Basics Electronics ,.Data communication

**PROJECT WORK**

* Automatic Hand sanitizer dispenser using Ardnino.
* Fingure print door lock
* Dark sensing automatic light
* Machine over heat detector
* Anti sleep Alarm using Ardino

**PATIENT (file)**

**REFRIGERATION AUTOMATION SYSTEM**

Date: 15/12/2021

## ADMINISTRATIVE EXPERIENCE

* Involved in central of examination at MANAV RACHNA UNIVERSITUY Faridabad
* Coordinator and admission dept., MRU Faridabad
* Member, Anti Ragging Committee, MRU Faridabad

## SHORT TERM TRAINING PROGRAMMES /FACULTY DEVELOPMENT PROGRAMMES/SEMINARS/SYMPOSIUMS/WORKSHOPS ATTENDED

|  |  |  |  |  |  |
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| **S.N** | | **Course title** | **Institute** | **No. of weeks** | **Duration** |
| 1. | | Self Configured Wireless and Sensor Network through ICT | National Institute of Technical Teachers Training and Research Chandigarh | 1 Week | 30/07/2018  to 03/08/2018 |
| 2. | | VLSI Design through ICT | National Institute of Technical Teachers Training and Research Chandigarh | 1 Week | 24/09/2018  to 28/09/2018 |
| 3. | | CMOS Analog Design through ICT | National Institute of Technical Teachers Training and Research Chandigarh | 1 Week | 04/02/2019  to 08/02/2019 |
| 4. | | VLSI chip design hands on using open source EDA | Ministry of Electronics and Information ech, (MeitY),Govt of  India, | 1 Week | 08/07/2019  to 12/07/2019 |
| **5** | Industry oriented STCon VLSI design Tools | | National Institute of Technical Teachers Training and Research Chandigarh | 1 Week | 21/03/2022 to 25/03/2022 |

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