a. De benne the op noetage sepression for 2-input
non-inverting summing amplifier.

9.2 countain in outpui noetage for R₁ = 33ka, R₂=22k. 2
R₃=12k. 2, R₅=68k. 2, N₁=0.2V, N₂=-0.5V and V₃=0.8V Assume These operationed amplifier. HA-II total daise daise of - 20/1/19.

Ris Disign a mighted summer using town spamps and levistors wat Emplements in following friction; Vo 11 -34, -4V2

- 51. Calculate the current through the 2.2-k Ω load in the circuit of Fig. 4.147.
- **52.** For the circuit of Fig. 4.148, calculate the current *I*.

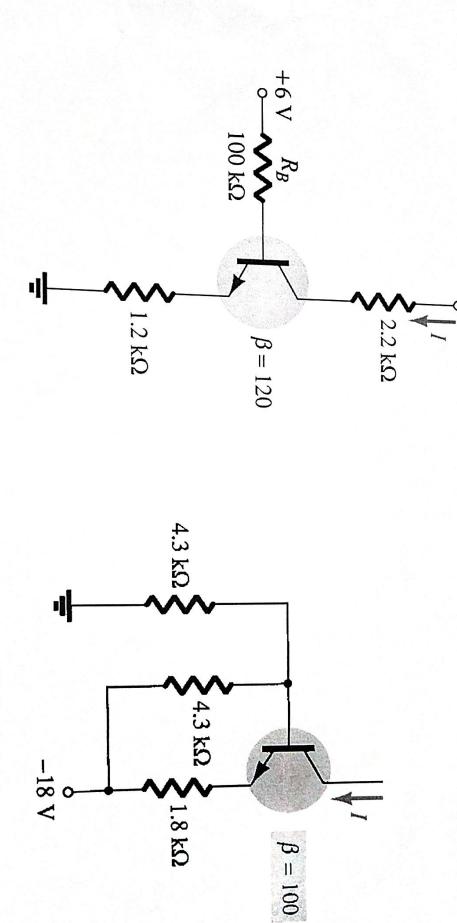


FIG. 4.147 Problem 51.

FIG. 4.148 Problem 52.

Assignment - ELAIIIO

- *43. a. Design the network of Fig. 2.187 to maintain V_L at 12 V for a load variation (I_L) from 0 mA to 200 mA. That is, determine R_S and V_Z .
- **b.** Determine $P_{Z \text{ max}}$ for the Zener diode of part (a).
- *44. For the network of Fig. 2.188, determine the range of V_i that will maintain V_L at 8 V and not exceed the maximum power rating of the Zener diode.

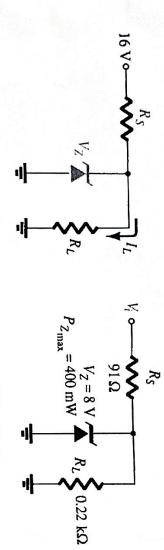
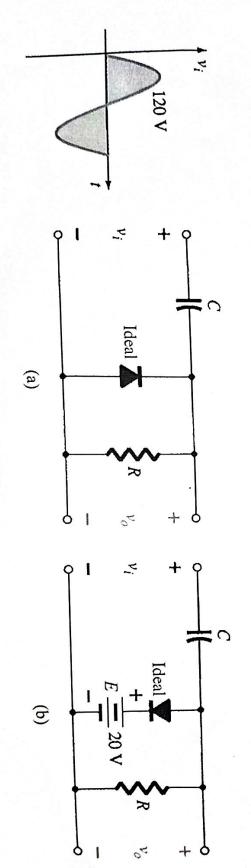


FIG. 2.187

FIG. 2.188

38. Sketch v_o for each network of Fig. 2.182 for the input shown.



Problem 38. FIG. 2.182