

ELA - 2110

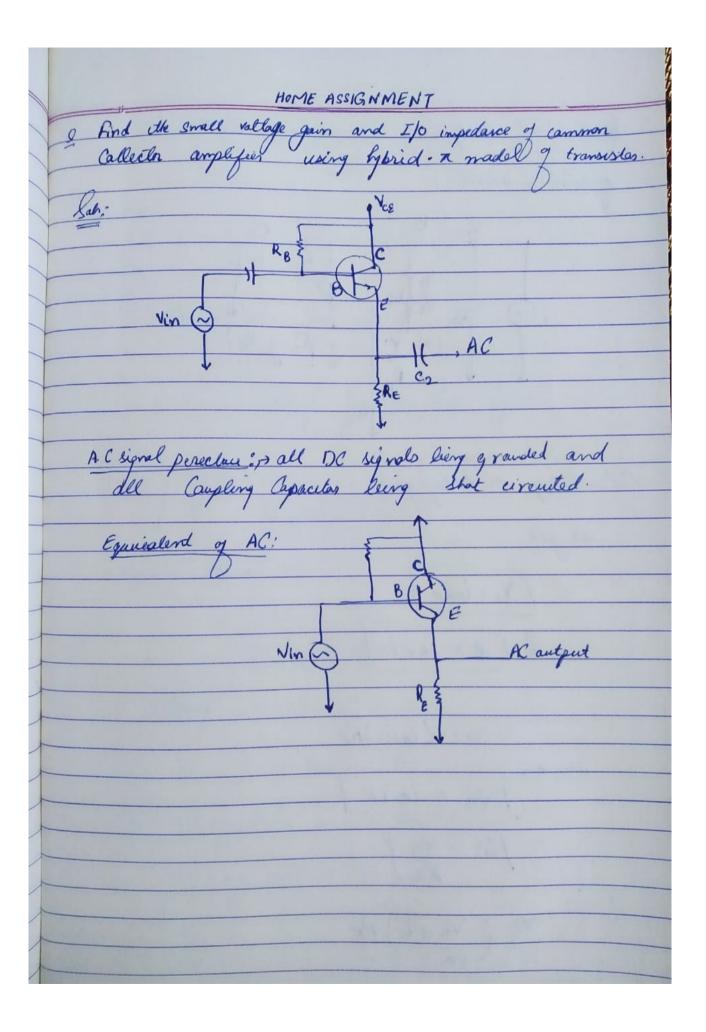
ELECTRONICS DEVICES AND CIRCUITS

HOME ASSIGNMENT 2

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Small signal Analysis of Camman Eneder BJT Hybrid - X - Model Calculating the Vallage gain Yo = ieRe & lie = iB+ic Vo= (iBtic) RE / Vx = iBYX

... un get, Vo = Va [1 + 9m] Re as /ie=9m Va] -> (2) Sine, $|\vec{Y}_{\pi} = \vec{B}| \rightarrow 1 = 9m$ $gm \mid \vec{y}_{\pi} = \vec{B}$ on substite gm in 2 Vo = Vn / gm + gm/ke p gm << gm me con neglect it 1/ Vo = Vx [gm] RE -3 Applying KVL in (a) Vin - Vx = Vo -11 Vin = Vo+ Vx] - (4) Substituy Va from (In () V: = (Vin-Vo)[gm] RE · · VotgmREVO = Vin gmRE Vallage = Vo - GmRE = 1 1 +1

Jam Vin 1+9mRE gmRE gmRE

gmuRE, Rever Av (vallage gars) ≈ RE =1
RE Since No negative signs Wherefore Input aufpel Signals are in phase. For Output Inpedace: On Shortcht all independe Sauces and Resistan Vx = Zo / (aut port Impedace) Applying KCL at E junction, $\frac{V_X + V_X}{V_R} = T_X + ic$ Vx + Vx = Ix+ gm Va & /Vx = -Vx/ fram KNLI

