

Ex 1 The transistor in the circuit shown in fig.1. has $\beta = 100$ and exhibits a ' V_{BE} ' of 0.7V at ' $i_c = 1\text{mA}$ '. Design the circuit so that a current of 2mA flows through the collector and a voltage of +5V appears at the collector.

Given.

$$\begin{aligned} \beta &= 100 \\ V_{BE} &= 0.7 \\ i_c &= 1\text{mA} \end{aligned}$$

Here design means we have to find the values of ' R_c ' & ' R_E '

* V_c = Voltage at collector

V_E = Voltage at emitter.

V_B = Voltage at base

V_{BE} = Voltage between base and emitter.

V_{CE} = Voltage between collector and emitter.

I_c = Current through collector.

I_E = " " " emitter.

V_{BE} and V_{CE} is given as:

$$V_{BE} = V_B - V_E$$

$$V_{CE} = V_C - V_E$$

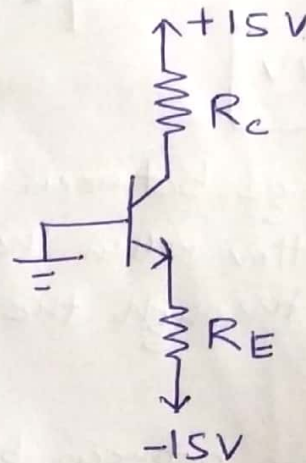


fig.1.

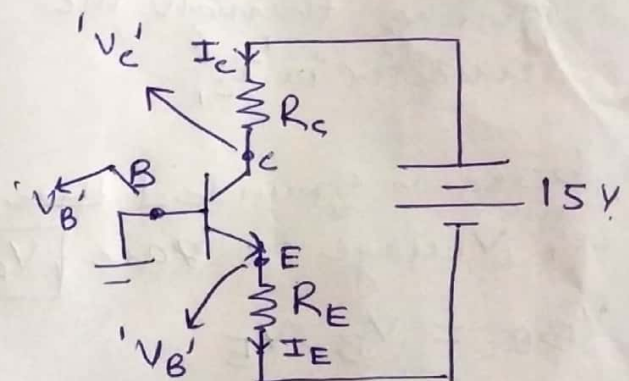


fig 1'

consider fig 2,

~~fig 2~~

Current through collector, $I_c = \frac{15 - V_c}{R_c}$

for $V_c = 5\text{V}$, I_c is given as 2mA

$$\therefore R_c = \frac{15 - 5}{2\text{mA}}, \quad \boxed{R_c = 5\text{k}\Omega}$$

Since $V_{BE_1} = 0.7V$ at $I_{C_1} = 1mA$, the value of V_{BE_2} at $I_{C_2} = 2mA$ Page (2)

$$V_{BE_2} = V_{BE_1} + V_T \ln \left[\frac{I_{C_2}}{I_{C_1}} \right]$$

$$V_{BE_2} = 0.7 + (25 \times 10^{-3}) \ln \left[\frac{2mA}{1mA} \right]$$

$$\boxed{V_{BE_2} = 0.717V}$$

* V_{BE_2} = voltage between base and emitter when the current flowing through the collector is I_{C_2}

* V_{BE_1} = voltage between base & emitter when the current flowing through the collector is I_{C_1}

Now, base is grounded ~~then~~ here
so, Voltage at base $\boxed{V_B = 0}$

$$V_{BE} = V_B - V_E$$

$$V_{BE} = 0 - V_E$$

$$\boxed{V_{BE} = -V_E} \quad (\text{for both the cases, either } I_{C_1} \text{ or } I_{C_2})$$

$$V_{BE_2} = -V_{E_2}$$

$$\boxed{V_{E_2} = -0.717V}$$

~~Since, the base is grounded, the emitter is at a negative voltage. The collector is at a positive voltage. The emitter-base junction is forward biased and the collector-base junction is reverse biased.~~

$$\alpha = \frac{\beta}{\beta + 1} = \frac{100}{101} = \boxed{\alpha = 0.99}$$

Rough work

As we know that

$$I_C = I_B e^{V_{BE}/V_T}$$

$$\text{So, } I_{C_1} = I_{B_1} e^{V_{BE_1}/V_T} \quad \text{--- (1)}$$

$$I_{C_2} = I_{B_2} e^{V_{BE_2}/V_T} \quad \text{--- (2)}$$

on dividing eqn (2) by (1)

$$\frac{I_{C_2}}{I_{C_1}} = e^{(V_{BE_2} - V_{BE_1})/V_T}$$

$$\ln \left[\frac{I_{C_2}}{I_{C_1}} \right] = \frac{V_{BE_2} - V_{BE_1}}{V_T}$$

$$V_T \ln \left[\frac{I_{C_2}}{I_{C_1}} \right] = V_{BE_2} - V_{BE_1}$$

$$\boxed{V_{BE_2} = V_{BE_1} + V_T \ln \left[\frac{I_{C_2}}{I_{C_1}} \right]}$$

Given α The collector current is

Page (3)

$$I_c = \alpha I_E$$

$$I_{c_2} = 0.99 I_{E_2}$$

$$I_{E_2} = \frac{2 \text{ mA}}{0.99}$$

$$I_{E_2} = 2.02 \text{ mA}$$

The current through the emitter can be determined as

$$I_{E_2} = \frac{V_{E_2} - (-15)}{R_E}$$

$$R_E = \frac{V_{E_2} - (-15)}{I_{E_2}}$$

$$R_E = \frac{-0.717 + 15}{2.02}$$

$$R_E = 7.07 \text{ k}\Omega$$

Ex. 2 In the circuit shown in fig. 2, the voltage at the emitter was measured and found to be -0.7 V . If $\beta = 50$, find I_E , I_B , I_C and V_C .

Given $\beta = 50$

$$V_E = -0.7 \text{ V}$$

As base is grounded, $V_B = 0$

To find I_E , I_B , I_C & V_C .

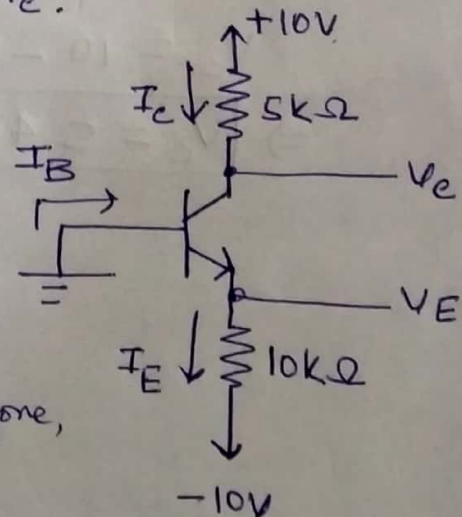


Fig. 2

Solution:- As Base is grounded, therefore,

$$V_B = 0$$

and we can find the

current I_C

$$I_C = \alpha I_E$$

$$\alpha = \frac{\beta}{\beta+1}, \quad \frac{50}{51} = \boxed{0.98 = \alpha} \quad \text{Page ④}$$

→ ~~To find~~ Emitter current I_E can be determined as

$$I_E = \frac{V_E - (-10)}{R_E} = \frac{-0.7 + 10}{10} = 0.93 \text{ mA}$$

$$\boxed{I_E = 0.93 \text{ mA}}$$

$$\therefore I_C = \alpha I_E = 0.98 \times 0.93 = 0.9114 \text{ mA}$$

$$\boxed{I_C = 0.9114 \text{ mA}}$$

As we know that,

$$I_E = I_C + I_B$$

$$I_B = I_E - I_C \Rightarrow 0.93 - 0.9114 = 0.0186 \text{ mA}$$

$$\boxed{I_B = 18.6 \mu\text{A}}$$

Similarly,

$$I_C = \frac{10 - V_C}{R_C}, \quad I_C R_C = 10 - V_C$$

$$\Rightarrow V_C = 10 - I_C R_C \Rightarrow 10 - (0.9114 \text{ mA} \times 5 \text{ k}\Omega)$$

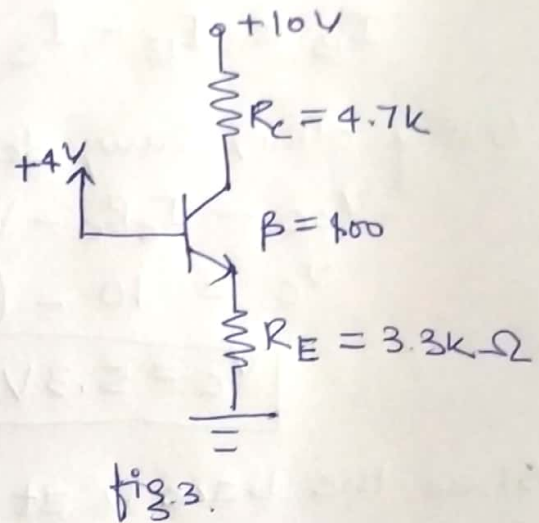
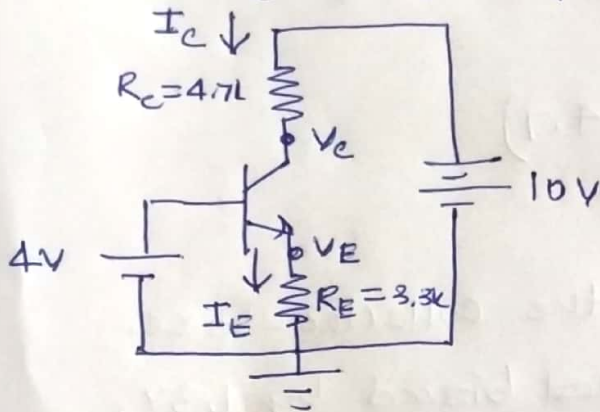
$$V_C = 10 - 4.557$$

$$\Rightarrow \boxed{V_C = 5.44 \text{ V}}$$

Ex 3 Analyze the circuit shown in fig. 3. Determine all the node voltages and branch current. Assume $\beta = 100$.

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Solution fig. 3 ~~is~~ can be redrawn as



Soln Let us assume that the transistor is in active mode

$$V_{BE} = 0.7V$$

$$V_{BE} = V_B - V_E$$

$$0.7 = 4 - V_E, \quad \boxed{V_E = 3.3V}$$

We know the voltages at the two end of the resistor R_E , therefore it is easy to find current I_E

$$I_E = \frac{V_E - 0}{R_E} \Rightarrow \frac{3.3}{3.3k} = \boxed{1mA = I_E}$$

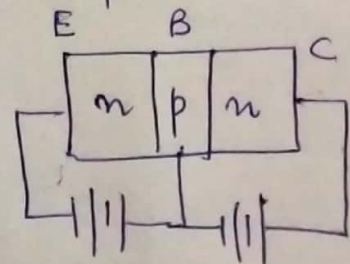
Since the collector is ~~not~~ connected through R_C to the +10V supply, it appears ~~that~~ possible that the collector voltage will be higher than the base voltage, which is essential for active mode operation.

Assuming this is the case, we can evaluate the collector current,

$$I_C = \alpha I_E$$

The value of α can be obtained as

$$\alpha = \frac{\beta}{1 + \beta} = \frac{100}{101} \approx \boxed{0.99 = \alpha}$$



$$I_c = 0.99 \times 1 = \boxed{0.99 \text{ mA} = I_c}$$

$$\sim I_E = I_c + I_B$$

$$I_B = I_E - I_c = 1 - 0.99 \Rightarrow \boxed{0.01 \text{ mA} = I_B}$$

Using Ohm's law, determine the voltage V_c .

$$V_{CC} - I_c R_c - V_c = 0$$

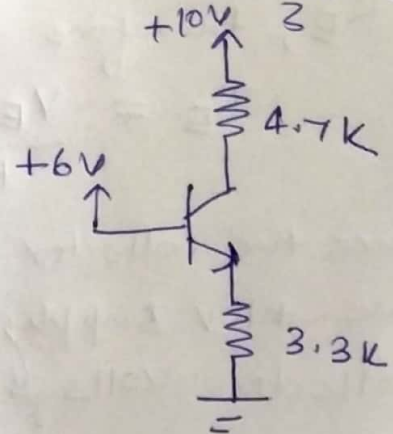
$$V_c = 10 - (0.99 \times 4.7)$$

$$\boxed{V_c = 5.3 \text{ V}}$$

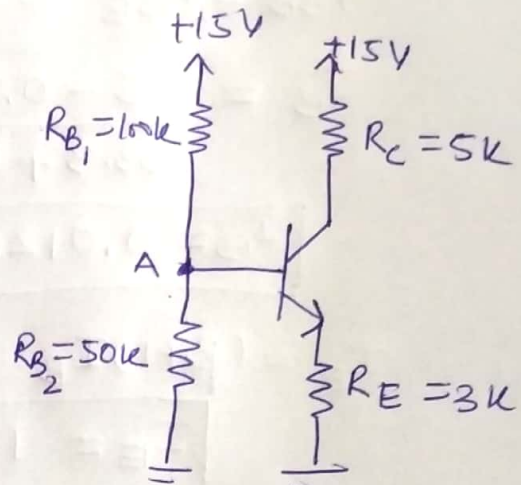
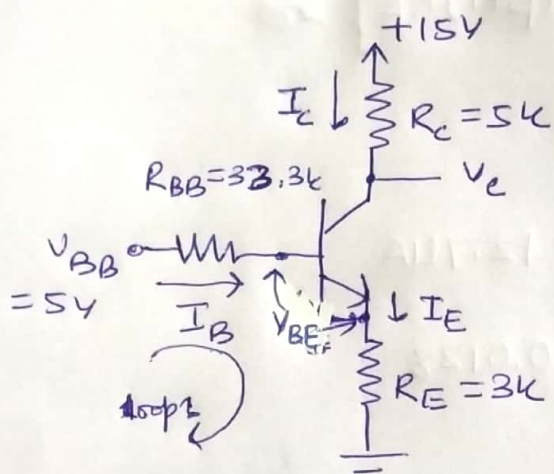
Since the base is at +4V, the collector-base junction is Reverse-biased by 1.3V, and the transistor is indeed in the active mode as assumed.

$$V_B - V_c \Rightarrow 4 - 5.3 \\ \Rightarrow -1.3 \text{ V}$$

EX 4 For the fig. shown below, determine the voltages at all nodes and current through all branches.



Ex. 5 For the circuit shown in fig 5. Determine the voltages at all nodes and the current through all branches. Assume $\beta = 100$.



fig's

→ using voltage divider / Thevenin's theorem we can find the voltage at node, and redraw circuit shown in fig 5'

$$V_{BB} = 15 \times \frac{R_{B2}}{R_{B1} + R_{B2}} = 15 \times \frac{50}{50 + 100}$$

$$V_{BB} = +5V$$

$$R_{BB} = (R_{B1} // R_{B2}) = \left[\frac{100 \times 50}{100 + 50} \right] = 33.33k\Omega$$

Apply KVL in loop 1

$$V_{BB} - I_B R_{BB} - V_{BE} - I_E R_E = 0 \quad \text{--- (1)}$$

as we know that

$$I_E = I_C + I_B$$

$$I_E = \beta I_B + I_B$$

$$I_E = (\beta + 1) I_B \quad \text{--- (2)}$$

From eqn (2) put the value of I_E in eqn (1)

$$V_{BB} - I_B R_{BB} - V_{BE} - (\beta + 1) I_B R_E = 0$$

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$$I_B (R_{BB} + (\beta + 1) R_E)$$

$$V_{BB} - V_{BE} = I_B [R_{BB} + (\beta + 1) R_E]$$

$$I_B = \frac{5 - 0.7}{33.3 + (101 \times 3)}$$

$$I_B = 0.0128 \text{ mA} = 12.7 \mu\text{A}$$

$$\therefore I_E = (\beta + 1) I_B = 101 \times 0.0128$$

$$I_E = 1.29 \text{ mA}$$

as $I_E = I_C + I_B$

$$I_E = I_C + I_B \Rightarrow 1.29 - 0.0128$$

$$I_C = 1.28 \text{ mA}$$

To find the V_B ,

$$I_B = \frac{V_{BB} - V_B}{R_{BB}}, \quad V_B = V_{BB} - I_B R_{BB}$$

$$V_B = 5 - [0.0128 \times 33.3]$$

$$V_B = 5 - 0.426$$

$$V_B = 4.57 \text{ V}$$

Similarly, we can find V_C and V_E

$$I_E = \frac{V_E - 0}{R_E}, \quad V_E = I_E R_E = 1.29 \times 3$$

$$V_E = 3.87 \text{ V}$$

$$I_C = \frac{V_{CC} - V_C}{R_E}, \quad V_C = V_{CC} - I_C R_E$$

$$V_C = 15 - (1.28 \times 5)$$

$$V_C = 8.6 \text{ V}$$

$$V_B - V_C = 4.57 - 8.6$$

$$\Rightarrow -4.03 \text{ V}$$

Thus, the collector is at higher potential by base by 4.03 V which means transistor is in active mode, as assumed.