

# High-speed Waveguide Modified Uni-Traveling Carrier Photodiodes with 130 GHz Bandwidth

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**Abstract**—We demonstrate evanescently-coupled waveguide modified uni-traveling carrier (MUTC) photodiodes with an external responsivity of 0.12 A/W, and a 3-dB bandwidth of 130 GHz.

**Keywords**—waveguide photodiode, high-speed photodetector, MUTC

## I. INTRODUCTION

The development of terahertz (THz) technology has driven the demand for high-speed and high-power photodiodes (PDs). Uni-traveling carrier photodiodes (UTC-PDs) [1], with only electrons as the active carriers in the drift layer [2], are one of the most promising options for applications such as mm-wave communication and photonic microwave generation [3]. The way of optical coupling could significantly influence the performance of the UTC PDs. For surface-illuminated PDs, since the carrier transport and the optical coupling are both in the vertical direction, it is challenging to achieve short carrier transit time and high responsivity simultaneously [4]. In contrast, waveguide PDs can effectively eliminate the bandwidth-responsivity tradeoff by coupling the light laterally into the active region of the device through an optical waveguide [5]. In this work, we demonstrate an evanescently-coupled waveguide modified uni-traveling carrier (MUTC) PD with optimized high impedance coplanar waveguide (CPW) designs. The device shows a 3dB bandwidth of 130 GHz and external responsivity of 0.12 A/W.

## II. DEVICE STRUCTURE AND FABRICATION

Fig. 1 (a) shows the designed epitaxial structure of the InGaAs/InP wafer. The structure was grown on the semi-insulating InP substrate. The epitaxial growth began with a total 600 nm heavily n-doped InP contact layer and a 400 nm InGaAsP waveguide layer. Then, an InP drift layer was deposited as the charge compensation layer and cladding for the waveguide, and a cliff layer was inserted in the collector to reduce the effect of space charge effect at high current levels [6]. The 180 nm thick InGaAs absorption layer consists of a depleted lightly n-doped layer and a graded doped p-type layer that form a quasi-electric field to assist electron transport. Finally, the structure was capped by a heavily p-doped InP diffusion block layer and an InGaAs contact layer. To smooth energy band discontinuities, InGaAsP quaternary layers were deposited at InGaAs/InP heterojunction interfaces.

The device was fabricated into a double-mesa structure, as shown in Fig. 1 (b). The p-mesa and the passive waveguide were formed by an inductively coupled plasma (ICP) dry etch process. The second mesa was terminated at the semi-

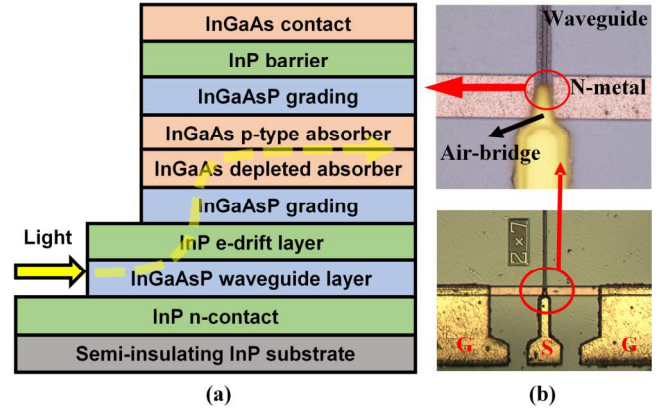


Fig. 1. (a) Epi-layer structure of the InGaAs/InP photodiode. (b) Micrograph of a fabricated device.

insulating InP substrate by wet etch ( $\text{H}_3\text{PO}_4:\text{HCl} = 1:3$ ) to ensure electrical isolation between devices. Ti/Pt/Au/Ti and GeAu/Ni/Au were deposited on the p-type and n-type contact layers by electron beam evaporation, respectively. A good ohmic contact was formed by the rapid thermal annealing process. The photodiodes were connected to gold-plated CPW pads through an air-bridge. Apart from CPWs with 50  $\Omega$  characteristic impedance, high impedance transmission lines were also fabricated to introduce inductive peaking to improve 3dB bandwidths.

## III. CHARACTERIZATION

The dark current versus bias voltage characteristics for devices with active areas of  $2 \times 20 \mu\text{m}^2$ ,  $2 \times 15 \mu\text{m}^2$ ,  $2 \times 10 \mu\text{m}^2$  and  $2 \times 7 \mu\text{m}^2$  at room temperature are shown in Fig. 2. Under  $-5 \text{ V}$  bias, typical dark currents are in the range of  $10^{-6} \text{ A}$ , which is mainly due to the surface leakage caused by dry etch process.

The frequency response of the InGaAs/InP waveguide devices was investigated by an optical heterodyne setup [7]. Two tunable lasers with wavelengths near  $1.55 \mu\text{m}$  were coupled together to generate a beat signal with a frequency from hundreds of megahertz to more than 140 GHz. The modulated optical signal at  $1.55 \mu\text{m}$  wavelength was coupled into the device by a lensed fiber. The measurements were conducted in two frequency ranges. From DC to 110 GHz, the RF signal was collected by the Rohde & Schwarz power meter (NRP-Z58) through the GSG Model 110H probe, while the DC bias was provided by the source meter through a 110 GHz bias tee. Frequency-dependent loss value introduced by cables, bias tee and GSG probe was calibrated by a vector network analyzer (VNA) with corresponding calibration kit (85058E & CS-5 for DC to 67 GHz, WR10-VDI & CS-5 for

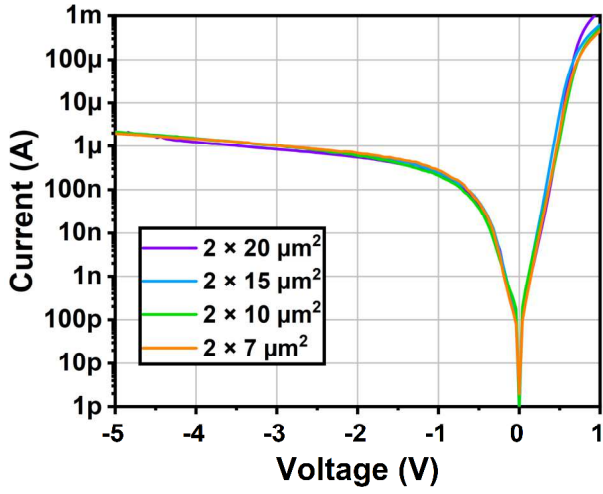


Fig. 2. Dark current versus bias voltage characteristic for devices with various areas at room temperature.

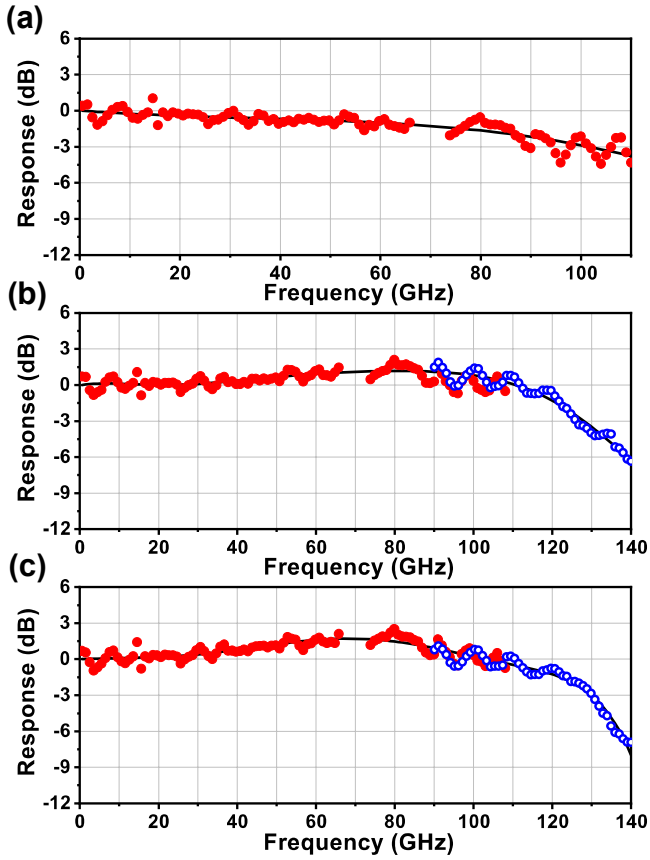


Fig. 3. Frequency response of the (a) 50 Ω load device with  $2 \times 10 \mu\text{m}^2$  area, (b) high impedance device with  $2 \times 10 \mu\text{m}^2$  area and (c) high impedance device with  $2 \times 7 \mu\text{m}^2$  area under  $-2.5 \text{ V}$  bias and 4mA photocurrent. (red points: data from DC to 110 GHz, blue points: data from 90 GHz to 140 GHz, solid line)

73.8 GHz to 110 GHz). From 90 GHz to 140 GHz, a GGB Model 140 waveguide probe with an integrated bias tee was used and connected to a VDI power meter (PM5). The loss file provided by the manufacturer was included in the measurement results. The external responsivity of the  $2 \times 7 \mu\text{m}^2$  device is 0.12 A/W.

Theoretically, the 3dB bandwidth of the photodiode is limited by transit time and RC time, as expressed by the equation [8]:

$$\frac{1}{f_{3dB}^2} = \frac{1}{f_{RC}^2} + \frac{1}{f_{tr}^2} \quad (1)$$

where  $f_{3dB}$ ,  $f_{RC}$ ,  $f_{tr}$  are the total 3-dB bandwidth, RC-limited bandwidth, and transit-time-limited bandwidth, respectively. Fig. 3 shows the normalized frequency response of PDs with different CPW designs under  $-2.5 \text{ V}$  bias at a photocurrent of 4 mA. For the  $2 \times 10 \mu\text{m}^2$  device with 50 Ω load, the 3dB bandwidth is 100 GHz. The  $2 \times 7 \mu\text{m}^2$  and  $2 \times 10 \mu\text{m}^2$  devices with high impedance CPWs show 3dB bandwidths of 130 GHz and 125 GHz respectively. The red and blue points, representing experimental data in two different frequency ranges, show good overlap from 90 GHz to 110 GHz. The high impedance transmission line incorporates an inductor into the PD circuit, resulting in a peaking effect at around 80 GHz, which greatly improves device bandwidths.

To further investigate the bandwidth limiting factor of devices, the scattering parameters (S11) of waveguide PDs were measured by VNA. Parameter fitting was conducted in Advanced Design System (ADS) software with the equivalent circuit as depicted in Fig. 4.  $R_s$ ,  $R_p$  and  $C_{PD}$  represent the series resistance, junction resistance and photodiode capacitance, respectively.  $C_t$  and  $L_t$  denote the capacitance and inductance of the high impedance CPW, respectively.

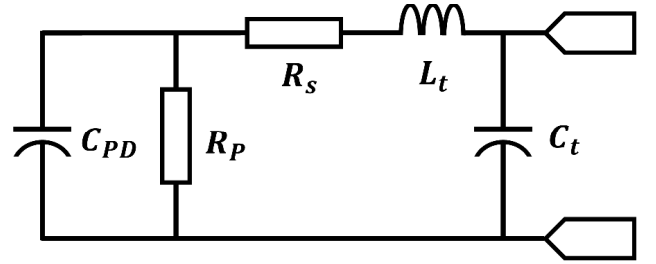


Fig. 4. Equivalent circuit model of the UTC device for S11 fitting.

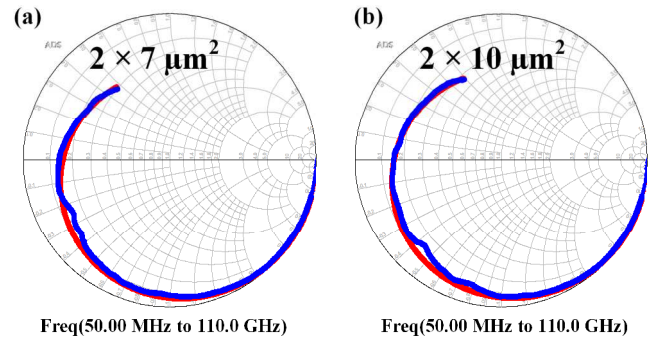


Fig. 5. Measured (blue line) and fitted (red line) S11 data of (a)  $2 \times 7 \mu\text{m}^2$  and (b)  $2 \times 10 \mu\text{m}^2$  devices with 50 MHz-110 GHz frequency range under  $-2.5 \text{ V}$  bias.

TABLE I. FITTED PARAMETERS

Area ( $\mu\text{m}^2$ )	$R_s$ ( $\Omega$ )	$C_{PD}$ (fF)	$C_t$ (fF)	$L_t$ (pH)
$2 \times 7$	7.7	26.1	21.7	107.2
$2 \times 10$	7.6	28.2	21.7	105.8
$2 \times 15$	7.2	32.2	20.1	112.7
$2 \times 20$	7.0	36.5	19.5	114.7

The fitting curves and the measured S11 data in the smith chart for the  $2 \times 7 \mu\text{m}^2$  and  $2 \times 10 \mu\text{m}^2$  devices under  $-2.5 \text{ V}$  are shown in Fig. 5. The corresponding extracted data is summarized in Table I. The fitted junction resistance ( $R_p$ ), typically on the order of  $500 \text{ M}\Omega$ , is not listed in the table. The extracted photodiode capacitance ( $C_{PD}$ ) is composed of junction capacitance ( $C_{pn}$ ) and parasitic capacitance ( $C_{st}$ ). A  $C_{st}$  of  $20.3 \text{ fF}$  was found by plotting the  $C_{PD}$  versus device areas, determined from the intercept of the linear fit, as shown in Fig. 6. Since  $C_{st}$  accounts for most of  $C_{PD}$ , it is the major limiting factor of RC time.

Based on the circuit model with the extracted parameters,  $f_{RC}$  can be calculated, and then the theoretical  $f_{tr}$  can be derived according to (1). The transit-time-limited bandwidth

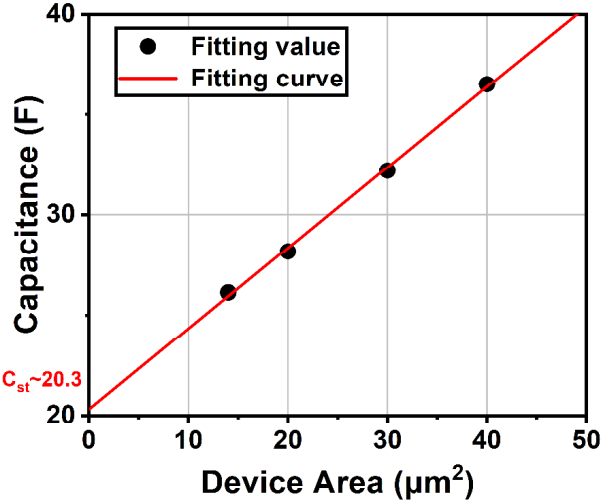


Fig. 6 Fitting capacitance of devices under  $-2.5 \text{ V}$  bias and linear fit.

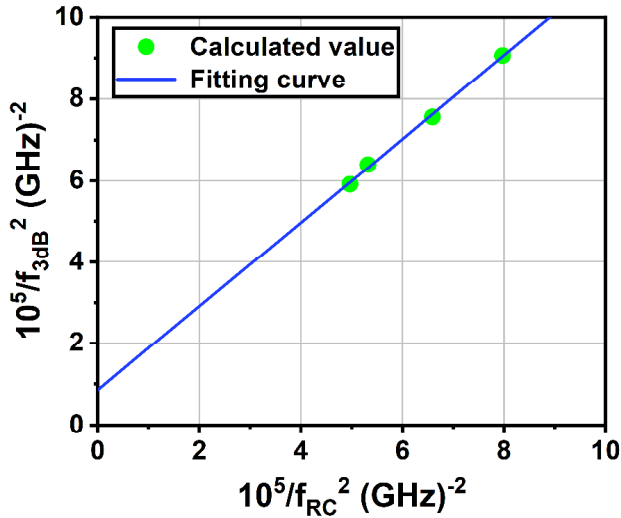


Fig. 7. Calculated  $10^5/f_{3dB}^2$  versus  $10^5/f_{RC}^2$  under  $-2.5 \text{ V}$  bias and linear fit.

is determined to be  $342 \text{ GHz}$  from the intercept of the linear fit by plotting  $10^5/f_{3dB}^2$  versus  $10^5/f_{RC}^2$  (see Fig. 7). It can be observed that the bandwidth is limited by  $f_{RC}$ , indicating that further optimization of the fabrication process is needed to reduce the parasitic capacitance.

#### IV. CONCLUSION

In this work, we have reported InGaAs/InP based high-speed waveguide MUTC PDs operating at  $1550 \text{ nm}$  wavelength with a responsivity of  $0.12 \text{ A/W}$ . The  $3 \text{ dB}$  bandwidths under  $-2.5 \text{ V}$  bias of the  $2 \times 7 \mu\text{m}^2$  devices reached  $130 \text{ GHz}$  with carefully designed high impedance CPW.

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