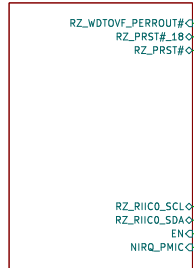


PMIC: Control and Reset



File: control_reset.kicad_sch

BUCK and LDO



File: buck_ldo.kicad_sch

LiPo Charger



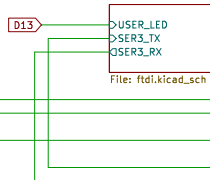
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RZ SoC Power

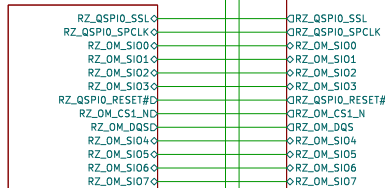


Sheetfile: power.kicad_sch

FTDI



Flash and RAM



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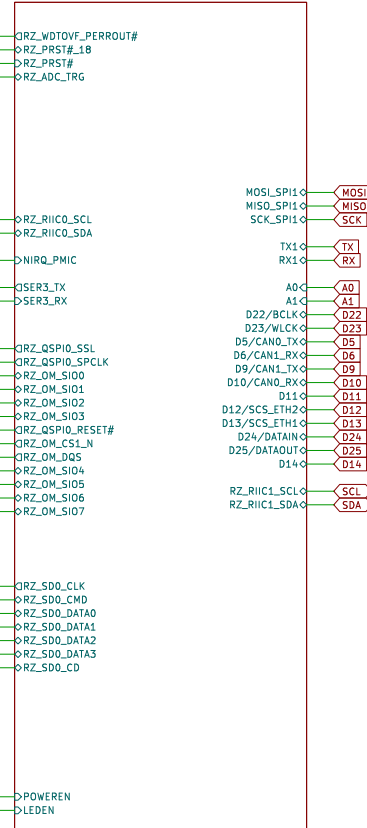


CSI

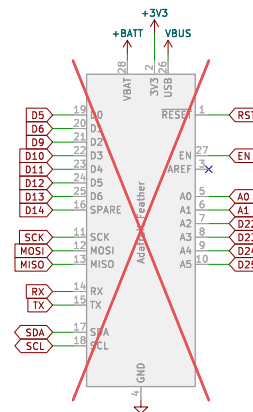


File: csi.kicad_sch

RZ SoC IO



Feather Pinout



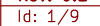
Zalnotek

Sheet: /
File: Feather-RZA3UL.kicad_sch

Title: RZ/A3UL Feather SoM

Size: A3 Date: 2023-12-18
KiCad E.D.A. 8.0.9

Rev: 0.1
Id: 1/9

[illegible]

MIPI CSI Camera

The schematic diagram illustrates the MIPI CSI Camera interface. It shows a camera module (IC1C, R9A07G063U01GBG#AC0) connected to a system with +1V8 and +3V3 power rails. The camera module has pins for CSI_VDD18_1, CSI_VDD18_2, CSI_CLKP, CSI_CLKN, CSI_DATA0_P, CSI_DATA0_N, CSI_DATA1_P, CSI_DATA1_N, CSI_DATA2_P, CSI_DATA2_N, CSI_DATA3_P, and CSI_DATA3_N. These are connected to corresponding pins on the system (AD11, AE11, AD12, AE12, AD10, AE10, AD13, AE13, AD9, AE9). The system also has pins for RZ_CSI_CLK_P, RZ_CSI_CLK_N, RZ_CSI_DATA0_P, RZ_CSI_DATA0_N, RZ_CSI_DATA1_P, RZ_CSI_DATA1_N, RZ_CSI_DATA2_P, RZ_CSI_DATA2_N, RZ_CSI_DATA3_P, and RZ_CSI_DATA3_N. The system is powered by +1V8 and +3V3, with a 100nF capacitor (C11) on the +1V8 line and a 100nF capacitor (C8) on the +3V3 line. The camera module is connected to the system via a 501951-2230 J2 connector.

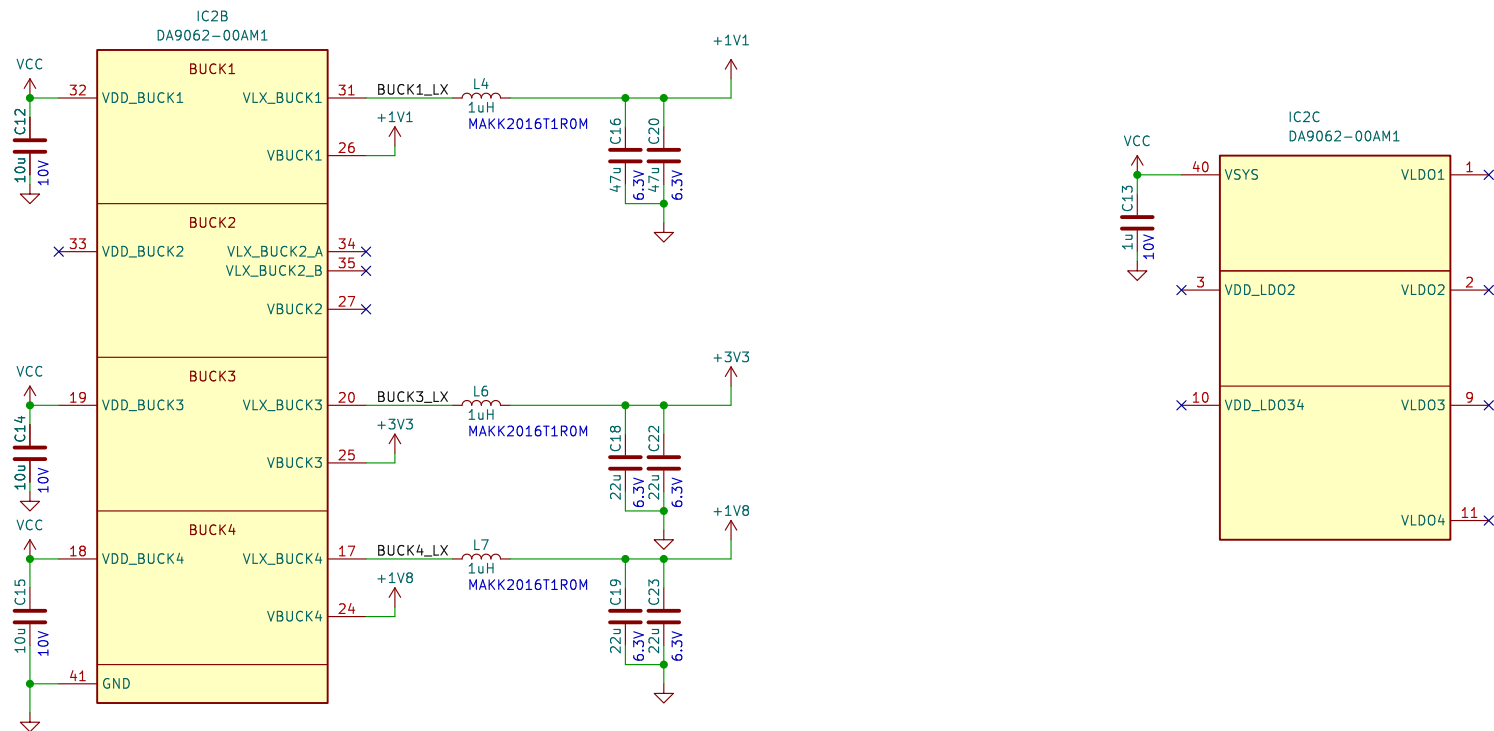
Zalmorek
Sheet: /CSI/
File: csi.kicad_sch

Title: RZ/A3UL Feather SoM

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| Size: A4 | Date: 2023-12-18 | Rev: 0.1 |
| KiCad E.D.A. 8.0.9 | Id: 2/9 | |

Id: 2/9

PMIC Buck and LDO



Zalotek

Sheet: /BUCK and LDO/

File: buck_ldo.kicad_sch

Title: RZ/A3UL Feather SoM

Size: A4 Date: 2023-12-18

KiCad E.D.A. 8.0.9

Rev: 0.1

Id: 3/9

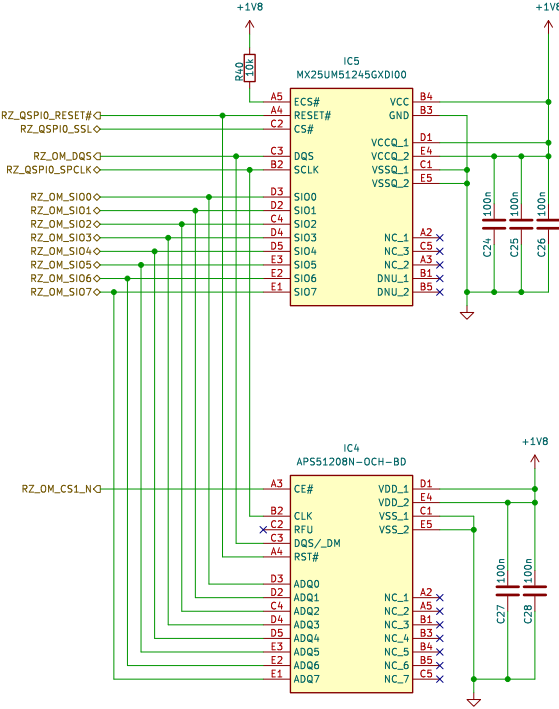
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File: power.kicad_sch

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| Size: A3 | Date: 2023-12-18 |
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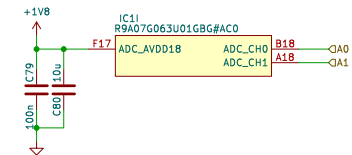
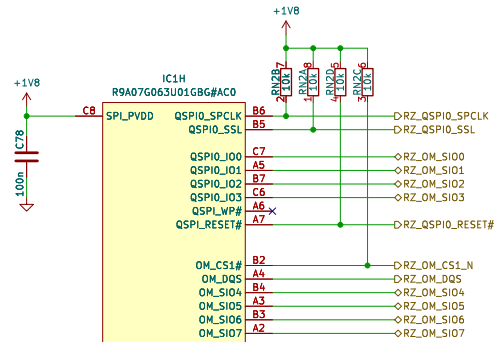
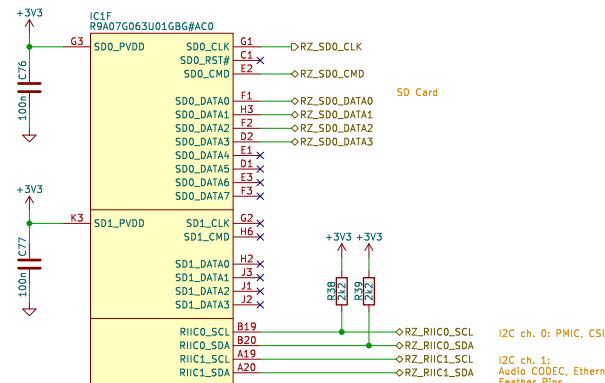
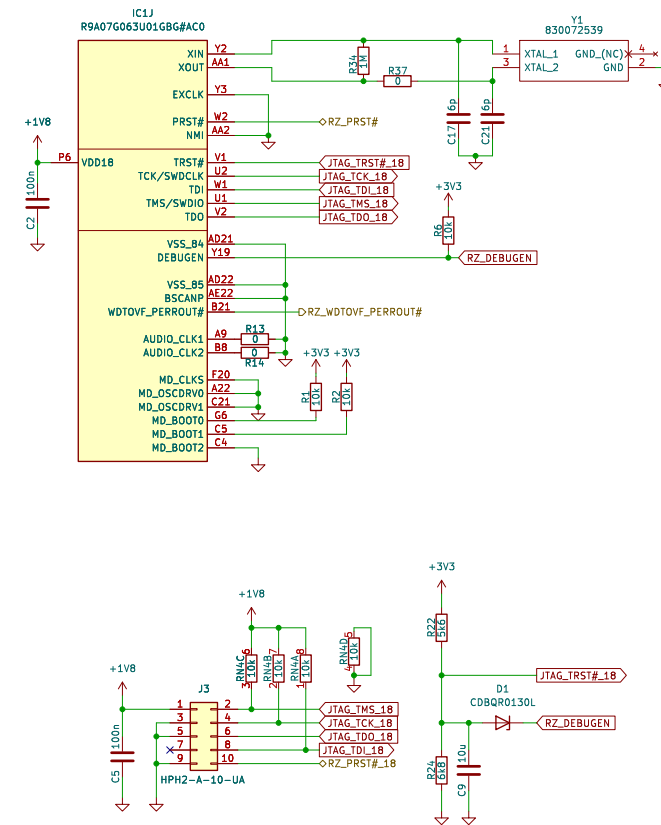
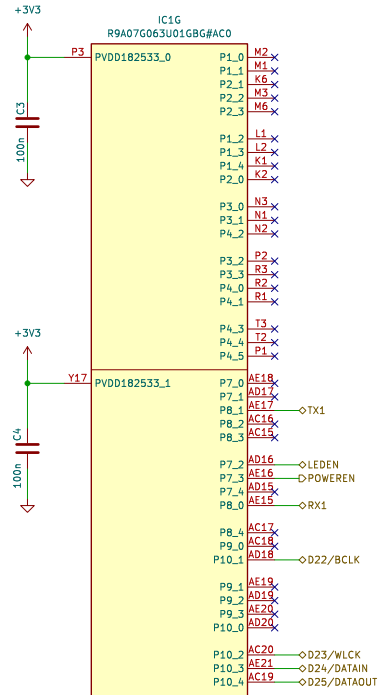
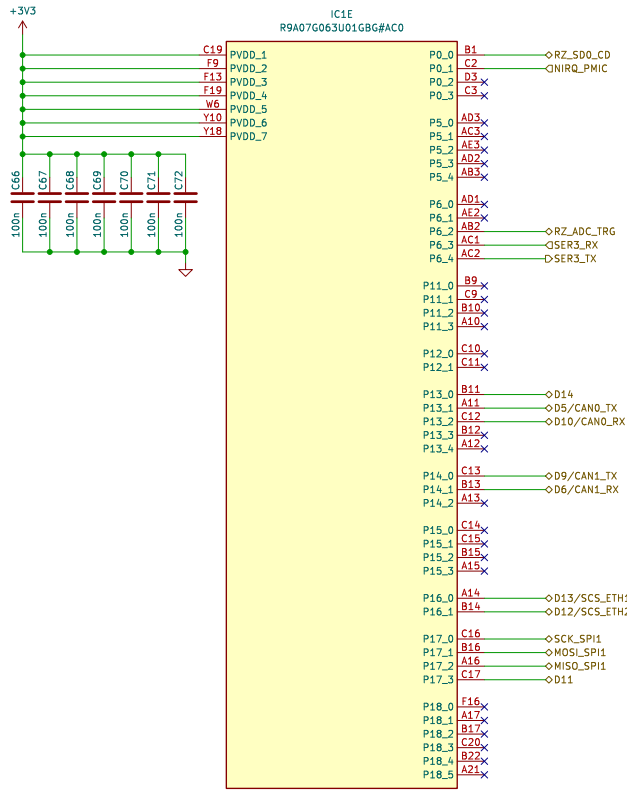
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| Size: A3 | Date: 2023-12-18 |
| KiCad E.D.A. 8.0.9 | |

Rev: 0.1
Id: 5/9

OctaFlash and OctaRAM



RZ IO



I2C ch. 0: PMIC, CSI
I2C ch. 1: Audio CODEC, Ethernet Transceiver (CB), Feather Pins
*Feather specification:
Pull ups must be placed on the Carrier Board

Zaltek

Sheet: /RZ SoC IO/
File: RZ_Soc_IO.kicad_sch

Title: RZ/A3UL Feather SoM

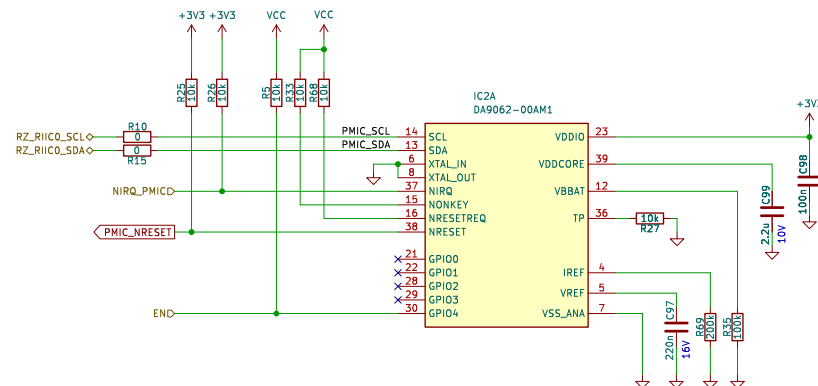
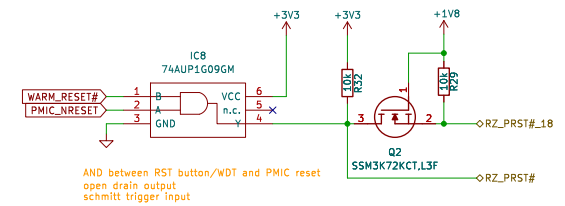
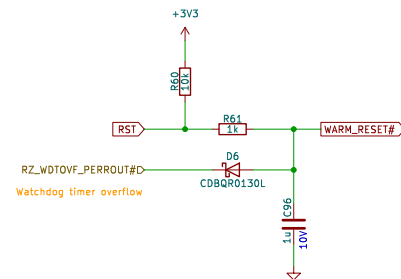
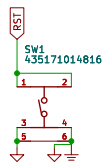
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Rev: 0.1

Id: 8/9

PMIC Control and Reset



Zalmotek

Sheet: /PMIC: Control and Reset/

File: control_reset.kicad_sch

Title: RZ/A3UL Feather SoM

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| Size: A3 | Date: 2023-12-18 |
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Rev: 0.1

Id: 9/9

FTDI

