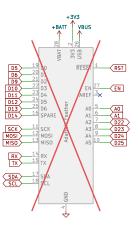
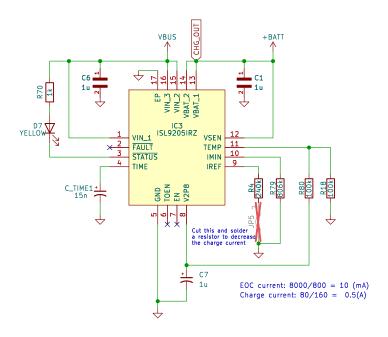


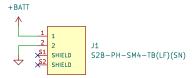
### Feather Pinout

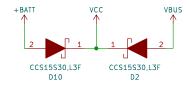


Zalmotek					
	Sheet: / File: Feather-RZA3UL.kicad_sch				
Title: RZ/A3UL Feather SoM					
	Size: A3	Date: 2023-12-18	Rev: 0.1		
KICad E.D.A. 8.0.9			ld: 1/9		

# LiPo Charging







	ote

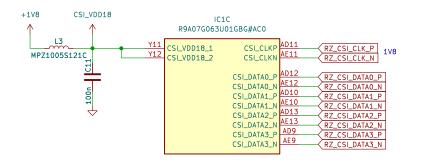
Sheet: /LiPo Charger/ File: lipo.kicad\_sch

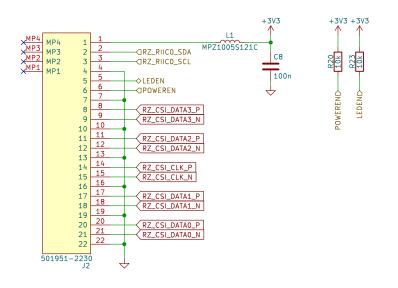
Title: RZ/A3UL Feather SoM

 Size: A4
 Date: 2023-12-18
 Rev: 0.1

 KiCad E.D.A. 8.0.9
 Id: 1/9

# MIPI CSI Camera





Za	lmot	el
-		-

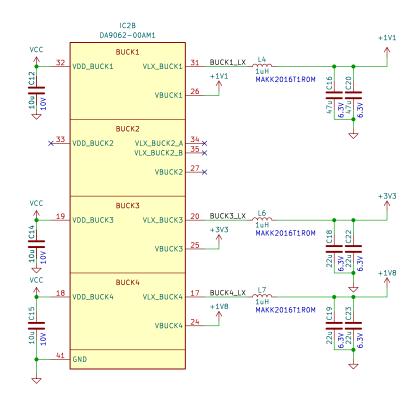
Sheet: /CSI/ File: csi.kicad\_sch

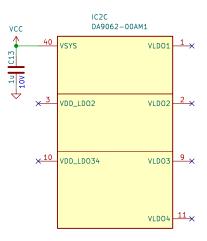
Title: RZ/A3UL Feather SoM

 Size: A4
 Date: 2023-12-18
 Rev: 0.1

 KiCad E.D.A. 8.0.9
 Id: 2/9

# PMIC Buck and LDO





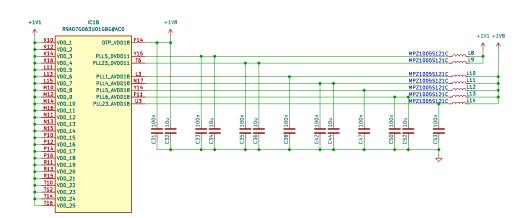
Zalmotek

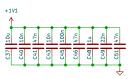
Sheet: /BUCK and LDO/ File: buck\_ldo.kicad\_sch

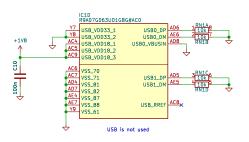
Title: RZ/A3UL Feather SoM

Size: A4 Date: 2023-12-18 Rev: 0.1 KiCad E.D.A. 8.0.9 ld: 3/9

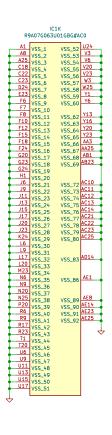
### Power







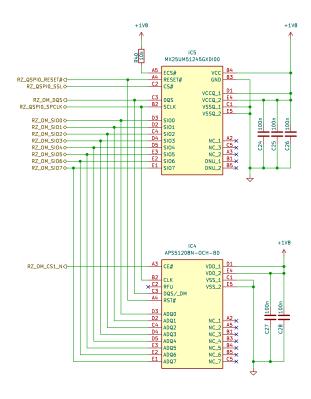
#### IC1A R9A07G063U01GBG#AC0



#20 DR. VDDL.1 DR. CLK.P P25,	R9A07G063U01GBG#AC0			
#200 DR.VDDQ.2 DDR.CLIC.N P22- #200 DDR.VDDQ.4 DDR.CLIC.N P22- #200 DDR.VDDQ.4 DDR.CSS #22- #200 DDR.VDDQ.6 DDR.RSS #22- #200 DDR.VDDQ.6 DDR.RSS #22- #200 DDR.VDDQ.6 DDR.RSS #22- #200 DDR.VDDQ.6 DDR.RSS #22- #200 DDR.DDR.DDR.DDR.DDR.DDR.DDR.DDR.DDR.DDR	H20	DDB VDDO 1	DDD CIK D	P25
BEQ   DDR.VDDQ_5   DDR.CKE   R22				P24_
#220 DOR.YODOL 6  #220 DOR.YODOL 5  DOR.REST# #224  DOR.CSS9 M24  DOR.DOR.DOR.DOR.DOR.DOR.DOR.DOR.DOR.DOR.				M25
DOR.   DOR.   DOR.   COR.				
DR_VDDQ_5  DDR_VEFF   224   224   225   226   22		DDR_VDDQ_5	DDR_RESET#	B23×
DDR_ASS  #24_ DDR_CSS  #25_ DDR_CSS  #25_ DDR_CSS  #25_ DDR_ASS  #24_ DDR_ASS  #24_ DDR_ASS  #24_ DDR_ASS  #24_ DDR_ASS  #24_ DDR_ASS  #25_ DDR_ADRIS  #25_ DD	W20		DDR_WE#	G25×
DR. CS09   N24   DR. CS19   123   DR. CS19   124   DR. DR. CS19   124   DR. DR. CS19   125   DR. ADDRS   125   DR. DRS   125   DRS   1	$\triangle$			124×
DR. CS1# 123  DR. ADR AD 624  DR. ADR AD 624  DR. ADR AD 625  DR. ADR AD 625  DR. ADR			DDR_RAS#	124×
DR. CS1# 123  DR. ADR AD 624  DR. ADR AD 624  DR. ADR AD 625  DR. ADR AD 625  DR. ADR				N24
DR. ADR 12-4- DR. ADR 13-12-4- DR. ADR				L23
DOR_BAI 1224 DOR_ADRAS 6234 DOR_ADRA			DDK_C51#	- "
DOR_BAI 1224 DOR_ADRAS 6234 DOR_ADRA			DDR BAO	E24
DR. BAD F 224  DR. ADRR 1525  DR. ADR 1525  DR. DR 1525  DR 1				L24.
DDR.ADDRO E22, DDR.DOS E				F23
DDR. ADDRES   C25,   DDR. ADDRES   C24,   DDR. DOSS				
DDR_ADDRS (22), DDR_DDS (22), DD			DDR_ADDRO	
DDR_ADDR3 52% DDR_ADDR4 52% DDR_ADDR5 82% DDR_ADDR6 52% DDR_DDS_D 52% DDR_DDR_DDS_D 52% DDR_DDS_D 52% DDR_DDS_D 52% DDR_DDS_D 52% DDR_DDS_DDR_DDS_D 52% DDR_DDS_DDS			DDR_ADDR1	K25×
DDR_ADDRH (22) DDR_DDR (22) DDR (22) DDR (22				C25×
DUR. ADDRES   225,   DUR. DOR. DOR. DOR. DOR. DOR. DOR. DOR. DO				K23
DDR ADDRE   226,				B25
DUR_ADDR   222,   DUR_ADDR   222,   DUR_ADDR   222,   DUR_ADDR   223,   DUR_ADDR   224,   DUR_ADDR				
DR. ADDRE   F25- DR. AD				
DDR.ADDR19 B22- DDR.ADDR19 DPR.ADDR19 DPR.AD				F25
DR. ADDR10   122-2-2-2-2-2-2-2-2-2-2-2-2-2-2-2-2-2-2				B240
DRRADDR1   123, DRRADDR1   124, DRRADDR1   124				P23
DRRADDRIS DEZ- DRRADSIN YZ- DR				I H25
DR_ADRH   H2S, DR_ADR				N23_
DR_ADRIS   D25,   D26,   D26,   D27,   D28,   D28,			DDR_ADDR13	
DR_DMO U23, DR_DMO U23, DR_DMO U23, DR_DMO U24, DR_DMO U25, DR_DMO U25, DR_DMO U25, DR_DMO U25, DR_DMO U25, DR_DMO U25, DR_DMO U26, DR_DMO				H23×
DR. DM1  DR. D0S. P. V25- DR. D0S. P. V25- DR. D0S. P. M24- DR. D0S. P. M24- DR. D0S. DM1  DR. D0S. DM1  DR. D0S. DM2  DR. D0S. DM3  DR. D0S.			DDR_ADDR15	025×
DR. DM1  DR. D0S. P. V25- DR. D0S. P. V25- DR. D0S. P. M24- DR. D0S. P. M24- DR. D0S. DM1  DR. D0S. DM1  DR. D0S. DM2  DR. D0S. DM3  DR. D0S.			DDD DMO	U23.
DOR_DOSO_P V25_A DOR_DOSO_P V26_A DOSO_P				Y24
DR. DOS. J. 1724; DR. DOS. J. M. 1724; DR. DOS. T. M. 1724; DR. DOS. DOS. DOS. DOS. DOS. DOS. DOS. DOS			DDK_DMI	l
DR. DOS. p. 8824, DR. DOS. p. 9824, DR. DOS. p.			DDR_DQS0_P	V25×
DDR_DQS_L N   M225,			DDR_DQSO_N	V24×
DDR_D00 1725, DDR_D01 1724, DDR_D01 1724, DDR_D01 1724, DDR_D02 1724, DD				AB25
DUL_DIG   W24,			DDR_DQS1_N	×
DDR_DOIS   1224,   DDR_DOIS   1225,   DDR_DOIS   12			000 000	
DR_DOS R25; DR_DOS DR_D				W24
DDR_DGS 1/2+, DD				R24
DDR_DG1 125x DDR_DG1 225x				Y25×
DDR_D06   225   DDR_D06			DDR_DQ4	T24×
DDR_DGT 123-x DDR_DGT 124-x DDR_DGT 142-x				
DDR_DDR   DDR_DDR_DDR   DDR_DDR_DDR   DDR_DDR_DDR_DDR_DDR_DDR_DDR_DDR_DDR_DDR				T23×
DDR_001 M23, DDR_001 M25,				
DDR_DG10 W23, DDR_DG11 M25, DDR_DG15 M22, DDR_DG15 M22, DDR_DG15 M22, DDR_DG15 M24, DDR_DG15 M24, DDR_DG16 M24, DD				
DR_D013 M23, DR_D013 M23, DR_D013 M23, DR_D013 M23, DR_D014 M24, DR_D016 M24, DR_D0				
DOR_DOI2 M224; DOR_DOI3 M24; DOR_DOI4 M224; DOR_DOI6 M24; DOR_DOI6 M25; DOR_DOI6 M24; DOR_DOI6 M25; DOI6 M25;				AD25
DR. D01.3 AA23, DR. D04.4 AC24, DR. D04.6 AE24, DDR. D01.5 AE24, DDR. D01.1 L625, DDR. D01.3 AE24, DDR. D01.				
DDR_DQ14 AE24, DDR_DQ15 AE24  DDR_ODT0 DDR_ODT1 L25×				AA23
DDR_DQ15				AC24
DDR_ODT1				AE24
DDR_ODT1				M24
427				125^
DDR_CALIBRATION A23×			DDR_ODT1	×
SOIL CONTROL A			DDR CALIBRATION	A23_
			2220.0000000000000000000000000000000	

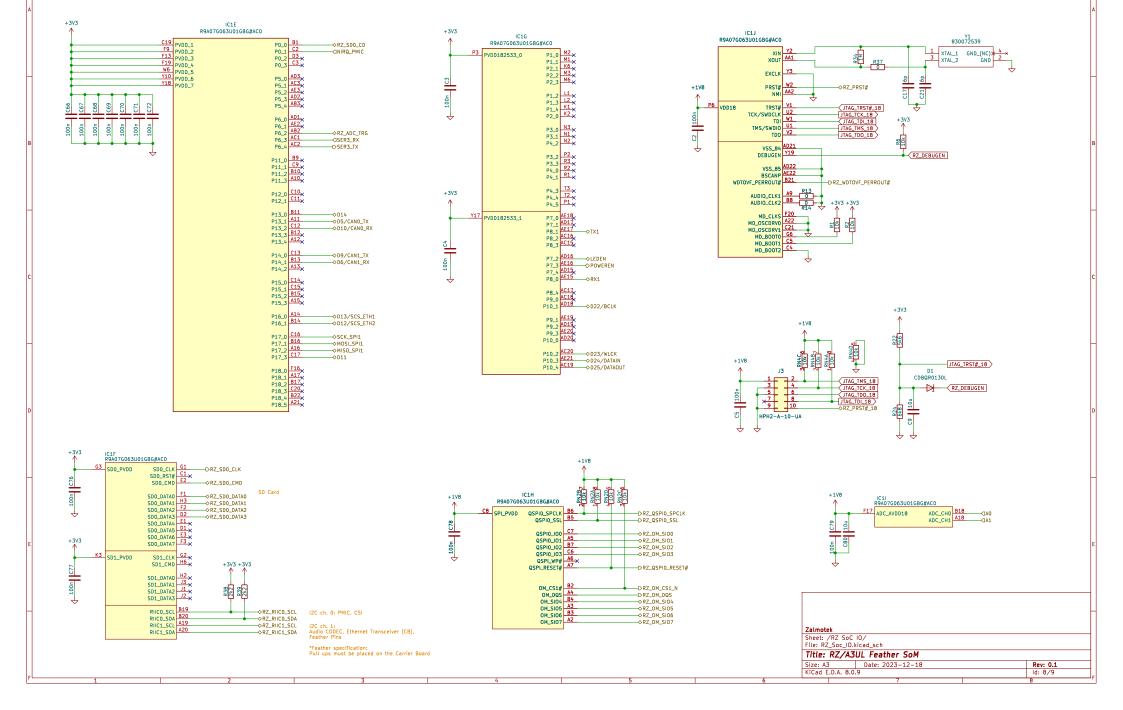
Zalmotek	Zalmotek			
Sheet: /RZ SoC Power/ File: power.kicad_sch				
Title: RZ/A3UL Feather SoM				
Size: A3	Date: 2023-12-18	Rev: 0.1		
KiCad E.D.A. 8.0.9		ld: 5/9		
	7	· 8		

### OctaFlash and OctaRAM



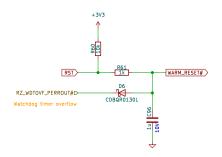
Zalmotek		
Sheet: /Flash File: flash_ran		
Title: RZ/A	A3UL Feather SoM	
Size: A3	Date: 2023-12-18	Rev: 0.1
KiCad E.D.A. 8.0.9		ld: 6/9
	7	9

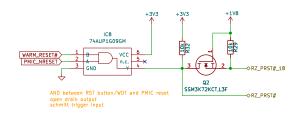
### RZ 10

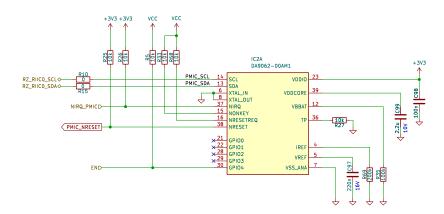


## **PMIC Control and Reset**









Zalmotek			
Sheet: /PMIC: Co File: control_reso	ontrol and Reset/ et.kicad_sch		
Title: RZ/A3UL Feather SoM			
Size: A3 KiCad E.D.A. 8.0	Date: 2023-12-18		Rev: 0.1 ld: 9/9

## FTDI

