

University of Information Technology and Sciences (UITS)

DEPARTMENT OF INFORMATION TECHNOLOGY

LAB-4: X-NOR

IT-202: DIGITAL LOGIC DESIGN LAB

X-NOR

Submitted To:

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1 X-NOR

An XNOR gate (sometimes referred to by its extended name, Exclusive NOR gate) is a digital logic gate with two or more inputs and one output that performs logical equality. The output of an XNOR gate is true when all of its inputs are true or when all of its inputs are false.

2 Related Work

We need to verify the truth table of X-NOR gate . So we have to create truth table for the NOR gate

2.1 Inserting Table Example

Presenting X-NOR Gate in Truth Table 1.

Input	Input	Output
0	0	1
0	1	0
1	0	0
1	1	1

Table 1: Truth Table of X-NOR Gate.

3 Methodology

3.1 Insert Image

Name of the image is indicated by Figure-2

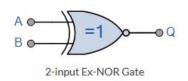


Figure 1: X-NOR gate

4 Results

output is shown in figure-2 for the X-NOR gate .

4.1 Output-1

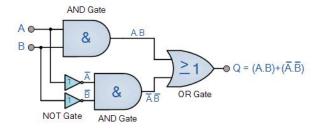


Figure 2: Output-1

5 Conclusion

After doing lab report, I learned about IC.Knowing also how to connect IC on the bread board with wires. We get different output for different input and where we used this Z-NOR gate.

References

 $1. https://www.electronics-tutorials.ws/logic/logic_4. html \\ 2. https://www.allaboutcircuits.com/textbook/digital/chpt - 3/not - gate/$