

University of Information Technology and Sciences (UITS)

Lab Report-2

IT-202: DIGITAL LOGIC DESIGN LAB

OR Gate

Submitted To:

Sk. Tanzir Mehedi Lecturer, Department of IT, UITS Email: tanzirmehedi@uits.edu.bd

Submitted By:

Name: Nazmul Zaman Student ID:2014755055 Department of IT UITS

November 23,2021

Department of IT, UITS © All rights reserved.

Contents

1	OR Gate	2
2	Related Work 2.1 Inserting Table Example	2 2
3	Methodology 3.1 Insert Image	2
4	Results 4.1 Output-1	3
5	Conclusion	3

1 OR Gate

An OR gate performs like two switches in parallel supplying a light, so that when either of the switches is closed the light is on. An OR gate is a digital logic gate that gives an output of 1 when any of its inputs are 1, otherwise 0.

2 Related Work

The function of an OR gate is to find the maximum between the inputs which are binary in nature. It is one of the basic gates used in Boolean algebra and electronic circuits like transistor-transistor logic,complementary metal-oxide semiconductors and also use in digital circuit make use of it .

2.1 Inserting Table Example

Input	Input	Output
0	0	0
0	1	1
1	0	1
1	1	1

Table 1: OR Gate

3 Methodology

3.1 Insert Image

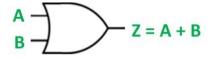


Figure 1: OR Gate

4 Results

The OR gate is a digital logic gate that implements logical disjunction (\vee) from mathematical logic – it behaves according to the truth table above. A HIGH output (1) results if one or both the inputs to the gate are HIGH (1). If neither input is high, a LOW output (0) results. In another sense, the function of OR effectively finds the maximum between two binary digits, just as the complementary AND function finds the minimum.

4.1 Output-1

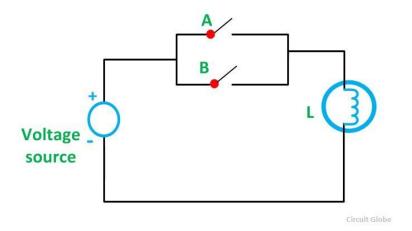


Figure 2: Output-1

5 Conclusion

In this report we learn about OR Gate ,An OR gate is a digital logic gate that gives an output of 1 when any of its inputs are 1, otherwise 0and also known it's working method and how does it works and where it is use and related work and also know the truth table formula and input/output .

References

 $https://www.electronics-tutorials.ws/logic/logic_4.html$

https: //www.allaboutcircuits.com/textbook/digital/chpt - 3/not - gate/digital/chpt - gate/digital/chpt - gate/digital/chpt - gate/digital/chpt - gate/digital/chpt - gate/digital/chpt - gate/digit