C5308

RISC - MIPS

SHILL IN Britton

Che potterson Hennessey

Need to support C compiler

with minimum "cost" I instruction

Comiler (stutic)

 $0 = V + C - J \times C$ 

Anithmetic expression (1) Evolute

a = 412 + 663

Logicol Expression (3)

if (((A == B) // (C& D))

<QXP>

Condition exp conditional Loyie if (Loy, col Exp) EEXP3 While (LEXI) Z X P Anithmetic expression one 3 operand expressions the operate only on Registers add a b c Syntax a & b + C Semontics Register Trasfer Longuage a, b, c are variables Stores in registers

Arithmetical/Logical set of instructions OP JSA SRY SACZ AUF 500 OR Source all or 1, c destination 3 source 2 34 GPR 32 lits Referred to by number name all \$5, \$6, \$7 Register allocation \$5 < \$6 + \$7

op RJ, Rs, R+ syntas \$n n=0,0,3/ SUL \$5, \$11, \$0 \$5-311-30 op Rd, Rs Rx RICAS SPRI a = (b + c) - (E + 1); t V V Register Allacyteus all \$1,\$1,\$2 mut sor good \$5 0 +1

Reg \$D \( \) D Hard wired to D

odd \$5,\$6,\$0 mov \$5,\$6

5)

aud\$5,\$1,\$2 aud\$5,\$1,\$2 aud\$6,\$3,\$4 sub\$0,\$5,\$6

Register \$0 is Hard W

> \$5 < \$6 +\$P

avu \$5, \$6, \$0 = mov \$5, \$6

Reul inst \$5 = \$6

Mochine cule

mov \$5, \$6 pseudv instruction no muchine cule Converted into Real 50 \$5, \$6, \$0 (\$Zen) or \$5, \$6, \$0 prefered way MIPS Logical operations are lit-wide all all b ALV ojb ajib 100000/10/ XJR 1 00100011

if (A&&B) Logicul AND A B AVVB F F F F F F Tow, ASIMe 00000000 & MIDS AND B=T 0000000/ 0000000 F 0 00000g sp or \$5,\$6,\$0 \$5 < \$6 OR \$P & Negota a = [-a]Ncy \$0,80,8t, Svb \$+P,\$0,\$+1