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| **CS3339: Computer Architecture - Tentative Schedule**  **Fall 2018 - Section 001 - M/W 2 - 3:20**  **Texas State University**  **Greg LaKomski** | | | | | |
| **Week #** | **Date** |  | **Topic** | **Reading** |  |
| 0 | Aug 27 | M | Class Intro  Evaluating Performance I | Section 1.1 - 1.7 |  |
| Aug 29 | W | Evaluating Performance II | Section 1.8, 1.11 | Homework 1 Posted |
| 1 | Sept 3 | M | Labor Day Holiday | Section 2 |  |
| Sept 5 | W | ARM V8 ISA I | Section 2 | Start Proj 1 |
| 2 | Sept 10 | M | Project 1, Python Basics, Using the Debugger, ARM V8 ISA II | Section 2 | Homework 1 Due |
| Sept 12 | W | ARM V8 ISA III  Floating Point Math | Section 3 | Homework 2 Posted |
| 3 | Sept 17 | M | Data Path I | Section 4.1 - 4.4 |  |
| Sept 19 | W | Data Path II | Section 4.1 - 4.4 |  |
| 4 | Sept 24 | M | Data Path III | Section 4.1 - 4.4 | Homework 2 Due |
|  | Sept 26 | W | Data Path IV - Quick review | Section 4.1 - 4.4 | Project 1 Due - Project2 Start |
| 5 | Oct 1 | M | Midterm I |  |  |
| Oct 3 | W | Pipelining I |  |  |
| 6 | Oct 8 | M | Review Midterm 1 - Pipelining II |  |  |
| Oct 10 | W | Pipelining III |  |  |
| 7 | Oct 15 | M | Pipelining IV |  |  |
| Oct 17 | W | Cache I |  |  |
| 8 | Oct 22 | M | Cache II |  |  |
| Oct 24 | W | Cache III |  | Project 2 Due - Project3 Start |
| 9 | Oct 29 | M | Cache IV |  |  |
| Oct 31 | W | Virtual Memory I |  |  |
| 10 | Nov 5 | M | Virtual Memory II |  |  |
| Nov 7 | W |  |  |  |
| 11 | Nov 12 | M | Midterm 2 |  |  |
| Nov 14 | W |  |  |  |
| 12 | Nov 19 | M | Review Midterm 2  Multiprocessor Architectures |  |  |
| Nov 21 | W | Thanksgiving Break |  |  |
| 13 | Nov 26 | M | Parallel Computation and Architectures |  |  |
| Nov 28 | W | Parallel Computation and Architectures |  | Project 3 Due |
| 14 | Dec 3 | M |  |  |  |
| Dec 5 | W | Final Review |  |  |
| 15 | Dec 12 | M | Final Exam 2:00 - 4:30 PM |  |  |