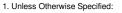
5	4	3	2	\equiv	1_		
Table of Contents				Revisions			- 11
1 Title	7			Rev	Description	Date	Approved
2 Block Diagram	1			X1	Initial Draft	04/10/12	M. NORMAN
3 KL25Z MCU	1			A	Release to production	05/03/12	M. NORMAN
4 OpenSDA INTERFACE	1			В	Changes made to KL25Z XTAL Circuit	05/09/12	M. NORMAN
5 I/O Headers and Power Supply	7			С	Fixed J10 orientation.	05/21/12	M. NORMAN
				CX1	Initial Re-spin Prototype revision	06/29/12	M. NORMAN
				D	Re-spin release	07/10/12	M. NORMAN
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FREEDOM KL25Z





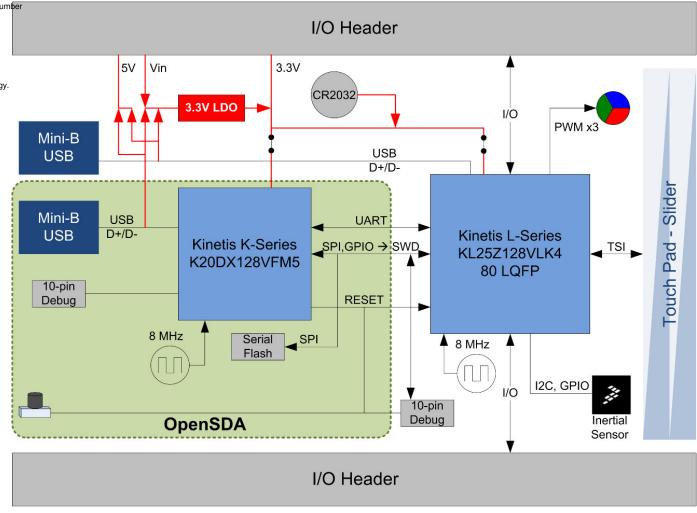
All resistors are in ohms, 5%, 1/8 Watt All capacitors are in uF, 20%, 50V

All voltages are DC

All polarized capacitors are aluminum electrolytic

- 2. Interrupted lines coded with the same letter or letter combinations are electrically connected.
- Device type number is for reference only. The number varies with the manufacturer.
- 4. Special signal usage:
 - _B Denotes Active-Low Signal
 - <> or [] Denotes Vectored Signals

5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.



Indicates optional items that will not be populated by default

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