

POT LIFE COMPOTER

By Ray Tully

This program is based on the "Life" game devised by Charles Conway, and described in the October, 1970 issue of "Scientific American" (p. 120). The game is initialized by placing into a grid a pattern of "cells" (i.e., bits on the video display). The cells then live, reproduce, and die according to these three "genetic laws": 1. Survivals - every cell with 2 or 3 neighbors survives to the next generation: 2. Deaths - an isolated cell, or one overcrowded with 4 or more neighbors, will not survive to the next generation: 3 Births - an empty grid location with exactly 3 neighbors will contain a cell in the next generation. From a home computerist's point of view, the object of the game is to devise starting patterns that will evolve into as spectacular a series of succeeding generations as possible.

How to Run the Program

First, the starting pattern (first generation) must be drawn on the display. Jump to the "Pattern Initialization Program" at 0060. Either load 30, 60, at 0000 and press and release "R" and "G" or use Quest Super Monitor to execute at 00DO. Page 02 will be erased and displayed. It is divided into six columns of bytes (see diagram under 'How the Program Works'). First, using the hex keypad enter the column in which you want to begin drawing, by typing the data on the hex keypad and pressing and releasing "I" (01-06). Next, enter the data for the drawing. The data will be entered into the display vertically. Each column is 30 bytes deep; when you reach the bottom, Q turns on. You can then enter another column number and continue the drawing, if desired. When the drawing is

finished, stop the program and jump to the Life program, 0100. Either load CO, 01, 00 at 0000 or use option 00 of Quest's Super Monitor to execute at 00EO. This will display the initial drawing, and then succeeding generations. The program calculates about 33 generations each minute on the 30x48 grid.

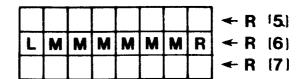
The program requires a minimum of 1K bytes of memory, and an 1861 graphics chip. The programs and subroutines are on pages 00 and 01, and the two generations in storage at any given time are on pages 02 and 03.

How The Program Works

Each bit in memory has eight nearest neighbors, numbered as follows:

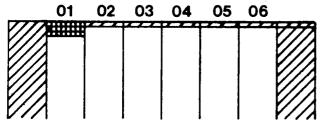
1	2	3
4		5
6	7	8

where the shaded block is the bit being tested. Consider next a column of three bytes in memory, pointed to by R(5), R(6), and R(7):



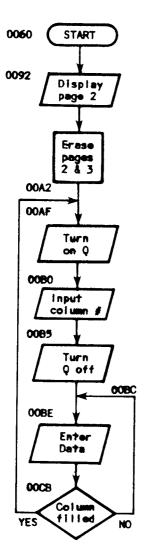
where M(R(6)) contains the particular bit under test. There are three generalized positions for this bit: left end of byte (L), middle of byte (M), or right end of byte (R). The routine for counting the number of neighbors of a given bit is thus divided into three sections, depending on whether the bit is in L, M, or R.

The starting diagram (first generation) is made on page 02. The top and bottom lines of the page, and the far left and right column of bytes, are kept blank (for ease of programming). R(5), R(6), and R(7) are initialized to the upper left corner:



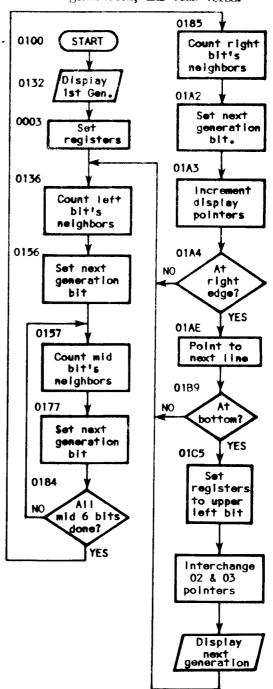
with the solid bit at 'L" in M(R(6)) being the first one under test.

The neighbors are counted by placing the appropriate bytes into the Data Register, shifting the individual on or off bits into DF, and incrementing R(B). O for each one that is on (by calling the subroutine at 0050 with a DE instruction). After counting the neighbors, the state of the bit under test in M(R(6)) is determined (by shifting into DF). If it is on, then Q is turned on. A branch is then made to



the "Subroutine to Set Next Generation" at 002C (DC instruction), whose job it is to set the bits for the next generation, depending on the state of the bits in the first. The bits in the next generation are pointed to by M(R(9)). They are set by placing M(R(9)) into the accumulator after having set DF to 0 or 1, shifting DF into the byte, and copying back into M(R(9)).

For each byte, the procedure is to test the leftmost bit (0136-0156), the middle six bits (by passing through the loop 0157-0184 six times), and the rightmost bit (0185-01A2). The current generation pointers (R(5)-R(7)) and next generation pointer (R(9)) are then incremented appropriately, and a test is made to see if the process is finished (01A4-01BA). If so, then the next generation is displayed and made into the current generation, and vice versa.



STATUS QUO (NO CHANGES TO NEXT GEN)

ADDR CODE	COMMENT	ADDR CODE	COMMENT
0000 C0 0001 00 60	Load 0060 at first then 0100 (see text)	004C 31 48	if Q=1, go set next generation
0003 F8 01 BD A5	(See Text)	004E 30 3B	bit to 1 Else, set to 0
0007 F8 19 AD 000A F8 02 5D B5 B6 B7 0010 F8 11 A7		SUBROUTINE TO COUNT NEIGHB	ORS
0013 F8 00 AB BE BC 0018 F8 09 A6 A9 001C F8 31 AC		0050 D3 0051 3B 50	Return If DF = 0, return
001F F8 03 B9 0022 F8 51 AE		0053 18 30 50	Else increment count, return
0025 C0 01 36	Return to main program	STANDARD VIDEO ROUTINE TO DISPLAY 1	PAGE OF MEMORY
REGISTER ASSIGNMENTS		ADDR CODE	COMMENT
R(5)-Points to neighbors #1-3 of currer R(6)-Points to bit under test, and neighbors #6-8 of currer R(9)-Points to next generation. R(B).0-Hoids count of neighbors. R(C)-Points to subroutine to set next R(D)-Points to location in video routing contains page of memory being dispage is initialized at 02 for the eration. R(E)-Points to subroutine to count neighbors.	ghbors #4 and 5. ent generation. generation. ne which splayed. This e first gen-	0060 90 B1 B2 B3 B4 0065 F8 91 A3 0068 F8 8F A2 006B F8 71 A1 006E D3 72 70 0071 22 78 22 52 0075 C4 C4 C4 0078 F8 02 B0 0078 F8 00 A0 007E 80 E2 0080 E2 20 A0	
SUBROUTINE TO SET NEXT GENER/	ATION	0083 E2 20 A0 0086 E2 20 A0	
ADDR CODE	COMMENT	0089 3C 7E 008B 30 6F 008D 00 00 00 00	
002C 7A F8 00 AB	Turn Q off, reset neighbor count to 00	0091 E2 61 ERASE PAGES 02 AND 03	
0030 D3	Return to main	ADDR CODE	COMMENT
0031 8B FB 02 32 4C	program If neighbors=02 go to Status	0093 E5 0094 F8 03 B5	X = 5
0036 8B FF 02 33 43	Quo If count>1, go test if =3	0097 F8 FF A5	R(5) points to area to be erased
SET NEXT GENERATION BIT TO	0 0	009A F8 00 73	Enter 00, work
ADDR CODE	COMMENT	0090 95 FB 01	your way down Continue erasing until you hit
003B F8 00 F6 003E 09 7E 59 30 2C	Set DF to 0 Shift DF into M(R(9)), return	00A0 3A 9A	page 01
TEST FOR MORE THAN 3 NEIGH	BORS	ENTER DATA INTO DESIRED COL	LUMN
ADDR CODE	COMMENT	ADDR CODE	COMMENT
0043 8B 7D 03	Test for count	00A2 F8 02 B5	R(5) points to column "0" = 0209
0046 3B 3B	If so, set next generation to 0	00A5 F8 08 A5 00A8 E6 00A9 F8 00 B6	X = 6
SET NEXT GENERATION BIT TO	0 1	OOAC F8 FF A6	M(R(6)) stores input data
ADDR CODE	COMMENT	00AF 7B	from keys Q on to prompt
0048 F8 FF 004A 30 3D	Set DF to 1 Go shift DF into M(R(9))		entry of desired column #

Pa	ne	4

1 age 4			
ADDR CODE	COMMENT	ADDR CODE	COMMENT
00B0 3F B0	Enter column#,	012B 30 0F	
	Push i	012D 00 00 00 00	Stack area
00B2 6C 64 26 7A	Store and display	0131 E2 61	Turn on TV Go initialize
	column #, turn Q off	0133 C0 00 03	registers
00B6 85 F4 A5	Add column # to		rogrators
	R(5).0	ROUTINE FOR LEFTMOST E	SIT
0089 37 89	Release I		
00BB E5	Reset X to 5	ADDR CODE	COMMENT
00BC 3F BC	Enter data for	0176 05 55 05	Shift neighbor
00BE 6C 64	pattern, push l Write data into	0136 05 FE DE	#2 into DF.
VODE VO V.	display area,		count if on
	R(X)+1	0139 FE DE	Shift neighbor
00C0 85 FC 07 A5	R(X)+7 to point		#3 into DF,
	to next byte down	013B 07 FE DE FE DE	count if on Repeat with
00C4 F6 F6 F6 FB 1F	column You're at bottom	OTOS OT THE SECTE SE	neighbors #7
0004 10 10 10 10 11	if R(X).0>=F8		and 8
	(1F if shifted)	0140 25 05 F6 DE 15	Repeat with
00C9 37 C9	Release I		neighbor #1, restore R(5)
00CB 3A BC	Not yet at bottom?	0145 26 06 F6 DE 16	Repeat with
00CD 30 A2	If yes, go	0149 20 00 10 DE 10	neighbor #4,
	select another		restore R(6)
	column	014A 27 07 F6 DE 17	Repeat with
Super Monitor Entry Point for Dat			πeighbor∦6,
00D0 F8 60 00D2 A0	Make RO point	014F 06 FE 3B 54 7B	restore R(7) Q on if bit on
00D3 F8 90	to program. Make R2 point	0154 FE DE	Count neighbor
00D5 A2	to scratch.		# 5
00D6 93	Get 00	0156 DC	Set next
00D7 B0	Do R0.1		generation
00D8 B2 00D9 52	Do R2.1 Put 00 in	ROUTINE FOR MIDDLE 6 B	271
0007 72	scratch	NOT THE TON THOSE OF	
00DA 70	X=0,P=0, enable interrupts	ADDR CODE	COMMENT
Super Monitor Entry Point for Dis	•	0157 F8 06 A8	Set R(8).0 to
00E0 F8 01	Make RO point		count middle
00E2 B0 00E3 B2	to program.	015A 05 AA	6 bits Store M(R(5)) in
00E4 F8 30	R2 too. Do R2.0	אל עט אכוט	R(A).0
00E6 A2	Do R2.0	015C 06 BA	Store M(R(6)) in
00E7 93	Get 00		R(A).1
00E8 A0	Do R0.0	015E 07 BB	Store M(R(7)) in
00E9 52	Store 00 in scratch	0160 8A FE DE FE DE FE DE	R(B).1 Count neighbors
00EA 70	X=0.P=0. enable	VIOU ON TE DE TE DE LE DE	#1-3
	interrupts	0167 9B FE DE FE DE FE DE	Count neighbors #6-8
		016E 9A FE DE	Count neighbor
Standard Video Routine to Display	l Page of Memory		#4
ADDO CODE	COMMENT	0171 FE 3B 75 7B	Q on if bit on
ADDR CODE	COMMENT	0175 FE DE	Count neighbor #5
0100 90 B1 B2 B3 B4	This routine is	0177 DC	Set next
	essentially that		genertion
0105 F8 31 A3	published in Questdata #2,p.12	0178 8A FE AA	Shift R(A).0 left
0108 F8 2F A2	•	017B 9A FE BA	Shift R(A).1
010B F8 11 A1 .		017F 00 FF 00	left
010E D3 72 70 0111 22 78 22 52		017E 9B FE BB	Shift R(B).1 left
0115 C4 C4 C4		0181 28 88 3A 60	Decrement count
0118 F8 02 B0			repeat loop
011B F8 00 A0			until = 0
011E 80 E2			
0120 E2 20 A0 0123 E2 20 A0			
0126 E2 20 A0			
0129 3C 1E			

ROUTINE FOR RIGHTMOST BIT

ROUTINE FOR RIGH	HIMUSI BII		
ADDR CODE	COMMENT	ADDR CODE	COMMENT
0185 05 F6 DE F6 DE	Count neighbors	01D0 F8 02 B9	Set next
018A 07 F6 DE F6 DE	#1 and 2		generation pointer
OTON OF TO BE TO BE	Count neighbors #6 and 7		to page 02
018F 06 F6 3B 94 7B	Q on if bit on	01D3 30 36	Go back to the
0194 F6 DE	Count neighbor #4	01D5 F8 02 B5 B6 B7 5D	beginning Set current
0196 15 16 17	Point to column		generation
	of 3 bytes to right		pointers to page 02
0199 05 FE DE	Count neighbor	01DB F8 03 B9	Set next
019C 06 FE DE	#3 Count neighbor		generation pointer
	#5		to page 03
019F 07 FE DE	Count neighbor #8	01DE 30 36	Go back to the beginning
01A2 DC	Set next		g
01A3 19	generation Advance byte	0000 3060 00F8 01BD A5F8 19AD F	802 5DB5 B6B7
	pointer in next	0010 F811 A7F8 00AB BEBC F809 A	6A9 F831 ACF8
	generation	0020 03B9 F851 AECO 0136 0000 0 0030 D38B FB02 324C 8BFF 0233 4	
TEST IF AT RIGHT MARG	IN OR PAGE END	0040 5930 2C8B 7D03 3B3B F8FF 3	303D 3148 303B
ADDR CODE	COMMENT	0050 D33B 501B 3050 0000 0000 0 0060 90B1 B2B3 B4F8 91A3 F88F A	
0144 96 55 55 57 57 57		0070 7022 7822 52C4 C4C4 F802 B	OF8 00A0 80E2
	14 6		

		TES	TIF	ΑT	RIGHT	MARGIN	OR	PAGE	END			5930		_	_				
												D33B							
ADDR	COD	E						CC	MMENT			9081							
											0070	7022	7822	52C4	C4C4	F802	BOF8	0A00	80E2
01A4	86	FE F	E FE	FΕ	FE			1 f	byte	pointer	0080	E220	AOE 2	20A0	E220	AO3C	7E30	6F00	0000
								R((6).0,	when	0090	00E2	61E5	F803	B5F8	FFA5	F800	7395	FB01
								sh	ifted	left by	00A0	3A9A	F802	B5F8	08A5	E6F8	00B6	F8FF	A67B
								5	bits,	equals	00B0	3FB0	6C64	267A	85F4	A537	B9E5	3FBC	6C64
								"E	Ό", ṫh	en you	00C0	85FC	07A5	F6F6	F6FB	1F37	C93A	BC30	A200
								ar	e at t	he edge.	00D0	F860	A0F8	90A2	93B0	B252	7000	0000	0000
									se con		00E0	F801	B0B2	F830	A293	A052	7000	0000	0000
								lo	oping		00F0	0000	0000	0000	0000	0000	0000	0000	0000
01AA	FΒ	E0							, ,		0100	9081	B2B3	B4F8	31A3	F82F	A2F8	11A1	D372
01AC	3A	36									0110	7022	7822	52C4	C4C4	F802	B0F8	0A00	80E2
OIAE	15	15 10	5 16	17	17			Ad	vance	pointers	0120	E220	AOE 2	20A0	E220	A03C	1E30	0F00	0000
								to	next	line	0130	00E2	61C0	0003	05FE	DEFE	DE07	FEDE	FEDE
01B4	19	19						Do	same	for next	0140	2505	F6DE	1526	06F6	DE 16	2707	F6DE	1706
								qe	enerati	on page	0150	FE3B	547B	FEDE	DCF8	06A8	05AA	06BA	07BB
0186	86	FB FS	•							0 points	0160	8AFE	DEFE	DEFE	DE9B	FEDE	FEDE	FEDE	9AFE
								to	F9. v	ou are	0170	DEFE	3B75	7BFE	DEDC	8AFE	AA9A	FEBA	9BFE
									the e		0180	BB28	883A	6005	F6DE	F6DE	07F6	DEF6	DE06
								†h	e page	• Else	0190	F63B	947B	F6DE	1516	1705	FEDE	06FE	DE07
									ntinue		01A0	FEDE	DC 19	86FE	FEFE	FEFE	FBE0	3A36	1515
0189	3A	36								•	01B0	1616	1717	1919	86FB	F93A	36F8	01A5	F809
											0100	A6A9	F811	A70D	FB02	3AD5	F803	B5B6	B75D

DISPLAY NEXT GENERATION

ADDR	CODE		COMMENT
	F8 01 F8 09	A5 A6 A9	Reset R(5) Reset R(6) and R(9)
	F8 11 OD FB		Reset R(7) If video routine
0108	3A D5	·	displays page 02, change it to 03. Else change back to 02
		B5 B6 B7 5D	Set current generation pointers to page 03

JINGLE BELLS????

01D0 F802 B930 36F8 02B5 B6B7 5DF8 03B9 3036

We at Questdata realize it's a little early to be thinking about Christmas, but it does seem to "pop up" before you know it. Therefore, this is a little reminder, to all you creative geniuses, that we need "holiday type" programs. We need them in the near future in order that we may review them for publication.

Many thanks,

QUESTDATA STAFF

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width ASCII characters. Flow charts for program.

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Programs in CHIP-8. Blackjack, biorhythm, pinball,
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calling address in Fig. 3 should read 02E9.

Weisbecker, Joseph A. "A Simplified Microprocessor Architecture," Computer magazine, March 1974, p. 41. Discusses the COSMAC microprocessor design philosophy.

Weisbecker, Joseph A. "Build the COSMAC 'Elf,' Part 1," Popular Electronics Magazine, August 1976, p. 33 (PE Reprint #40857).
Construction of basic Elf; description of architecture; introduction to programming.

Weisbecker, Joseph A. "Build the COSMAC 'Elf,' Part 2," Popular Electronics Magazine, September 1976, p.37 (PE Reprint #40858).

Hardware: LED bus disptay, parallel I/O port, hex keyboard. More on programming.

Weisbecker, Joseph A. "Build the COSMAC 'Elf,' Part 3," Popular Electronics Magazine, March 1977, p. 61 (PE Reprint #40859).

ETOPS-256, a 32-byte operating system for basic 1/4 K EIf with toggle switches or for basic 1/4 K Super EIf. EHOPS-256, a 74-byte operating system for basic 1/4 K EIf with scanned hex keyboard. Memory expansion to 1 1/4 K.

Weisbecker, Joseph A. "Build the PIXIE Graphic Display," Popular Electronics Magazine, July 1977, p. 41 (PE Reprint #40870).
Addition of 1861 video display to basic Elf.

Weisbecker, Joseph A. "COSMAC VIP, the RCA Fun Machine," Byte Magazine, 2, #8, p. 30 August (1977).

For availability of magazine reprints and back issues write to:

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The Paul C. Moews booklets and several of the RCA publications are available from Quest Electronics. The RCA publications may also be ordered directly from RCA Microcomputer Products

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Note from Editor:

If you come across any discrepancies or omissions, please let us know and we will publish corrections in a future issue. Your participation is greatly appreciated by the Questdata staff.

BIORHYTHM

by Gary Gehlhoff

Recently there has been some controversy concerning the use of biorhythms as they pertain to our daily lives. I decided to find out how they related to me by writing a program to calculate the current level of each biorhythm and the composite of the three.

Two inputs are required; first your birth date (month/day/year), and second, today's date (month/day/year). The three biorhythm levels along with the composite are then the output.

Lines 10 thru 180 are the input section. Line 190 calculates the number of leap year days since your birth day that are to be added. Line 260 determines whether the current month is before of after your birth date. Lines 210 thru 600 calculate the total number of days lived. Lines 610, 620, & 630 determine the number of days in each biorhythm cycle. Lines 650 thru 730 assign a numerical value to each one of the cycles (numbers are expressed as integers rather than decimals, as in traditional biorhythm plots). Finally, lines 760 thru 790 are the output.

Also included are a flow diagram and check examples.

		CHECK	EXAMP	LES				
BIRT	'H DATE	2-22-46		BIR	TH DATE		2-2	2-46
TODA	YS DATE	1-15-79		TODA	AYS DATE		2-2	3-79
DAVC	יייייי			DAV	S LIVED			
	LIVED ARS	12045			S LIVED EARS			12045
	P YEAR	12045			ARS NP YEAR			12045
	YS	•37			NE TEAR			8 1
אט	113	12015		Ur	(15		-	12054
(=	12015/33 J= +9	= 364 R3		1=	12054/33 J= 22	=	365	R9
	12015/28 T= +12	= 429 R3		S=	12054/28 T= 0	=	430	R14
P=	12015/23 Q= +5	= 522 R9		P≖	12054/23 Q= 4	=	524	R2
C= 2	6			C= 2	26			
	-				-			
F=	- 7			F=	1			
A= 1:	2015			A= 1	2054			

```
REM BIORHYTHM ---- PROGRAM
 10
 20
      PR "BIORHYTHM"
 30
      PR .
 40
      PR "BIRTH DATE";
      PR "-----MONTH-";
 50
 60
      INPUT M
 70
      PR "-----DAY---":
      INPUT D
 80
 90
      PR "----YEAR---";
      INPUT Y
 100
 110
      PR
 120
      PR "TODAYS DATE"
 130
      PR "-----MONTH-":
      INPUT N
 140
 150
     PR "-----DAY---";
 160
      INPUT E
 170 PR "----YEAR---";
 180
     INPUT Z
 190 L=(Z-Y)/4
 200
     B=(Z-Y)*365
 210 REM
 215
     U=0
 220
     F=0
 230
     W≈O
 240
     0=M-N
 250
     REM
 260
     IF 0>0 GOTO 431
 270
     IF M=N THEN GOTO 415
 280
     IF M=1 THEN W=W+31
290
     IF M=3 THEN W=W+31
     IF M=5 THEN W=W+31
 300
 310
     IF M=7 THEN W=W+31
     IF M=8 THEN W=W+31
 320
     IF M=10 THEN W=W+31
 330
    IF M=12 THEN W=W+31
350 IF M=4 THEN W=W+30
360
    IF M=6 THEN W=W+30
370
     IF M=9 THEN W=W+30
     IF M=11 THEN W=W+30
390 IF M=2 THEN W=W+31
400
    M=M+1
410
    GOTO 270
415
    F=E-D
420 A=L+B+W+F
430 · GOTO 610
431 N=N-1
432 M=M-1
433
     IF M=0 THEN M=12
434
     IF N=0 THEN N=12
435
    IF M=N THEN GOTO 590
    IF M=1 THEN W=W-31
436
437
     IF M=3 THEN W=W-31
438 IF M=5 THEN W=W-31
439 IF M=7 THEN W=W-31
440 IF M=8 THEN W=W-31
500
     IF M=10 THEN W=W-31
    IF M=12 THEN W=W-31
510
520
    IF M=4 THEN W=W-30
530
    IF M=6 THEN W=W-30
540
    IF M=9 THEN W=W-30
    IF M=11 THEN W=W-30
550
560
    IF M=2 THEN W=W-28
570
    M=M-1
580
    GOTO 433
590
    F=E-D
600 A=L+B+W+F
610
    P=A-(A/23)*23
620 S=A-(A/28)*28
630
    I=A-(A/33)*33
640
    REM ASSUME AVG PERSON
    IF I>=1 IF I<=8 THEN J=1*3
650
```

```
Page 9
660 IF 1>=9 IF 1<=24 THEN J=19+(10-1)*3
670 IF 1>=25 IF 1<=33 THEN J= -24+(1-25)*3
680 IF S>=1 IF S<=7 THEN T=S*4
690 IF S>=8 IF S<=21 THEN T=24-(S-8)*4
    IF S>=21 IF S<=28 THEN T= -24+(S-22)*4
700
    IF P>=1 IF P<=5 THEN Q=P*2
710
720
     IF P>=6 IF P<=17 THEN Q=11+(6-P)*2
730
    IF P>=18 IF P<=23 THEN Q= -12+(P-17)*2
740
    C=J+T+Q
750
    PR
     PR "INTELLECTUAL-", J
760
770
    PR "SENSITIVITY--",T
    PR "PHYSICAL----",Q
780
790
    PR "COMPOSITE ----", C
799
     END
          Input Birth
            date
          input today's
                           Line No.
                           10 - 180
            date
                          190
        Calculate leap
         year days
                          200
       Calculate days
       lived from birth
       day to current
       year birth day
                         260
           if Birth
                          YES
        Month - Todays
          Month is
             > 0
                 NO
       Month x Days in
                         431 -
                                   -(Month x Days
       Month & Add to
                         600
                                    in month) &
       Days Lived in
                                    Add to Days
       Years & Add to
                         270 -
                                    lived in years
       (Todays Days
                                    & add to
                         420
        - Birth Days)
                                    (Todays Days
                                    - Birth Days)
                        610 - 630
       Calculate Days
       into Current
       Biorhythm Cycle
                         640 - 730
        Calculate
        Numeric value
                        760 - 790
       Pr 1; S; P:C
```

799

END

SUPEG ELF BASSETTE TAPE GEADEG

0000

0000

0000

0000

0000

0000

0000

0000

0000

0000

0000

0000

0000

0000

0000

0000

0000

by Van C. Baker

If you have a need to read cassettes generated by the Super Monitor, or wish to read Quest-supplied cassette software, but need to relocate the tape data or program in a location other than that from which the tape was originally created, use the program listed below. To use the routine, load the program into memory, noting that it may be located beginning on any page boundary. Also note that location XXOl (where "XX" represents the page number) must contain the byte defining the page in which the routine is located. The program listed below, for example, runs in page zero, hence, byte OOOl is "OO".

The program assumes flag line EF3 is used for the cassette serial input; if your system differs from this convention, patch in the appropriate EFn conditional branch instructions at XX38,XX3F,XX4F,XX8E,XX95 and XXA2.

To use the program, proceed as follows:

- Execute the program using your monitor or other means. It does not matter what register is the program counter when the program is entered.
- Note that "AA" will be displayed on the hex display. Enter the high byte of the starting address into which the tape contents are to be loaded. Press the "I" key on the hex keypad.
- 3. Enter the low byte of the starting address. Press the "I" key.
- 4. Enter file number to be read (01-FF). Press the "T" key.
- 5. Start the recorder (on playback).

As the tape advances, the current file number being skipped (if the file entered in 4 was greater than 1) will be displayed until the requested file is reached. As the contents of the tape are being loaded into the requested memory locations, the hex display will rapidly flicker. When the tape has been read, "AA" will appear on the hex display. At this point, you may load another tape by proceeding with step 2 above. Since the Super Monitor tapes have a

record defining the total number of bytes on the tape, it is necessary only to input the starting address for the cassette load; the program does the rest. Use caution, however, to avoid loading the cassette data over the tape read program.

If "FE" should appear on the display while the tape is being read, it indicates that a read error (e.g., a parity error) occured. The address of the byte at which the error occured can be determined by examining locations XXFE and XXFF (High and low address bytes, respectively). To recover from a read error, press the "I" key and proceed from step 2 above.

0000	************
0000	;
0000	; MANUAL CASSETTE LOAD ROUTINE
0000	;
0000	BY V C BAKER
0000	:
0000	* THIS ROUTINE READS A STANDARD
0000	* "SUPER MONITOR" CASSETTE,
0000	* LOADING THE DATA INTO USER-
0000	* DESIGNATED MEMORY AREA.
0000	•
0000	:
0000	* TO USE:
0000	*
0000	* (1) LOAD THE FOLLOWING
0000	* ROUTINE INTO MEMORY.
0000	* ALTHOUGH THE ROUTINE
0000	* LISTED HERE STARTS AT 0000
0000	* (HEX), IT MAY BE LOCATED
0000	* ANYWHERE AS LONG AS IT
0000	<pre> # BEGINS ON A PAGE BOUNDARY,</pre>
0000	* I.E., AT XX00. PATCH IN
0000	* THE ACTUAL PAGE NUMBER AT
0000	* BYTE XX01.
0000	*
	, , , , , , , , , , , , , , , , , , ,

PROGRAM COUNTER.

(3) WHEN "AA" IS DIS-

USING ANY REGISTER FOR THE

(2) EXECUTE THE PROGRAM

- PLAYED ON THE HEX DISPLAY, ENTER THE FOLLOWING USING THE HEX KEYPAD:
 - (A) HIGH BYTE (MSH) OF STARTING ADDRESS INTO WHICH TAPE CONTENTS ARE TO BE LOADED.
 - (B) PRESS THE "I" KEY.
 - (C) LOW BYTE (LSH) OF STARTING ADDRESS.

```
ADDR CODE
               LABEL
                        OPCODE OPERAND COMMENT
 0000
 0000
                                                               ADDR CODE
                                                                             LABEL
                                                                                     OPCODE OPERAND COMMENT
                                  (D) PRESS THE "I" KEY.
 0000
                                                               0008 BB
                                                                                     PHI
                                                                                                      ; RB = BOTM2 PC.
 0000
                                  (E) TAPE FILE NUMBER
                                                               0009 B8
                                                                                     PHI
                                                                                             R8
                                                                                                        R8 = DI PC.
 0000
                                  (1-FF)
                                                               000A B6
                                                                                     PHI
                                                                                             R6
                                                                                                        R6 = ERR PC.
 0000
                                                               000B B5
                                                                                     PHI
                                                                                             R5
                                                                                                        R5 = HEXIN PC.
 0000
                                  (F) PRESS THE "I" KEY.
                                                               000C B2
                                                                                     PHI
                                                                                                       R2 = Stack
                                                                                             R2
 0000
                                                               0000
                                                                                                        pointer
 0000
                               (4) START THE RECORDER
                                                               000D
 0000
                               ON PLAYBACK.
                                                               0000
 0000
                                                               000D
 0000
                                    AS THE TAPE IS READ.
                                                               000D F8 6A
                                                                                     LDI
                                                                                             BOTM2
                                                                                                      ; BOTM2 address
 0000
                               ITS CONTENTS WILL BE
                                                               000F AB
                                                                                     PLO
                                                                                             RB
 0000
                               LOADED INTO THE DESIGNATED
                                                               0010 F8 8A
                                                                                     LD1
                                                                                             DI
                                                                                                      ; DI address
 0000
                               MEMORY LOCATIONS.
                                                               0012 A8
                                                                                     PIO
                                                   SINCE
                                                                                             Ŕ8
 0000
                                                               0013 F8 7A
                               THE STANDARD SUPER MONITOR
                                                                                     LDI
                                                                                             FRR
                                                                                                      ; ERR address
 0000
                               TAPES CONTAIN RECORDS
                                                               0015 A6
                                                                                     PLO
                                                                                             R6
 0000
                                                              0016 F8 FF
                                                                                             #FF
                               GIVING THE NUMBER OF BYTES
                                                                                     LDI
                                                                                                      ; Stack at XXFF
 0000
                               STORED ON THE TAPE, THE
                                                               0018 A2
                                                                                     PLO
                                                                                             R2
 0000
                               ROUTINE WILL STORE THE
                                                               0019 E2
                                                                                             R2
                                                                                     SEX
                                                                                                       SP = R2
 0000
                               ENTIRE CONTENTS OF THE
                                                              001A F8 71
                                                                                     LDI
                                                                                             HEXIN
                                                                                                      ; HEXIN address
 0000
                                                              001C A5
                               TAPE AT CONSECUTIVE MEMORY
                                                                                     PLO
                                                                                             R5
 0000
                               LOCATIONS. CAUTION MUST
                                                              0010
 0000
                               BE USED TO ASSURE THAT THE
                                                              001D
                                                                                     * BEGIN MAIN PROGRAM
 0000
                               CASSETTE ROUTINE ITSELF IS
                                                              001D
0000
                               NOT WRITTEN OVER AS THE
                                                              001D
0000
                               TAPE IS LOADED! IF FILE
                                                              001D
                                                                                     * DISPLAY "AA" ON LED'S
0000
                               2 OR GREATER WAS SPECIFIED
                                                              001D
0000
                               IN STEP 3-K, THE CURRENT FILE NUMBER BEING SKIPPED
                                                              001D F8 AA
                                                                            BEGIN:
                                                                                     1D1
                                                                                             #AA
                                                                                                      ; Load "AA"
0000
                                                              001F 52
                                                                                     STR
                                                                                            R2
                                                                                                       Store it
0000
                               WILL BE DISPLAYED UNTIL
                                                              0020 64
                                                                                     OUT
                                                                                             4
                                                                                                       Output it.
0000
                               THE REQUESTED FILE IS
                                                              0021 22
                                                                                     DEC
                                                                                            R2
                                                                                                     ; Reposition SP.
იიიი
                              REACHED.
                                         THE DISPLAY WILL
                                                              0022
0000
                              FLICKER AS THE TAPE IS
                                                              0022
                                                                                     * GET STARTING ADDRESS FROM
0000
                              BEING READ.
                                                              0022
                                                                                     * KEYPAD
0000
                                                              0022
0000
                                                              0022 D5
                               (6) WHEN THE TAPE HAS
                                                                                     SEP
                                                                                            R5
                                                                                                     ; Get MSH of
0000
                              BEEN SUCCESSFULLY READ
                                                              0022
                                                                                                       address
0000
                                                              0023 BE
                              "AA" WILL APPEAR ON THE
                                                                                     PHI
                                                                                            RE
0000
                                                              0024 D5
                              HEX DISPLAY. IF "EE"
                                                                                     SEP
                                                                                            R5
                                                                                                       Get LSH
0000
                                                              0025 AE
                                                                                     PLO
                              APPEARS, A READ ERROR
                                                                                            RF
                                                                                                       Start address
0000
                                                              0025
                              OCCURED (E.G., A PARITY
                                                                                                       in RE.
0000
                              FRROR).
                                       TO RECOVER FROM
                                                              0026
0000
                              THE ERROR, REWIND THE
                                                              0026
                                                                                     * GET FILE NUMBER FROM KEYPAD
0000
                              TAPE, PRESS THE "I" KEY
                                                              0026
0000
                              AND START OVER FROM STEP 3
                                                              0026 D5
                                                                                     SEP
                                                                                            R5
                                                                                                     ; Get file number
0000
                                THE ADDRESS OF THE BYTE
                                                              0026
                                                                                                       (1-FF).
0000
                              BEING READ WHEN THE ERROR
                                                              0027 A4
                                                                                     PL0
                                                                                            R4
                                                                                                      Save in R4.
0000
                              OCCURED CAN BE DETERMINED
                                                              0028
0000
                              BY INSPECTING MEMORY LOC-
                                                              0028
                                                                                     * SET TIMING VALUE FOR A ONE BIT
0000
                              ATIONS XXFE (FOR THE HIGH
                                                              0028
0000
                              BYTE OF ADDRESS) AND XXFF
                                                              0028 F8 0D
                                                                                     LDI
                                                                                            ONECT
0000
                              (FOR THE LOW BYTE).
                                                              002A B9
                                                                                    PHI
                                                                                            R9
                                                                                                     ; "1" TIMING VALUE
0000
                                                              002A
                                                                                                       IN R9.1
0000
                                                              002B
0000 CODE
                       OPCODE OPERAND COMMENT
              LABEL
                                                              002B
0000
                      ORG
                              #0000
                                       ; Start of program
                                                              002B F8 00
                                                                                    LDI
                                                                                            #00
                                                                                                       Initialize
0000
              START:
                              #0000
                      FOL
                                                              002B
                                                                                                      current file
0000
              ONECT:
                      EQL
                              #000D
                                       ; One-bit timing
                                                              002B
                                                                                                     : number
0000
                                         value
                                                              002D
0000
              LDRCT:
                      EQL
                              #000A
                                         Leader one's
                                                              002D
0000
                                         timing
                                                              002D 52
                                                                            FTST:
                                                                                    STR
                                                                                            R2
                                                                                                     ; Save current
0000
                                                              002D
                                                                                                       file number
0000
                                                              002E
                                                                   64
                                                                                    OUT
                                                                                                      Display it.
0000 F8 00
              GO:
                      LDI
                              START
                                        Load page number
                                                             002F 22
                                                                                    DEC
                                                                                            R2
0002 B3
                      PHI
                              R3
                                         for this routine
                                                             0030 84
                                                                                    GLO
                                                                                                      Get requested
0003 F8 07
                                        initialization
                      LDI
                              INIT
                                                             0030
                                                                                                       file number.
0003
                                         address
                                                             0031 F3
                                                                                    XOR
                                                                                                       Check if equal
0005 A3
                      PLO
                              R<sub>3</sub>
                                                             0031
                                                                                                       to current.
0006 D3
                      SEP
                              R3
                                       ; SEP to PC = R3
                                                             0032 32 4F
                                                                                    BZ
                                                                                            RDF
                                                                                                       Branch if so.
0007 93
              INIT:
                      GHI
                              R3
                                        Initialize
                                                             0034
0007
                                                                                    * CHECK FOR LEADER
                                        registers.
                                                             0034
                                                             0034
```

ADDD C	CODE	LADE	ODCODE	OPERAND	COMMENT	ADDR CO	ODE	LABEL	OPCODE	OPERAND	COMMENT
ADDR C 0034 F		LABEL LDFND:	LDI	LDRCT	; Load test value	0063 26		LADEL	DEC	R6	; Decrement byte
0034	3 3 , 1	25. 115.	LO 1	LUINO	; for leader.	0063			0.0	1.0	; counter
0036 B			PHI	R7	; Save In R7.1	0064 96			GHI	R6	
0037 D 0037	שמ	OZTST:	SEP	RB	; Check for abort : (BOTM2)	0065 3A	N DC		BNZ	RDOPT	; Loop back if not ; done
0038 3 0038	36 37		В3	OZTST	; Find data pulse ; transition	0067 30 0067	10		BR	BEGIN	; Else, go display ; "AA".
003A 9		PTIM:	GHI SMI	R9 #01	; Test pulse width ; is it too long	0069 0069		;			
003B	,	1 11114	341	# 01	; for a one?	0069		;			
003D 3			BNF BN3	ZER PTIM	; JMP If so. ; else loop till	0069 0069		*****	***	*********	***********
003F 0041 9	7		GHI	R7	; pulse is ov er. ; This bit is a	0069 0069		; *****	BOTM2 F	ROUTINE -	ABORT THE READ
0041 0042 3	2 37		8Z	OZTST	; "1". Count the ; ones till LDRCT	0069 0069 D3	3 '	BTMRET:	SEP	R3	; Return
0042 0044		;			; of them are ; found. (Verify	006A 3F 006A	69	BOTM2:	BN4	BTMRET	; Return if I-Key ; is not in
0044	_	•			; leader located.)	006C 37	7 6C	WAIT:	B4	WAIT	; software "de-
0044 2°			DEC BR	R7 OZTST		006C 006E 30	00		BR	GO	; bounce" step. : Abort. Start
0047 9		ZER:	GHI	R7	; the recieved bit	006E	, 00		ON	00	; all over
0047	A 7.4		DUZ	LOCUE	; must be a "O".	0070		,	*****	******	*******
0048 3/ 0048	A 34		BNZ	LDFND	; Is leader dome ; yet?	0070 0070		*****	*****	******	· * * * * * * * * * * * * * * * * * * *
004A 0	2		LDN	R2	; Yes. Increment	0070		;			
004A 004B F0	C 01		ADI	#01	; file counter and	0070 0070		*****	HEXIN RO	OUTINE	- HEX KEYPAD INPUT
004B	C OI		אטו	***	; then go check ; file number to	0070 D3	3	HEXRET:	SEP	R3	; Return
004D 30	0 2D		BR	FTST	; see If It Is the	0071 3F	71	HEXIN:	BN4	HEXIN	; Wait for I-key
004D 004F		;			; one requested.	0071 0073 60	2		INP	4	; in. ; Get input byte
004F		;	* REQUE	STED FIL	E FOUND	0074 64	1		OUT	4	; Display it
004F	r ar	;	017	205	11.14.6	0075 37 0075	7 75	WAIT2:	B 4	WAIT2	; Wait till key ; released
004F 3I 004F	c. 4r	RDF:	BN3	RDF	; Wait for end of : leader bit.	0077 22	2		DEC	R2	, rereased
0051											
		;			,	0078 30	70		BR	HEXRET	; Return
0051		;	* NOW F	READ TAPE	FOR START ADDRESS	007A	70	*****	BR ******* ******	HEXRET ******** *****	; Return ************************************
	8	;	* NOW F	READ TAPE		007A 007A 007A	70	*****	*****	**************************************	*******
0051 0051 0051 D8 0051	8	;			FOR START ADDRESS ; Read (But ; ignore) start	007A 007A 007A 007A	70	*****	*****	**************************************	***********
0051 0051 0051 D8 0051 0051		;	SEP	R8	FOR START ADDRESS ; Read (But ; ignore) start ; address.	007A 007A 007A 007A 007A 007A F8	B EE	; ***** ; ***** ; ***** ERR:	*****	******** ********* JTINE #EE	*******
0051 0051 0051 D8 0051 0051 0052 D8 0052		;			FOR START ADDRESS ; Read (But ; ignore) start	007A 007A 007A 007A 007A 007A F8 007C 52	3 E E	;	ERR ROLL LD1 STR	******** *****************************	ERROR "PROCESSOR" ; Load "EE"
0051 0051 0051 D8 0051 0051 0052 D8 0052 0053		;	SEP	R8	FOR START ADDRESS ; Read (But ; ignore) start ; address. ; Finish reading ; start address.	007A 007A 007A 007A 007A 007A F8 007C 52 007D 64	3 EE 2	;	ERR ROU LD! STR OUT	******** ********* JTINE #EE	ERROR "PROCESSOR"
0051 0051 0051 D8 0051 0051 0052 D8 0052		; ; ;	SEP SEP * NEXT,	R8	; Read (But; ignore) start; address.; Finish reading; start address. MBER OF BYTES	007A 007A 007A 007A 007A 007A 52 007C 52 007D 64 007E 22 007F 8E	3 EE 2 1	;	ERR ROLL LD1 STR	********* ****************************	ERROR "PROCESSOR" ; Load "EE" ; Display "EE" ; Get low address
0051 0051 0051 D8 0051 0051 0052 D8 0052 0053 0053 0053	8	; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ;	SEP SEP * NEXT, * STORE	R8 R8 READ NU	; Read (But; ignore) start; address.; Finish reading; start address. MBER OF BYTES	007A 007A 007A 007A 007A 007A F8 007C 52 007D 64 007E 22 007F 8E	3 EE 2 1 2	;	ERR ROLL LDI STR OUT DEC GLO	********* ********* JTINE #EE R2 4 R2	ERROR "PROCESSOR" ; Load "EE" ; Display "EE"
0051 0051 0051 0051 0051 0052 0052 0053 0053 0053 0053	8	;	SEP * NEXT, * STORE	R8 R8 READ NU ED ON TAP R8	; Read (But ; Ignore) start ; address. ; Finish reading ; start address. MBER OF BYTES	007A 007A 007A 007A 007A 007A 52 007C 52 007D 64 007E 22 007F 8E	3 EE 2 1 2 2	;	ERR ROU LDI STR OUT DEC	********* ********* JTINE #EE R2 4 R2	ERROR "PROCESSOR" ; Load "EE" ; Display "EE" ; Get low address
0051 0051 0051 0051 0051 0052 0052 0053 0053 0053 0053 0053 0054 86 0055 D8	8 8 6 8	;	SEP * NEXT, * STORE SEP PHI SEP	R8 READ NU ED ON TAP R8 R6 R8	; Read (But ; Ignore) start ; address. ; Finish reading ; start address. MBER OF BYTES	007A 007A 007A 007A 007A 007A 52 007C 52 007D 64 007E 22 007F 80 007F 0080 73 0081 96	3 EE 2 3	;	ERR ROL LD! STR OUT DEC GLO STXD GHI	********* JTINE #EE R2 4 R2 RE	ERROR "PROCESSOR" ; Load "EE" ; Display "EE" ; Get low address ; byte ; Get high address ; byte
0051 0051 0051 0051 0051 0052 0052 0053 0053 0053 0053 0053 0054 86 0055 D8	8 8 6 8	;	SEP * NEXT, * STORE SEP PHI SEP	R8 READ NU ED ON TAP R8 R6	; Read (But ; Ignore) start ; address. ; Finish reading ; start address. MBER OF BYTES	007A 007A 007A 007A 007A 007A 007C 52 007D 64 007E 22 007F 80 007F 0080 73 0081 9E 0081	3 EE 2 3	;	ERR ROU LD! STR OUT DEC GLO STXD	********* JTINE #EE R2 4 R2 R2 RE	ERROR "PROCESSOR" ; Load "EE" ; Display "EE" ; Get low address ; byte ; Get high address ; byte ; Save error
0051 0051 0051 0051 0051 0052 0052 0053 0053 0053 0053 0053 0054 86 0055 D8	8 8 6 8	;	SEP * NEXT, * STORE SEP PHI SEP PLO	R8 READ NU ED ON TAP R8 R6 R8	; Read (But; ignore) start; address.; Finish reading; start address. MBER OF BYTES	007A 007A 007A 007A 007A 007A 007C 52 007D 64 007E 22 007F 86 007F 0080 73 0081 96 0081 0082 52 0082 0083 3F	3 EE 2 3 2 5	;	ERR ROL LD! STR OUT DEC GLO STXD GHI	********* JTINE #EE R2 4 R2 RE	######################################
0051 0051 0051 0051 0051 0052 0052 0053 0053 0053 0053 0054 0055 0055 0056 0057	8 8 6 8 6		SEP * NEXT, * STORE SEP PHI SEP PLO * SET L	R8 READ NU ED ON TAP R8 R6 R8 R6	; Read (But ; ignore) start ; address. ; Finish reading ; start address. MBER OF BYTES	007A 007A 007A 007A 007A 007A 007C 52 007D 64 007E 22 007F 88 007F 0080 73 0081 98 0081 98 0082 52 0082 52 0083	3 EE 2 3 3 5 2 5	ERR:	ERR ROU LDI STR OUT DEC GLO STXD GHI STR	********* JTINE #EE R2 4 R2 RE RE RE	######################################
0051 0051 0051 0051 0051 0052 0052 0053 0053 0053 0053 0053 0055 0055	8 8 6 8 6	;	SEP * NEXT, * STORE SEP PHI SEP PLO * SET L	R8 READ NU DON TAP R8 R6 R8 R6	; Read (But; ignore) start; address.; Finish reading; start address. MBER OF BYTES TA READ; Set up byte	007A 007A 007A 007A 007A 007A 007C 52 007D 64 007E 22 007F 86 007F 0080 73 0081 96 0081 0082 52 0082 0083 3F	3 EE 2 3 5 5 7 85	ERR:	ERR ROU LD! STR OUT DEC GLO STXD GHI STR	********* JTINE #EE R2 4 R2 RE RE RE	######################################
0051 0051 0051 0051 0051 0052 0052 0053 0053 0053 0053 0054 0055 0056 0057 0057 0057 0057	8 8 6 8 6		SEP * NEXT, * STORE SEP PHI SEP PLO * SET L DEC GHI	R8 READ NU ED ON TAP R8 R6 R8 R6	; Read (But ; ignore) start ; address. ; Finish reading ; start address. MBER OF BYTES	007A 007A 007A 007A 007A 007A 007C 52 007D 64 007E 2007F 8E 007F 0080 73 0081 9E 0082 0082 0082 0083 3F 0083 37 0085 37 0088	3 EE 2 3 5 5 7 85	ERR:	ERR ROU LD! STR OUT DEC GLO STXD GHI STR BN4 B4	********* JTINE #EE R2 4 R2 RE RE RE LOOP	######################################
0051 0051 0051 0051 0051 0052 0052 0053 0053 0053 0053 0054 0055 0056 0057 0057 0057 0057 0057 0057	8 8 6 8 6		SEP * NEXT, * STORE SEP PHI SEP PLO * SET L DEC	R8 READ NU ED ON TAP R8 R6 R8 R6 R8 R6 P FOR DA	; Read (But; ignore) start; address.; Finish reading; start address. MBER OF BYTES TA READ ; Set up byte; counter; so that when; all bytes have	007A 007A 007A 007A 007A 007A 007C 007D 007E 007F 0080 73 0081 0082 0082 0083 0083 0085 37 0088 0089	3 EE 2 3 5 5 7 85	ERR:	ERR ROU LD! STR OUT DEC GLO STXD GHI STR BN4 B4 BR	********* JTINE #EE R2 4 R2 RE R2 RE RE R2 LOOP LOOP2 GO	######################################
0051 0051 0051 0051 0051 0052 0053 0053 0053 0053 0054 0055 0055 0057 0057 0057 0057 0057	8 8 6 8 6 6 6 6		SEP * NEXT, * STORE SEP PHI SEP PLO * SET L DEC GHI ADI	R8 READ NU ED ON TAP R8 R6 R8 R6 R9 R6 P FOR DA R6	; Read (But; ignore) start; address.; Finish reading; start address. MBER OF BYTES TA READ; Set up byte; counter; so that when	007A 007A 007A 007A 007A 007A 007A 007C 52 007D 64 007E 22 007F 88 007F 0080 73 0081 0082 52 0082 0083 37 0083 37 0085 37 0087 0088 0089 0089	3 EE 2 3 5 5 7 85	ERR: LOOP: LOOP2: ;******	ERR ROU LD! STR OUT DEC GLO STXD GHI STR BN4 B4 BR ********************************	********* JTINE #EE R2 4 R2 RE RE RE LOOP LOOP2 GO	ERROR "PROCESSOR" ; Load "EE" ; Display "EE" ; Get low address ; byte ; Get high address ; byte ; Save error ; address on stack ; loop "till I-key ; is pressed". ; Start all over ; again
0051 0051 0051 0051 0051 0052 0053 0053 0053 0053 0053 0054 0055 0055	8 6 8 6 6 6 0 0 1	;;;; CNTSET:	SEP * NEXT, * STORE SEP PHI SEP PLO * SET L DEC GHI ADI PHI	R8 READ NUED ON TAP R8 R6 R8 R6 IP FOR DA R6 R6 R6	; Read (But; ignore) start; address.; Finish reading; start address. MBER OF BYTES TA READ ; Set up byte; counter; so that when; all bytes have; been read.	007A 007A 007A 007A 007A 007A 007A 007C 52 007D 64 007E 22 007F 0080 73 0081 9E 0082 52 0082 52 0083 3F 0083 3F 0085 37 0087 3088 0089 0089	8 EE 2 2 3 5 5 7 85 0 00	LOOP: LOOP2: ;******;	******* ERR ROU LD! STR OUT DEC GLO STXD GHI STR BN4 B4 BR ********	******** JTINE #EE R2 4 R2 RE RE RE LOOP LOOP2 GO ***********	ERROR "PROCESSOR" ; Load "EE" ; Display "EE" ; Get low address ; byte ; Get high address ; byte ; Save error ; address on stack ; loop "till I-key ; is pressed". ; Start all over ; again ************************************
0051 0051 0051 0051 0051 0052 0053 0053 0053 0053 0054 0055 0055 0057 0057 0057 0057 0057	8 8 6 8 6 6 6 0 1 6		SEP * NEXT, * STORE SEP PHI SEP PLO * SET L DEC GHI ADI PHI SEP	R8 READ NU ED ON TAP R8 R6 R8 R6 IP FOR DA R6 R6 #01	; Read (But; ignore) start; address.; Finish reading; start address. MBER OF BYTES TA READ ; Set up byte; counter; so that when; all bytes have; been read. ; Read a data byte	007A 007A 007A 007A 007A 007A 007C 52 007D 64 007E 22 007F 86 0081 96 0081 0082 52 0082 0083 36 0083 0085 37 0088 0089 0089 0089 0089	3 EE 2 3 5 5 7 85 0 0 0 0 0	ERR: LOOP: LOOP2: ;******	ERR ROU LD! STR OUT DEC GLO STXD GHI STR BN4 B4 BR ********************************	********* JTINE #EE R2 4 R2 RE RE RE LOOP LOOP2 GO **********************************	ERROR "PROCESSOR" ; Load "EE" ; Display "EE" ; Get low address ; byte ; Get high address ; byte ; Save error ; address on stack ; loop "till l-key ; is pressed". ; Start all over ; again
0051 0051 0051 0051 0051 0052 0053 0053 0053 0053 0053 0054 0055 0056 0057 0057 0057 0057 0057 0057	8 8 6 8 6 6 6 6 6 8 8 8	;;;; CNTSET:	SEP * NEXT, * STORE SEP PHI SEP PLO * SET L DEC GHI ADI PHI SEP STR	R8 READ NUED ON TAP R8 R6 R8 R6 IP FOR DA R6 R6 R6 R6 #01 R6	; Read (But; ignore) start; address.; Finish reading; start address. MBER OF BYTES TA READ ; Set up byte; counter; so that when; all bytes have; been read.	007A 007A 007A 007A 007A 007A 007C 52 007D 64 007E 22 007F 80 0081 0081 0082 52 0082 52 0083 36 0083 37 0083 30 0089 0089 0089 0089 0089 0089	3 EE 2 3 5 6 7 85 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	LOOP: LOOP2: ;******;	ERR ROU LD! STR OUT DEC GLO STXD GHI STR BN4 B4 BR ********************************	******** JTINE #EE R2 4 R2 RE RE RE LOOP LOOP2 GO ********* INERE RB #08 R7	ERROR "PROCESSOR" ; Load "EE" ; Display "EE" ; Get low address ; byte ; Get high address ; byte ; Save error ; address on stack ; loop "till I-key ; is pressed". ; Start all over ; again ************************************
0051 0051 0051 0051 0051 0052 0052 0053 0053 0053 0053 0053 0055 0056 0057 0057 0057 0057 0057 0057	8 8 6 8 6 6 6 0 1 6 8 8 8	;;;; CNTSET:	SEP * NEXT, * STORE SEP PHI SEP PLO * SET L DEC GHI ADI PHI SEP STR INC	R8 READ NU D ON TAP R8 R6 R8 R6 P FOR DA R6 R6 #01 R6 R8 R8	; Read (But; ignore) start; address.; Finish reading; start address. MBER OF BYTES TA READ ; Set up byte; counter; so that when; all bytes have; been read. ; Read a data byte; Store it in; memory	007A 007A 007A 007A 007A 007A 007C 007D 007C 007D 008C 0081 0082 0081 0082 0082 0083 0083 37 0083 37 0085 37 0087 30 0089 0089 0089 0089 0089 0089 0080 A9	3 EE 2 3 5 6 7 85 0 00 00 00 00 00 00 00 00 00 00 00 00	LOOP: LOOP2: ;****** ;******; DIRET: D1:	ERR ROU LD! STR OUT DEC GLO STXD GHI STR BN4 BR ###################################	******** JTINE #EE R2 4 R2 RE RE RE LOOP LOOP2 GO ********* INERE RB #08 R7 R9	ERROR "PROCESSOR" ; Load "EE" ; Display "EE" ; Get low address ; byte ; Get high address ; byte ; Save error ; address on stack ; loop "till 1-key ; is pressed". ; Start all over ; again ***********************************
0051 0051 0051 0051 0051 0052 0052 0053 0053 0053 0053 0053 0054 0055 0056 0057 0057 0057 0057 0057 0057	8 6 6 6 6 6 6 8 8 8 8	;;;; CNTSET:	SEP * NEXT; * STORE SEP PHI SEP PLO * SET L DEC GHI ADI PHI SEP STR INC GLO	R8 READ NU ED ON TAP R8 R6 R8 R6 IP FOR DA R6 R6 #01 R6 R8 R8 RE	; Read (But; ignore) start; address.; Finish reading; start address. MBER OF BYTES TA READ ; Set up byte; counter; so that when; all bytes have; been read. ; Read a data byte; Store it in; memory; increment memory	007A 007A 007A 007A 007A 007A 007A 007C 52 007D 64 007E 22 007F 0080 73 0081 9E 0081 9E 0083 3F 0083 3F 0083 3F 0085 37 0089 0089 0089 0089 0089 0089 0080 0	3 EE 2 3 5 6 7 85 0 00 00 00 00 00 00 00 00 00 00 00 00	LOOP: LOOP2: ;****** j. DIRET:	ERR ROU LD! STR OUT DEC GLO STXD GHI STR BN4 BR ###################################	******** JTINE #EE R2 4 R2 RE RE RE LOOP LOOP2 GO ********* INERE RB #08 R7	######################################
0051 0051 0051 0051 0051 0052 0053 0053 0053 0053 0053 0053 0055 0056 0057 0057 0057 0057 0057 0057	8 8 6 8 6 6 6 6 8 8 8 8 8	;;; ;; ;; ;; ;; ;; ;; ;; ;; ;; ;; ;; ;;	SEP * NEXT, * STORE SEP PHI SEP PLO * SET L DEC GHI ADI PHI SEP STR INC GLO STR	R8 READ NUED ON TAP R8 R6 R8 R6 IP FOR DA R6 R6 #01 R6 R8 RE RE RE	; Read (But ; ignore) start ; address. ; Finish reading ; start address. MBER OF BYTES TA READ ; Set up byte ; counter ; so that when ; all bytes have ; been read. ; Read a data byte ; Store it in ; memory ; increment memory ; address ; Display low-	007A 007A 007A 007A 007A 007A 007A 007C 52 007D 64 007E 22 007F 88 0081 98 0081 98 0082 52 0083 37 0083 37 0083 37 0083 0089 0089 0089 0089 0089 0089 0089	3 EE 2 3 5 5 6 8 5 7 8 5 8 5 8 5 8 5 8 5 8 5 8 5 8 5 8	LOOP: LOOP2: ;****** ;******; DIRET: D1:	ERR ROIL LD! STR OUT DEC GLO STXD GHI STR BN4 B4 BR ******* DI ROUT SEP LD! PLO PLO B3	******** JTINE #EE R2 4 R2 RE RE R2 LOOP LOOP2 GO ********* INERE RB #08 R7 R9 LHTRAN	ERROR "PROCESSOR" ; Load "EE" ; Display "EE" ; Get low address ; byte ; Get high address ; byte ; Save error ; address on stack ; loop "till I-key ; is pressed". ; Start all over ; again ************************************
0051 0051 0051 0051 0051 0052 0053 0053 0053 0053 0053 0053 0055 0056 0057 0057 0057 0057 0057 0057	8 8 6 8 6 6 6 6 8 8 8 8 8 8 8 8 8 8 8 8	;;;; ;;; ;; ;; ;; ;; ;; ;; ;; ;; ;; ;;	SEP * NEXT, * STORE SEP PHI SEP PLO * SET L DEC GHI ADI PHI SEP STR INC GLO STR OUT	R8 READ NU ED ON TAP R8 R6 R8 R6 IP FOR DA R6 R6 R6 #01 R6 R8 RE RE RE RE RE R2 4	; Read (But; ignore) start; address.; Finish reading; start address. MBER OF BYTES TA READ ; Set up byte; counter; so that when; all bytes have; been read. ; Read a data byte; Store it in; memory; Increment memory; address ; Display low-; order address.	007A 007A 007A 007A 007A 007A 007A 007C 52 007D 64 007E 22 007F 0080 73 0081 9E 0081 9E 0083 3F 0083 3F 0083 3F 0085 37 0089 0089 0089 0089 0089 0089 0080 0	3 EE 2 3 5 5 6 8 5 7 8 5 8 5 8 5 8 5 8 5 8 5 8 5 8 5 8	LOOP: LOOP2: ;****** jDIRET: DI: LHTRAN:	ERR ROU LD! STR OUT DEC GLO STXD GHI STR BN4 BR ###################################	******** JTINE #EE R2 4 R2 RE RE RE LOOP LOOP2 GO ********* INERE RB #08 R7 R9	######################################
0051 0051 0051 0051 0051 0052 0052 0053 0053 0053 0053 0053 0055 0056 0056	8 8 6 8 6 6 6 6 8 8 8 8 8 8 8 8 8 8 8 8	;;;; ;;; ;; ;; ;; ;; ;; ;; ;; ;; ;; ;;	SEP * NEXT, * STORE SEP PHI SEP PLO * SET L DEC GHI ADI PHI SEP STR INC GLO STR OUT DEC	R8 READ NU CD ON TAP R8 R6 R8 R6 IP FOR DA R6 R6 R6 R7 R8 R8 RE	; Read (But; ignore) start; address.; Finish reading; start address. MBER OF BYTES TA READ ; Set up byte; counter; so that when; all bytes have; been read. ; Read a data byte; Store it in; memory; increment memory; address ; Display low-; order address.; Reposition SP	007A 007A 007A 007A 007A 007A 007A 007C 52 007D 64 007E 22 007F 80 0081 0081 0082 52 0083 52 0083 36 0083 37 0085 37 0088 0089 0089 0089 0089 0089 0089 008	3 EE 2 2 3 3 5 8 5 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	LOOP: LOOP2: ;****** jDIRET: DI: LHTRAN:	ERR ROU LD! STR OUT DEC GLO STXD GHI STR BN4 B4 BR ******** DI ROUT SEP LD! PLO PLO PLO B3 GH! SM!	********* JTINE #EE R2 4 R2 RE RE R2 LOOP LOOP2 G0 ********* RB #08 R7 R9 LHTRAN R9 #01	ERROR "PROCESSOR" ; Load "EE" ; Display "EE" ; Get low address ; byte ; Get high address ; byte ; Gat high address ; byte ; Save error ; address on stack ; loop "till I-key ; is pressed". ; Start all over ; again ************************************
0051 0051 0051 0051 0051 0052 0053 0053 0053 0053 0053 0053 0055 0055 0056 0057 0057 0057 0057 0057	8 8 6 8 6 6 6 6 8 8 8 8 8 8 8 8 8 8 8 8	;;; ;;; ;; ;; ;; ;; ;; ;; ;; ;; ;; ;; ;	SEP * NEXT, * STORE SEP PHI SEP PLO * SET L DEC GHI ADI PHI SEP STR INC GLO STR OUT DEC	R8 READ NU ED ON TAP R8 R6 R8 R6 IP FOR DA R6 R6 R6 #01 R6 R8 RE RE RE RE RE R2 4	; Read (But; ignore) start; address.; Finish reading; start address. MBER OF BYTES TA READ ; Set up byte; counter; so that when; all bytes have; been read. ; Read a data byte; Store it in; memory; increment memory; address ; Display low-; order address.; Reposition SP	007A 007A 007A 007A 007A 007A 007A 007C 52 007D 64 007E 22 007F 80 0081 0081 0082 52 0083 36 0083 37 0085 37 0088 0089 0089 0089 0089 0089 0089 008	3 EE 2 2 3 3 7 85 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	LOOP: LOOP2: ;******; DIRET: DI: LHTRAN:	ERR ROU LD! STR OUT DEC GLO STXD GHI STR BN4 B4 BR ********************************	********* JTINE #EE R2 4 R2 RE RE RE LOOP LOOP2 GO ********* RB #08 R7 R9 LHTRAN R9	ERROR "PROCESSOR" ; Load "EE" ; Display "EE" ; Get low address ; byte ; Get high address ; byte ; Gat high address ; byte ; Save error ; address on stack ; loop "till I-key ; is pressed". ; Start all over ; again ************************************

ADDR CODE	LABEL	OPCODE	OPERAND	COMMENT
0097 0097 27 0098 30 A 009A F8 0		DEC BR LD1	R7 DNCHK #00	; Got a "1" Bit. ; Got a zero bit.
009A 009C FC 0 009C 009E 3B A		ADI BNF '	#01 HLTRAN	; Test ; For excessive ; pulse width.
00A0 30 7 00A2 3E 9 00A4		BR BN3	ERR TLTST	; Error
00A4 89 00A5 32 AI 00A5 00A7		GLO BZ	R9 PARCHK	; Got a byte? ; check parity if ; so.
00A7 02 00A7	;	LDN	R2	; Not full byte ; yet.
00A8 7E 00A8 00A9 52		RSHL	R2	; Insert bit into ; buffer
00AA 29 00AB 30 8E 00AD	Ē.	DEC BR	R9 LHTRAN	
00AD 87 00AD	PARCHK:	GLO	R7	; Check for even ; parity.
00AE F6 00AF 02 00AF		shr LDN	R2	; Put data value ; into D-Reg.
00B0 3B 89	•	BNF	DIRET	; Return if parity ; okay.
00B2 30 7A 00B2 00B4	\	BR	ERR	; Call error ; routine

Note from the Editor:

The preceding assembly listing is the output of the Quest Editor Assembler.

 0000
 F800
 B3F8
 07A3
 D393
 BBB8
 B6B5
 B2F8
 6AAB

 0010
 F88A
 A8F8
 7AA6
 F8FF
 A2E2
 F871
 A5F8
 AA52

 0020
 6422
 D5BE
 D5AE
 D5AE
 F80D
 B9F8
 0052
 6422

 0030
 84F3
 324F
 F80A
 B7DB
 3637
 99FF
 013B
 473E

 0040
 3B97
 3237
 2730
 3797
 3A34
 02FC
 0130
 2D3E

 0050
 4FDB
 D8DB
 86DB
 A626
 96FC
 01B6
 D85E
 1E8E

 0060
 5264
 2226
 963A
 5C30
 1DD3
 3F69
 376C
 3000

 0070
 D33F
 716C
 6437
 7522
 3070
 F8EE
 5264
 228E

 0080
 739E
 523F
 8337
 8530
 00DB
 F808
 A7A9
 368E

 0040
 307A
 3E9C
 8932

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CHECKEOOK -

BEEKONONG

by Gary Gehlhoff

The program title has included the word "reckoning" because when it comes to the end of the month that's my day of reckoning (Or I'm in the RED again).

The program uses the standard method of checkbook balancing, by subtracting the outstanding checks from the sum of the balance shown on the bank statement and the deposits not shown on the statement.

Each input requires the dollars number to be separated by a comma from the cents number. To signal the program that the list of "Outstanding checks" or the list of "Unshown deposits" is complete a 00,00 should be entered.

The program is straightforward with only minor subtleties around creating decimal division (lines 200,320, and 460) and outputting a number that's less than 10 cents (Line 480).

```
10
    D=0
20
     C=0
    M=0
30
40
     P=0
50
    N=0
60
     O=0
70
     0=0
80
    Ř=0
90
    PR "CHECKBOOK"
95
    PR
100 PR "CHECKS"
110 PR "OUTSTANDING"
120 PR "DOLS, CTS"
130 PR "$";
```

```
INPUT D,C
IF D=0 THEN IF C=0 THEN GOTO 190
150
160
     M=M+D
170 P=P+C
180
     GOTO 130
190 M=M+ P/100
200
     P=P- P/100 * 100
210
     PR "TOTAL $";M;".";P
220
230
240
     PR "DEPOSITS NOT SHOWN"
250
     PR "$";
260
     INPUT D.C
270
     IF D=0 THEN IF C=0 THEN GOTO 310
280
290
     Q=Q+C
     ĠOŤO 250
300
310 N=N+ C/100
     Q=Q- Q/100 * 100
320
330
     PR "----"
340
     PR "TOTAL $":N:".";Q
350
360
     PR "BAL FROM STATEMENT"
365
     PR "$":
     INPUT O,R
370
380
390
     PR "CURRENT BALANCE"
400
     D=N+ O-M
410
     C=R+ Q-P
420
     IF C>=0 THEN GOTO 450
430
    D= D-1
440
     C = C + 100
450 D= D+ C/100
460 C=C- C/100 * 100
470 PR "$";D;".";
480
     IF C<10 THEN PR "0";
490 PR C
495 PR
    END
499
```

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