EAST WEST UNIVERSITY

Project Report

Course Name: Computer Architecture

Course code: CSE360

Section: 03

Group No: 10

Submitted To

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Title: Construct an Interpreter for Multiple Accumulators CPU Organization.

Objectives: The Accumulator is a register in which intermediate arithmetic logic unit (ALU) results are stored. Modern computer systems often have multiple general-purpose registers that can operate as accumulators, and the term is no longer as common as it once was. Creating a translator written in C language that will help to convert an assembly language based on a given set of basic instructions for a machine, which will have multiple registers, which are accumulators that will have all the operands in memory. Our goal will be to make an assembly language interpret in C language that has the ability with the following instructions for a machine having multiple register. There will be given some set of assembly code instructions in the database. There will be some functions to develop our goal.

Tools: Computer, CodeBlocks IDE.