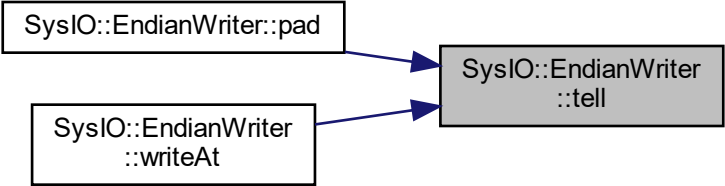


SysIO::EndianWriter::pad

SysIO::EndianWriter
::writeAt

SysIO::EndianWriter
::tell



```
graph LR; A[SysIO::EndianWriter::pad] --> D[SysIO::EndianWriter::tell]; B[SysIO::EndianWriter::writeAt] --> D;
```