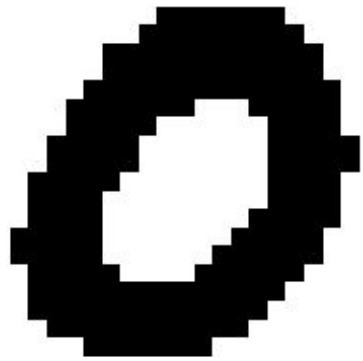


RAIO-X DA SUA ROM VERILOG
(O que a FPGA realmente está vendo)

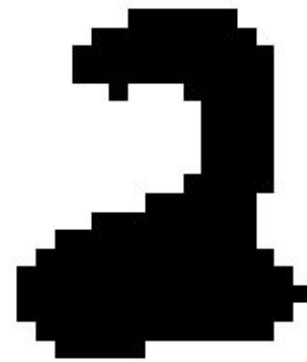
DIGITO 0
(Bit 0)



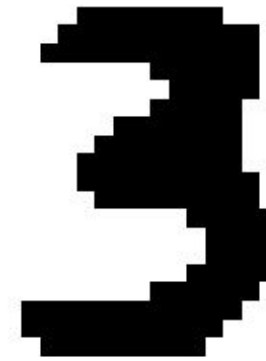
DIGITO 1
(Bit 1)



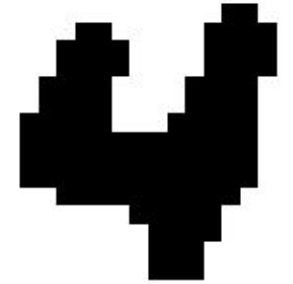
DIGITO 2
(Bit 2)



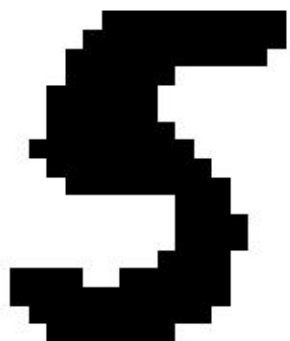
DIGITO 3
(Bit 3)



DIGITO 4
(Bit 4)



DIGITO 5
(Bit 5)



DIGITO 6
(Bit 6)



DIGITO 7
(Bit 7)



DIGITO 8
(Bit 8)



DIGITO 9
(Bit 9)

