



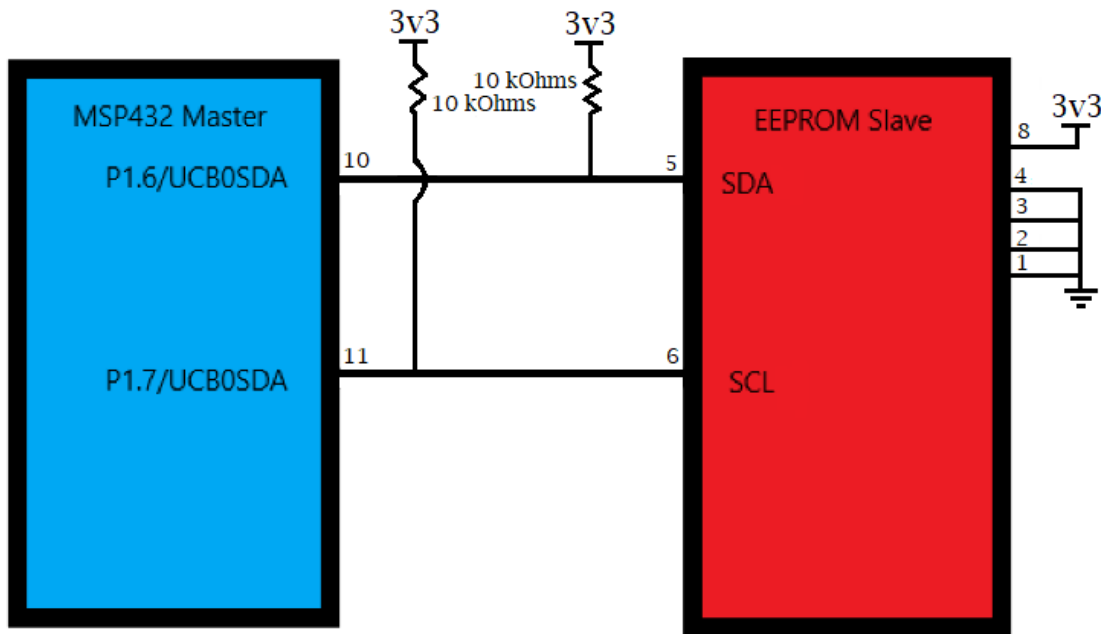
## **A10 - I2C EEPROM**

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**Class:** CPE 329-03

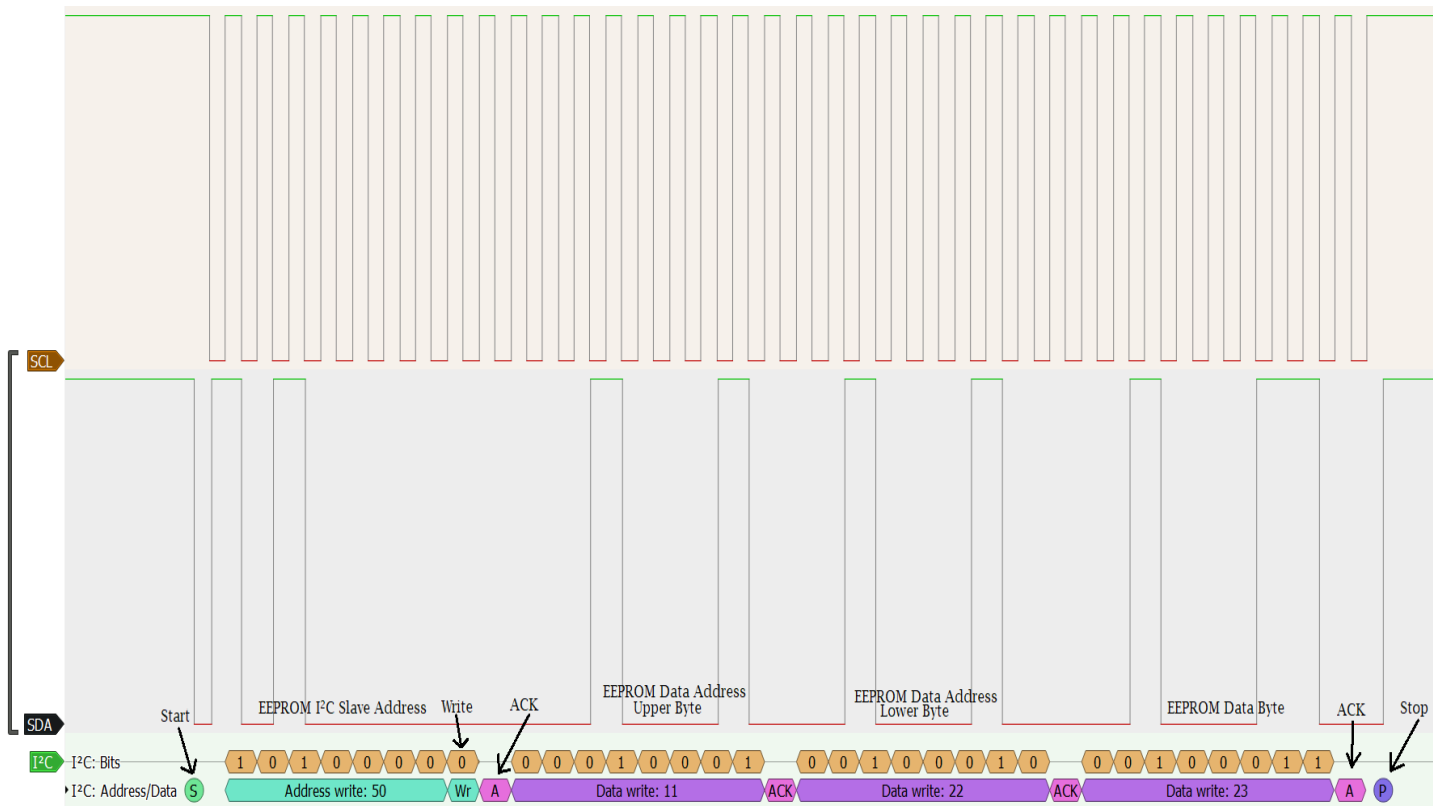
**Professor:** Gerfen, Jeffrey

## Schematic

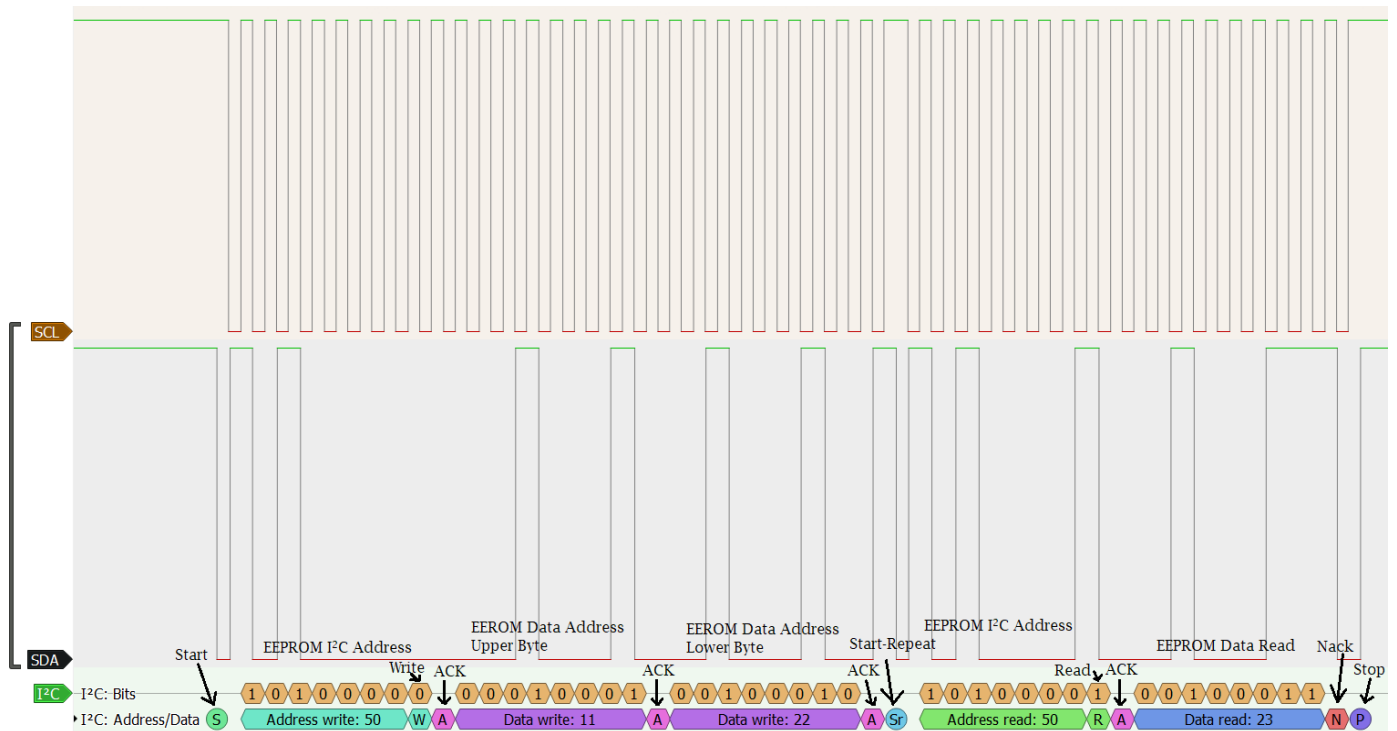


*Figure 1: Schematic Diagram*

## Annotated Screenshots



*Figure 2: EEPROM I2C Byte Write*



*Figure 3: EEPROM I<sup>2</sup>C Byte Read*

## Questions

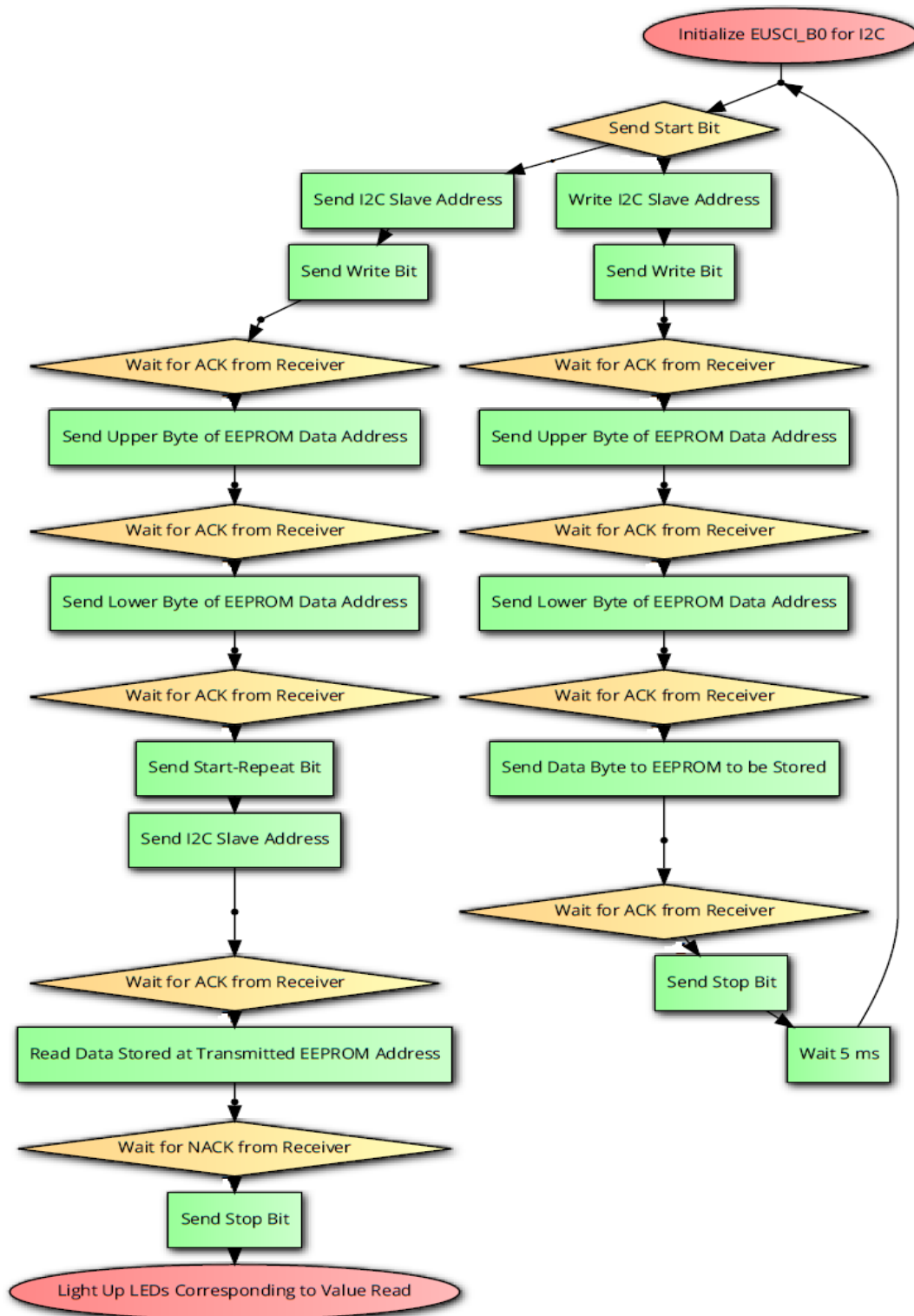
1. Is the first ACK bit affected?

The first ACK is instead a NACK, as it is high rather than low.

2. What about the rest of the bits?

The rest of the bits are not transmitted, as the NACK bit received ended the transmission early.

## Program Flow



*Figure 4: Program flow of the provided I<sup>2</sup>C Read-Write Sample Code*