18220 Exam 2 Review

We got the POWER

Def. Power is defined as
$$P = \frac{1}{1} V$$
cument

Cor: W/V=IR we can write:

$$P = IV = I^2R = \frac{V^2}{R}$$

<u>Def:</u> we define the capacitor equation as

$$i_c(t) = C \frac{dV_c(t)}{dt}$$

Lor: Using power, we have

$$P_c(t) = i_c(t) \cdot V_c(t)$$

Since energy = power · time,

$$E_{c} = \int P_{c}(t) dt = \int i_{c}(t) V_{c}(t) dt$$

$$= \int \frac{dV_{c}(t)}{dt} V_{c}(t) dt$$

$$= \int \int V_{c}(t) dV_{c}(t) dt$$

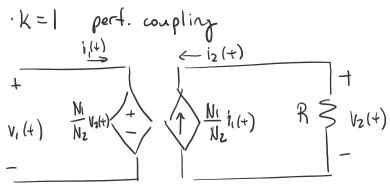
$$= \int C V_{c}^{2}$$

Def: The industr equation is

$$V_{c}(t) = L \frac{di_{c}(t)}{dt}$$

Cor: Similar to the deriv. for the cap, we can get

long Time / Frequency Analysis of L/C
Consider
Vs (+) Vs (+) Vs (+)
where C and L are not charged, then $ \frac{t=0}{t\to\infty} $
Cap: $\frac{t=0}{\text{short}}$ $\frac{t\to\infty}{\text{open}}$
Ind: open short
Det: The complex impedance for a capacitor is
$Z_c = \frac{1}{\omega C}$
Let: The complex impedance for an inductor is Z _L = juL here w>00 blc sudden jump
Cap: $w \to \infty$ $w = 0$ That: open short over long time, $w \to 0$
Just an interesting coincidence
Transforming the POWER
Def. We define M to be the mutual inductance. M=KSCICZ (Henries
Ref! k is the coupling coefficient. (unitless)
· k=0 if no coupling · k=1 is a partect transformer
Dot: An ideal transformer is defined to be:
· L, M -> 20 so little carrent flows through inductors and therefore the transformer stores no energy



(a: Using transformer properties, we can derme

$$|V_{1}(t)| = |V_{1}|^{2} |V_{1}(t)|^{2} |V_{1}(t)$$

Det: A portect transformer is a transformer w/ k=1 but

MIL not nec. -> 00.

This means we have a larkage inductance, so when we derive the equivalent resistance, we instead have:

+
$$V_{i(t)}$$
 $Lp \xi$ ξ $k_{eq} = \left(\frac{N_{i}}{N_{i}}\right)^{2} R$

where Lp is the leakage inductance, can be defined in several ways. One such way is:

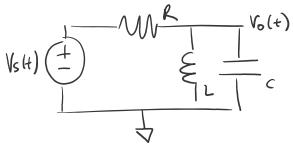
Pet: A nen-ideal transformer in general does not have any ideal assumptions. You have to write out the coupled differential equations to some these problems.

Oscillating w/the POWER

Def: A RLC circuit has a resistor (usually in series)

Def: A RLC circuit has a resistor (usually in series) what L, C either in parallel or series.

For our example, we'll do the one in parallel:



If we write the diff. of for this circuit, we get $\frac{d^2V_0(t)}{dt^2} + \frac{1}{RC}\frac{dV_0(t)}{dt} + \frac{1}{LC}V_0(t) = \frac{1}{RC}\frac{dV_0(t)}{dt}$

To solve the homogeneous solution, we first make an ansatz of

Plugging in gives us $S = -\frac{1}{2RC} \pm \sqrt{\frac{1}{2RC}^2 - \frac{1}{LC}}$ we like to define $d = \frac{1}{2RC}$ $\tau = \int C$ $w = \frac{1}{LC} = \frac{1}{2RC}$

Now, before we write our general solutions, we need to consider 3 cases

· det. is <0, then we have a complex root, i.e. $S=a\pm jb$

Aet (a+jb) + Azet (a-jb)

=> A, eat cos(bt) + Azeat sin(bt) (spanssame sol. space)

this called inderdamping.

Out = 0 Then we only have one sol. It turns out we need to make our general solution

A,eat + Azteat

in this case.

This is called critical damping.

. det > 0 Both characteristic roots are real, so

A, e + (a+b) + Aze+(a-b)

is our general solution

This is called overdamping.



Kesonating w/ POWEK Consider the LC circuit + | V2H) -

KVL gives us:

$$0 - V_{c}(t) - V_{L}(t) = 0$$

 $V_{c}(t) + V_{c}(t) = 0 = > V_{c}(t) + L \frac{di_{c}(t)}{dt} = 0$
KCL gives us:

$$-j_c(+)+i_c(+)=0$$

$$\Rightarrow C \frac{dV_{c(t)}}{dt} = i_{c}(t)$$

$$V_{c}(t) + L \frac{d}{dt} \left(C \frac{dV_{c}(t)}{dt} \right) = 0$$

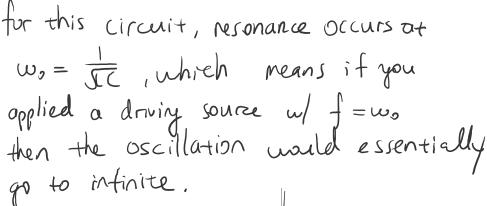
$$\frac{d^{2}V_{c}(t)}{dt^{2}} + \frac{1}{LC} V_{c}(t) = 0$$

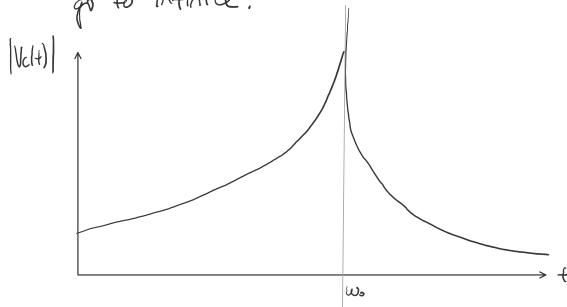
Solving w/ Aest gues
$$S^{2} = -\frac{1}{L} = S = \pm j \left(\frac{1}{5LC} \right)$$

our solution is in general

$$V_{c}(t) = A_{1} \cos(\omega_{s}t) + A_{2} \sin(\omega_{s}t)$$

$$= K_{cos}(\omega_{s}t + \emptyset)$$





Def: Since the amplitude at resonance is not so in real life, we define our quality factor as ∞ height of resonance, which is

$$Q = \frac{\omega}{2\alpha}$$

Notice that since we did not have a resistor in our LC circuit, there was no damping. In general, the damping caused by the Ms istor depends on the circuit, i.e.

	RLC series	RLC parallel
L	JR ZL	1 2RC