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PROJECTS

-Kernel for the RISC V architecture

Implementation of memory allocator (contiguous memory allocation; first fit), threads, synchronous and asynchronous (on interrupts from timer and keyboard) context switching, semaphores and periodic threads.

-MicroJava Compiler

MicroJava is a simpler version of Java. It supports namespaces, if else statements, arrays, for loops, upacking of arrays (check specification for more details) and classes (polymorphism) with static fields and static initializers.

-Store app

Dockerized app for selling products with calculating statistics using Apache Spark and Ethererum blockchain payments. Implemented in Python using Flask.

-EPutuj

A ride-sharing platform connecting drivers with empty seats to passengers traveling in the same direction. Users can also request specific rides, and drivers can schedule those rides. A team project, implemented with Codelgniter4 framework.

-B* tree implementation (C++)

-Hashing (C++)

Implemented a hashing table where the collision resolving is done using split sequence linear hashing. Implemented extendible hashing as an example of external hashing.

-Decision tree (Python)

Implemented a magic square solver, where every node is the unique state of the magic square, and its children are the possibilites.

-Graph (C)

Implementation of a graph using matrix representation. Using that graph to calculate a winner of a ludo game, where one player moves a tile per move, and the other on odd moves moves one tile per move, while on even moves moves two tiles per move.

EXPERIENCE

UNDERGRADUATE TEACHING ASSISTANT

OCT 2022 - PRESENT

At the School of Electrical Engineering, University of Belgrade

EDUCATION

SCHOOL OF ELECTRICAL ENGINEERING

SOFTWARE ENGINEERING

GPA: 9,71