

May 4th 1:30 PM (All team members) first construction of the program. Added all the conversion functions between binary, decimal, and hexadecimal. Implemented all the arithmetic and logical operations as well as the shifting and set less operations. Created the register map and instructions vector

May 6th (Hamza Algobba) added the parsing functions and the validation functions

May 7th (All team members) implemented auipc and lui instructions. Added validation of labels and created the labels map.

May 9th (All team members) implemented the store, lode, jump, and branching instructions. Created the map mem that maps memory addresses to values.

May 10th (All team members) implemented the program counter

May 11th (All team members) Tested the simulator with some Risc-v programs and debugged whenever necessary.

