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 Grupo: 3CV8
 Fecha de entrega: 10 de junio del 2020

Práctica 13

Microinstrucciones

Instrucción													ALUOP							
	SDMP	UP	DW	WPC	SR2	SWD	SEXT	SHE	DIR	WR	SOP1	SOP2	[3]	[2]	[1]	[0]	SDMD	WD	SR	LF
LI	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
LWI	0	0	0	0	0	1	0	0	0	1	0	0	0	0	0	0	1	0	0	0
LW	0	0	0	0	0	1	1	0	0	1	0	1	0	0	1	1	0	0	0	1
SWI	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0
SW	0	0	0	0	1	0	1	0	0	0	0	1	0	0	1	1	0	1	0	1
ADD	0	0	0	0	0	1	0	0	0	1	0	0	0	0	1	1	0	0	1	1
SUB	0	0	0	0	0	1	0	0	0	1	0	0	0	1	1	1	0	0	1	1
ADDI	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	1	0	0	1	1
SUBI	0	0	0	0	0	1	0	0	0	1	0	1	0	1	1	1	0	0	1	1
AND	0	0	0	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	1	1
OR	0	0	0	0	0	1	0	0	0	1	0	0	0	0	0	1	0	0	1	1
XOR	0	0	0	0	0	1	0	0	0	1	0	0	0	0	1	0	0	0	1	1
NAND	0	0	0	0	0	1	0	0	0	1	0	0	1	1	0	1	0	0	1	1
NOR	0	0	0	0	0	1	0	0	0	1	0	0	1	1	0	0	0	0	1	1
XNOR	0	0	0	0	0	1	0	0	0	1	0	0	1	0	1	0	0	0	1	1
NOT	0	0	0	0	0	1	0	0	0	1	0	0	1	1	0	1	0	0	1	1
ANDI	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	0	0	1	1
ORI	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	1	0	0	1	1
XORI	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0	0	0	1	1
NANDI	0	0	0	0	0	1	0	0	0	1	0	1	1	1	0	1	0	0	1	1

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[illegible]