→ IEEE floating points (8-4m)  → Division (anyone of both)  daigrams: floating points.   Hunit 2: → (10 marks)  → added instructions  — Typu of addressing  → pipe lining  → 8086 mirroprocessor Architecture.  (no numericals, complete theory), (complete notes).   Hard wire control unit  → microprogramm mirroprogrammed control  unit.  Explain with daigram.   **  Unit 4: → (12 marks)  **  **  **  **  **  **  **  **  **		C O
Smoons	9	Uniti: -> (13 marks).
Mumerical: → Booths algorithm		Theory: -> (less) -> von Neuman algorithm /architect)  -> Components of Computer.  -> But structure
# Unit 2: → added instructions  — Types of addressing  — pipe lining  — 8086 microprocessor Architecture.  [no numericals, complete theory), (complete notes).  # Unit 3: → [5 marks]  — Hard wire control unit  — microprograms microprogrammed control  unit.  Explain with daigram.  # Unit 4: → [12 marks]  numericals: → Page reprensent RAM  — memory design and its  — hemory design  — Cache mapping Types.  — segmentation paging  Thory: → memory hiarchy 12-3 marks)	(most)	Numerical: > Booths algorithm  - multiplication using bit pair recarding  & booths
→ added instructions  — Types of addressing  — pipe lining  — 8086 microprocessor Architecture.  (no numericals, complete theory), (complete notes).  A Unit 3: → (5 marks)  — Hard wire control unit  — microprograms microprogrammed control  unit.  Explain with daigram.  A Unit 4: → (12 marks)  numericals: → Page reprensent RAM  — memory design  — Memory design  — Cache mapping  Theory: → memory hiardry 12-3 marks)	9	-> IEEE floating points (8-4m)  -> Division (anyone of both)  daigrams: floating points.
→ Hard wire control unit  → microprogramm microprogrammed control  unit.  Explain with daigram.   # Unit 4:→ (12 Marks)  numericals:→ Page reprensent RAM  → Memory design  → Cache mapping  Theory:→ memory hiardry 12-3 marks)		→ added instructions  — Types of addressing  → pipe lining  → 8086 microprocessor Architecture.
numericals:   Page reprensent  Am  memory design  And its  mapping  Tynes.  Segmentation paging  Theory:   memory hiardry 12-3 marks)		-> Hard wire control unit -> microprogramm microprogrammed control unit.
Ginst overlook)	A	numericals:   Page reprensent RAM  memory design  and its  Cache mapping  segmentation paging

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Unit 5: ->	(5 monks)
Unit 5: →  → Working mechanism  → 1/P, 0/P systems.  → Every topic before	
→ Every topic before	woring periferal.