CO

☆ Unit 1 :→ (13 Marks).

(3-4m) (5m max) Theory :→ (less) → von Neuman algorithm /architect)
→ Components of Computer.
→ Bus structure

(most) Numerical :→ Booths algorithm ✓
→ multiplication using bit pair recoding & booths
→ IEEE floating points (3-4m)
→ Division (anyone of both)

daigrams :→ floating points.

☆ Unit 2 :→ (10 marks).
→ added instructions
→ Types of addressing
→ pipe lining
→ 8086 microprocessor Architecture.

(no numericals, complete theory), (complete notes).

☆ Unit 3 :→ (5 marks)
→ Hard wire control unit
→ ~~microprogramn~~ microprogrammed control unit.

Explain with daigram.

☆ Unit 4 :→ (12 Marks)

numericals :→ Page represent
→ Memory design
→ Cache mapping
→ segmentation paging

RAM and its Types.

Theory :→ memory hiarchy (2-3 marks)
Virtual memory, TLB, secondary storage (just overlook)

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Unit 5 : → (5 marks)

→ Working mechanism.
→ I/P, O/P systems.
→ Every topic before woring periferal.