

# A SLR of Modern AI-Driven SCA on NIST's PQC Standards

Edgard Nicéas Arcoverde Gusmão Lima<sup>1,2</sup>[0000–0002–5241–8092], Fábio Wladimir Monteiro Maia<sup>1,2</sup>, Tiago Alessandro Espínola Ferreira<sup>3</sup>, and Danilo Monteiro Ribeiro<sup>2</sup>

<sup>1</sup> CISSA, CESAR, Recife, Brazil  
`enagl@cesar.org.br`, `fwmm@cesar.org.br`

<sup>2</sup> CESAR School, Recife, Brazil  
`enagl@cesar.edu.br`, `fwmm@cesar.edu.br`, `dmr@cesar.school`

<sup>3</sup> PPGIA, Universidade Federal Rural de Pernambuco (UFRPE), Recife, Brazil  
`tiago.espinola@ufrpe.br`

**Abstract.** The imminent threat of quantum computing has catalyzed a global migration to Post-Quantum Cryptography (PQC), guided by the U.S. National Institute of Standards and Technology (NIST) standardization process. While these new cryptographic algorithms are designed for mathematical resilience against quantum adversaries, their physical implementations expose a critical vulnerability to Side-Channel Attacks (SCAs). This threat is fundamentally amplified by Artificial Intelligence (AI), which has transformed the adversary model from one requiring statistical analysis of numerous device interactions to one capable of potent, often single-trace, key extraction. This paper presents a systematic literature review (SLR) mapping the collision of AI-driven SCAs with the first cohort of NIST-selected PQC standards: CRYSTALS-Kyber, CRYSTALS-Dilithium, FALCON, SPHINCS+, and the prominent fourth-round candidate HQC. From the synthesized literature, we develop a comprehensive taxonomy that classifies attacks by linking specific AI methodologies to the unique vulnerabilities in each cryptographic family, such as the repetitive arithmetic in lattice-based schemes. Our analysis reveals three prevailing trends: the ascendancy of single-trace attacks that make in-field exploitation practical, the systematic neutralization of established countermeasures like masking by deep learning models, and the efficacy of hybrid attacks combining machine learning with algebraic cryptanalysis.

**Keywords:** Post-Quantum Cryptography · PQC · Side-Channel Analysis · SCA · Artificial Intelligence · AI · Machine Learning · Deep Learning · NIST PQC Standardization · ML-KEM · CRYSTALS-Kyber · ML-DSA · CRYSTALS-Dilithium · FN-DSA · FALCON · SLH-DSA · SPHINCS+ · HQC.

# 1 Introduction: The Collision of Post-Quantum Cryptography and AI-Powered Cryptanalysis

## 1.1 The Quantum Imperative and the NIST PQC Standardization

Modern digital infrastructure relies on public-key cryptography (PKC) schemes such as RSA and Elliptic Curve Cryptography (ECC), whose security stems from problems that are classically hard to solve [88]. Large-scale quantum computers running Shor’s algorithm threaten this foundation by solving those problems in polynomial time [89]. To anticipate that “quantum day,” the U.S. National Institute of Standards and Technology (NIST) launched an open competition to standardize quantum-resistant algorithms [64]. In 2022, NIST announced CRYSTALS-Kyber for public-key encryption and CRYSTALS-Dilithium for signatures, adding FALCON and SPHINCS+ as alternatives, while the code-based HQC remains under evaluation in the fourth round [3,30,4]. Table 22 summarizes the algorithms targeted in this review.

## 1.2 The Achilles’ Heel: Implementation Security and the Side-Channel Threat

Mathematical soundness alone does not guarantee security. PQC schemes must run on physical devices that leak information via side channels such as timing, power consumption, and electromagnetic emanations [53,17]. These leakages let adversaries bypass theoretical guarantees by recovering keys from real deployments [80]. NIST therefore required side-channel resilience throughout the PQC evaluation process [1], effectively crowdsourcing a global red-team exercise. Discoveries such as the FALCON-targeting attacks [46] were treated as valuable feedback that shaped countermeasures, while several proposals in Table 23 were eliminated after catastrophic implementation-level breaks. PQC’s novel arithmetic widens the attack surface, making practical defenses a primary concern.

## 1.3 The Paradigm Shift: Artificial Intelligence as a Force Multiplier

Side-channel analysis itself is being transformed by Artificial Intelligence (AI). Profiled attacks can be cast as supervised learning where traces are features and secret-dependent intermediates act as labels [20]. Deep learning—especially MLP- and CNN-based classifiers—learns leakage patterns directly from raw traces, eliminating much of the manual feature engineering required by classical statistics [57]. These models regularly defeat masking, shuffling, and other countermeasures by exploiting residual higher-order dependencies [73]. The integration of AI therefore lowers the barrier to executing high-efficacy attacks and must be assumed in any realistic threat model [80,39].

## 1.4 Problem Statement, Objectives, and Research Questions

PQC standardization, physical implementation risks, and AI-accelerated cryptanalysis intersect to form an urgent security gap. Although NIST is publishing

quantum-resistant algorithms, AI-equipped adversaries may compromise their implementations on day one. This systematic literature review (SLR) maps AI-driven side-channel attacks against CRYSTALS-Kyber, CRYSTALS-Dilithium, FALCON, SPHINCS+, and HQC. We focus on peer-reviewed or IACR ePrint-style work describing practical attacks that leverage Machine Learning, Deep Learning, or related modern AI techniques. Our objectives are to (1) catalogue the state of the art, (2) build a taxonomy that links AI vectors to cryptographic vulnerabilities, and (3) highlight trends, gaps, and open challenges to guide future defenses.

- **RQ1:** What peer-reviewed or relevant pre-print literature documents practical AI-based side-channel attacks on the NIST PQC standards and HQC?
- **RQ2:** How can those attacks be categorized to map AI techniques to the targeted cryptographic operations and leakage sources?
- **RQ3:** Which attack trends, research gaps, and challenges emerge from the synthesized data?

## 1.5 Structure of the Review

Section 2 provides background. Sections 3-4 follow the Kitchenham protocol [50,51] to detail the methodology and study selection. Sections 5 review related surveys. Section 6 report and synthesize the results with our taxonomy and analysis. Section 7 concludes and outlines future work.

# 2 Theoretical Background: The Convergence of AI, Side-Channel Analysis, and Post-Quantum Cryptography

## 2.1 The Quantum Threat to Modern Cryptography

Large-scale quantum computers pose a direct threat to modern public-key cryptography because Shor’s algorithm can factor RSA moduli and solve elliptic-curve discrete logarithms in polynomial time [88,89,76,32], while Grover’s algorithm quadratically accelerates brute-force search on symmetric primitives [33]. In contrast to symmetric schemes—which can be repaired by doubling key sizes—public-key systems such as RSA, Diffie-Hellman and ECC offer no viable parameter scale-up once a cryptanalytically relevant quantum computer becomes available. This creates an urgent need for post-quantum cryptography (PQC) [64], particularly for protecting long-term confidentiality against “harvest now, decrypt later” adversaries who can store encrypted data today and decrypt it once quantum capabilities mature [21,97,59]. More broadly, quantum algorithms fundamentally challenge assumptions underlying classical complexity theory [24,74,14,62], reinforcing the necessity of transitioning to cryptographic schemes whose security does not collapse under quantum computation.

## 2.2 Post-Quantum Cryptography and NIST’s Standardization Effort

Post-quantum cryptography (PQC) aims to provide cryptographic primitives secure against both classical and quantum adversaries [14,105]. Unlike quantum cryptography, PQC algorithms are classical and designed to run efficiently on existing processors [62,24]. Their security is based on mathematical problems for which no quantum algorithm is known to provide exponential speedups, in contrast to the integer factorization and discrete logarithm problems broken by Shor’s algorithm. The main families of PQC include lattice-based schemes grounded in problems such as SVP, LWE and Module-LWE [9,10,3,67,80]; code-based ([72,71]) cryptography originating with McEliece’s 1978 system [61]; hash-based signatures built from minimal and well-understood assumptions [8,14]; as well as multivariate and isogeny-based constructions [42,84]. These families offer complementary trade-offs in security reductions, key sizes and implementation characteristics, and collectively form the foundation of quantum-resistant cryptography.

To orchestrate a global and transparent transition to PQC, NIST launched its public standardization process in 2016 through an open call for proposals [64,63,92]. From 69 complete submissions entering Round 1 [2], candidates were evaluated under criteria encompassing security (including resistance to classical, quantum and side-channel attacks) [66,1], cost and performance across platforms [65], and practical considerations such as design simplicity, IP status and implementability. After multiple competitive rounds, NIST selected a diverse portfolio for standardization: ML-KEM/CRYSTALS-Kyber for key establishment [68,69,9]; ML-DSA/CRYSTALS-Dilithium as the primary signature scheme [67,69,10]; FN-DSA/FALCON for compact signatures based on NTRU lattices [3,72,29,46]; and SLH-DSA/SPHINCS+ as a conservative, stateless hash-based alternative [70,69,8]. In March 2025, NIST further expanded the portfolio by selecting HQC as a code-based KEM [72,71,75,30], adding a non-lattice standard whose security relies on the hardness of quasi-cyclic syndrome decoding and whose design offers well-analyzed decryption-failure behavior and long-studied code-based security. These five algorithms—Kyber, Dilithium, FALCON, SPHINCS+, and HQC—constitute the standardized suite that will secure next-generation communication systems in the presence of quantum-capable adversaries.

## 2.3 Fundamentals of Physical Side-Channel Attacks (SCA)

While the theoretical security of a cryptographic primitive rests on its mathematical hardness, its practical security depends on the behavior of its physical implementation. Executing an algorithm on real hardware inevitably produces side effects—power consumption, electromagnetic (EM) radiation, and timing variations—that correlate with the secret-dependent internal state [106,53]. Side-channel attacks (SCA) exploit these leakages to recover long-term keys, often completely bypassing the underlying cryptographic assumptions.

**Non-profiled** attacks operate directly on the target device and typically require many traces to amplify weak leakage. Simple Power Analysis (SPA) inspects individual traces to reveal coarse-grained patterns such as square-and-multiply sequences in classical RSA implementations [52,53]. Differential and Correlation Power Analysis (DPA/CPA) use statistical techniques: DPA partitions traces according to a key hypothesis and observes a differential spike when the hypothesis is correct [53], whereas CPA correlates hypothetical power models (e.g., Hamming weight or Hamming distance) with measured leakage to identify the correct key [17,54].

**Profiled** attacks assume a worst-case adversary with access to an identical device. The attack proceeds in two phases [20]: during profiling, the attacker characterizes the device’s leakage by collecting traces under known keys and constructing a statistical model; during the attack phase, only a few traces from the victim are required to identify the secret key. Template Attacks, which model leakage using multivariate Gaussian distributions, exemplify this paradigm and can succeed with a single trace when profiling is accurate [20,109].

**Profiled AI-based** attacks extend classical template ones, modern profiled attacks increasingly rely on machine learning (ML) and deep learning (DL) techniques to learn complex, non-linear leakage distributions. Early works showed that classical ML models such as Random Forests and SVMs can outperform Gaussian templates when leakage deviates from simple statistical assumptions [57]. More recently, deep neural networks—including MLPs and especially CNNs—have demonstrated remarkable robustness to noise, desynchronization and higher-order effects [12,60]. These AI-based profiled attacks follow the same two-phase structure as template attacks but replace parametric Gaussian models with learned discriminative models, enabling key recovery with significantly fewer traces and widening the applicability of profiled SCA to protected implementations.

The AI models applied to SCA form a broad taxonomy (as detailed in Table 1). This includes classical baselines like RF [55,37,12]; the dominant deep/neural models, such as MLPs for automatic representation learning [12,57], state-of-the-art CNNs [18,60,49], and RNNs for temporal dependencies [39,44]; and reinforcement/decision models (e.g., DQNs), which are typically used to optimize attack parameters rather than for classification itself [39].

## 2.4 Side-Channel Analysis Countermeasures

Countermeasures against SCA operate at either the software or hardware level and aim to suppress exploitable leakage by (a) removing secret-dependent behavior or (b) randomizing it so that leakage becomes statistically unusable. However, recent ML-based profiled attacks have demonstrated the ability to recover higher-order information, undo shuffling, and exploit microarchitectural artefacts that classical protections fail to eliminate.

**Software-Level Countermeasures** are widely adopted in PQC libraries due to their portability and low deployment cost. Masking (or sharing) removes

**Table 1.** Modern AI-based models applied in SCAs against PQC algorithms, grouped by family.

|                             |   |
|-----------------------------|---|
| Classical                   | <b>Random Forest</b><br>Ensemble of decision trees; robust, handles nonlinearities and mixed features.  |
| Reinforcement /<br>Decision | <b>Deep Q-Network (DQN)</b><br>Value-based RL: approximates $Q(s, a)$ with a deep net and experience replay.  |
| Deep / Neural               | <b>Convolutional Neural Network (CNN)</b><br>Learns local patterns via convolutions; strong on spatial/structured signals.<br><br><b>Recurrent Neural Network (RNN)</b><br>Captures temporal dependencies with hidden state across timesteps.<br><br><b>Multi-Layer Perceptron (MLP)</b><br>Feed-forward dense layers; general nonlinear baseline.<br><br><b>Graph Neural Network (GNN)</b><br>Message-passing on graphs; aggregates neighborhood information.<br><br><b>Large Language Model (LLM)</b><br>Transformer-based sequence model; self-attention for long-range context.<br><br><b>Neural Network (NN)</b><br>Generic neural architecture (unspecified); usually feed-forward baseline.<br><br><b>Adaptive Slimmed Pyramid Network (ASP)</b><br>Lightweight <i>CNN</i> variant; pyramid design with slimmed channels for multi-scale feature extraction. |

first-order statistical dependencies by splitting a sensitive variable into random shares [73,83], while shuffling randomizes the execution order of independent operations to induce temporal desynchronization [73,99]. Constant-time implementations suppress timing and cache-based leakages by eliminating secret-dependent branches or memory accesses [52,13,5], and blinding randomizes intermediate values to decorrelate successive executions [11,47].

**Hardware Countermeasures** aim to decorrelate or suppress physical leakage at the circuit, FPGA, or microarchitectural level and often provide stronger protection than their software counterparts. Modern gate-level techniques focus on balanced or leakage-equalizing logic styles and bit-level netlist augmentation to reduce data-dependent switching, with recent evaluations outlining their security-efficiency trade-offs [94,6]. Another line of work introduces on-chip hardware noise, such as low-cost correlated-power noise generators (CPNGs), which inject controlled noise into the power distribution network to reduce SNR and hinder CPA-style attacks [95]. Desynchronization-based approaches remain relevant, where randomized clocking and jitter disrupt trace alignment and mitigate both classical and DL-based SCAs on FPGA and ASIC platforms [85]. For PQC, hardware-friendly shuffling architectures—such as optimized Fisher–Yates variants for Kyber—provide practical execution-order randomization with minimal area and performance cost [107]. Finally, adaptive mitigation techniques leverage Dynamic Partial Reconfiguration (DPR) on FPGAs, enabling runtime reconfiguration combined with DL-based monitoring to disrupt the attacker’s profiling assumptions and provide proactive protection [16].

## 2.5 Common SCA metrics

The Guess Entropy, Success Rate  
 TODO TODO

## 3 Review Methodology

We followed the Kitchenham and Charters SLR protocol [50,51]. We queried seven databases (IEEE Xplore, ACM DL, Scopus, SpringerLink, Engineering Village, ScienceDirect, IACR ePrint) up to August 2025 using a PICOC-derived search string targeting AI-based SCAs on NIST PQC algorithms. Full methodology details, including search strings, inclusion/exclusion criteria, quality checklist, and data extraction form, are provided in Appendix A.

## 4 Study Selection Overview

We identified 1,495 records across seven databases, reduced to 1,338 unique entries after de-duplication, and selected 27 primary studies following our inclusion and exclusion criteria. A high-level summary of quality assessment outcomes and full search statistics are provided in Appendix B. The synthesized findings from these studies are presented in Section 6.

## 5 Related Works

Other surveys and overview papers have been published in recent years that discuss side-channel and fault-injection threats to post-quantum cryptography (PQC). These related works provide useful background by classifying attack methodologies, highlighting vulnerable components of lattice-based schemes, and outlining known countermeasures. In particular, they help situate our systematic review within the broader research landscape by showing how the community has so far organized knowledge about PQC vulnerabilities, before the rapid expansion of AI-driven side-channel analysis in the last two years.

Hernández-Álvarez et al. [35] (2023) conducted a survey-style review of recent AI-based side-channel attacks targeting the NIST-selected PQC standards CRYSTALS-Kyber and CRYSTALS-Dilithium. Their work categorized attacks by the algorithmic function exploited (e.g., encoding, NTT, re-encryption) and analyzed the types of machine learning models employed, with a strong emphasis on multilayer perceptrons (MLPs) and clustering techniques such as k-means. In addition to cataloging attack scenarios and success rates, they discussed hyperparameters, regularization strategies, and limitations of current approaches, arguing that research should extend beyond MLPs toward CNNs and RNNs for better efficiency and generalization. As such, this publication provides a broad overview of AI-driven SCA methodologies but lacks the systematic methodology of an SLR, complementing our study by situating our results within the broader landscape of PQC-focused AI-based SCA research.

Li [56] (2023) provided an overview of attacks against CRYSTALS-Kyber, covering both classical cryptanalytic strategies and physical side-channel threats. The survey structured its discussion around four categories: common attacks (including module-LWE parameter considerations and decryption failures), side-channel attacks (timing, SASCA, message encoding, deep learning, and LDPC-based frameworks), side-channel assisted chosen-ciphertext attacks (e.g., EM-based plaintext-checking and CPA), and fault injection techniques (Roulette and error-tolerant recovery). Although written as an overview rather than a systematic review, the work offers a broad categorization of known vulnerabilities and highlights the practicality of advanced side-channel and fault-based approaches even against masked or optimized implementations. In contrast to our SLR, which systematically synthesizes AI-driven SCA results across PQC algorithms, Li’s review provides a Kyber-focused narrative that helps contextualize attack feasibility and evolving defense requirements.

Ravi et al. [80] (2024) presented a comprehensive survey of side-channel and fault-injection attacks on lattice-based PQC schemes, focusing primarily on Kyber and Dilithium. Unlike prior overviews, this work systematically categorized known attacks by target procedure (key generation, encapsulation, decapsulation, signing) and attack vector (power, EM, glitches), while also proposing a unified characterization framework based on attacker capabilities, profiling requirements, number of traces, and SNR assumptions. Beyond surveying, they contributed new countermeasures, including customized protections that combine shuffling, masking, and novel defenses against chosen-ciphertext-based SCA/-FIA, benchmarked on embedded platforms such as ARM Cortex-M4 (pqm4) and Cortex-A53 (liboqs). However, their review does not emphasize modern AI-based techniques and is limited to Kyber and Dilithium, whereas our SLR systematically synthesizes AI-driven SCA results across a broader set of PQC algorithms. This distinction highlights how our work complements theirs by extending the perspective from general SCA/FIA to the specific and growing role of machine learning in PQC side-channel analysis.

While these surveys provide valuable insights, they also exhibit limitations when compared to a systematic literature review. Some, such as Hernández-Álvarez et al. [35], adopt a survey-style approach without a reproducible SLR methodology; others, like Li [56], restrict their focus to CRYSTALS-Kyber; and Ravi et al. [80] broaden the scope to Kyber and Dilithium but do not emphasize modern AI-based attack techniques. Moreover, most of these reviews were published in 2023 or early 2024, which makes them rapidly outdated: as shown in Figure 1, the number of AI-based SCA publications on PQC has nearly doubled each year from 2023 to 2025. This underscores both the fast pace of research in this field and the need for our systematic review, which synthesizes the latest AI-driven SCA advances across multiple PQC algorithms and protected implementations.



## 6 Synthesis of Key Findings

### 6.1 Attack Surface and Leakage Vectors in Kyber

The attack surface of Kyber, particularly concerning leakage vectors exploited by Side-Channel Attacks (SCAs), is broad, encompassing physical measurement channels, specific cryptographic operations, and residual vulnerabilities within countermeasures. See Tables 2 and 3.

**Table 2.** Channels, Leakage Models and Countermeasures Weakness in **Kyber**

| Physical Channels and Measured Leakages  |
|--|
| <p><b>Power Consumption (PA):</b> Dynamic power consumption is a primary source of leakage, widely exploited, particularly in implementations on ARM Cortex-M4 microcontrollers [22,31,44,81,82,100,111,38].</p> <p><b>Electromagnetic (EM) Emanation:</b> EM radiation is a prominent leakage vector [22,44]. EM attacks based on Deep Learning (DL) remain viable even when the microcontroller is protected by an <b>opaque anti-tampering cover</b>, despite signal attenuation [22].</p> <p><b>Timing:</b> Execution time leakage can be exploited, notably in relation to the integrity check in the Fujisaki-Okamoto (FO) transformation [44,98].</p>   |
| Leakage Models   |
| <p><b>Hamming Weight (HW):</b> A common model used, for instance, in deep learning classifiers to recover the secret key coefficients during pointwise multiplication [36,43,81,82,103,38].</p> <p><b>Hamming Distance (HD):</b> This model quantifies the change between successive operations or shares. In hardware implementations, the HD between adjacent bits of polynomial shares (<math>m1/m2</math>) exhibits <b>**strong leakage**</b> due to the high fanout of internal registers (e.g., <math>m1\_reg/m2\_reg</math>) when bit values change [44].</p> <p><b>Joint Distribution:</b> Used in advanced blind SCAs, where the classifier exploits the joint distribution of the Hamming Weights of relevant intermediate values [81,82]. Second-order attacks specifically analyze joint information leakage in masked hash functions [103].</p>   |
| Vulnerability of Countermeasures   |
| <p><b>Masking Defeat:</b> Masked implementations, including those that are first-order protected, have been successfully broken via DL-based SCAs [31,44,100,38]. Furthermore, advanced DL techniques, such as the recursive learning/copy-paste method, have demonstrated message recovery success against Kyber implementations masked up to <b>fifth-order</b> (<math>\omega \leq 5</math>) [25,22].</p> <p><b>Hiding Defeat:</b> The shuffling countermeasure can be bypassed. Voltage fault injection has been demonstrated as a method to <b>disable shuffling</b> in a masked implementation, enabling a single-trace message recovery attack by eliminating the temporal randomness introduced by shuffling [43]. Similarly, <b>**standalone temporal hiding countermeasures**</b> like Random Delays and Clock Jitter show limited efficacy against DL-based attacks [38]. However, combining shuffling with masking effectively resists recently proposed second-order attacks targeting the masked hash function [103].</p> |

### 6.2 Attack Surface and Leakage Vectors in Dilithium

The attack surface of Dilithium is extensive, focusing primarily on operations involving the secret key components and intermediate random values. Attacks target both the signature generation and key generation procedures. See Tables 4 and 5.

**Table 3.** Key Algorithmic Attack Points in **Kyber**

| Plaintext Checking (PC) Oracles via FO Transformation  |
|--|
| <p><b>PRF/Hash Function Execution:</b> The leakage arises during the execution of the Pseudo-Random Function (PRF) or Hash function (e.g., <b>SHAKE</b> or <b>SHA3/Keccak</b>) when the decrypted message (<math>m'</math>) is re-encrypted (<math>ct'</math>) to check equality with the received ciphertext (<math>ct</math>) [93,98,103,38].</p> <p><b>Masked Hash Leakage:</b> In first-order masked Kyber implementations, second-order attacks specifically exploit the joint information leakage in the masked hash function (SHA3-512 based Keccak) [103]. This leakage can be used to distinguish decrypted messages differing by <b>1 bit</b> or, through specialized ciphertext construction, by <b>32 bits</b> [103,93].</p> <p><b>Multiple-Valued Oracles:</b> The leakage can be utilized to create a Multiple-Valued PC (MV-PC) oracle, generalizing the binary oracle to extract more secret key information per trace, significantly reducing the total required traces [93].</p> |
| Message Decoding and Encoding  |
| <p><b>Masked Decoding (poly_to_msg):</b> This function, which converts the shared key from the polynomial domain back to the message domain, is a demonstrated vulnerability in masked implementations [31,44,38]. The attack exploits leakage found in the <b>**masked message decoding function**</b> during the decapsulation decryption step [44,38].</p> <p><b>Message Encoding (masked_poly_frommsg):</b> Attacks also target the masked message encoding procedure carried out during the re-encryption step [43,100,38]. This leakage, often referred to as "direct-copy leakage," allows message recovery [43].</p>   |
| Core Arithmetic Operations   |
| <p><b>Pointwise Multiplication:</b> The computation of <math>w^* = \text{NTT}(u') \circ \text{NTT}(\hat{s})</math> (the multiplication of the ciphertext coefficients with the secret key coefficients) is a critical leakage target [36,81]. Attacks target the individual coefficient multiplications (e.g., five <i>fqmul</i> operations per <i>basemul</i>) where register loading and storing result in leakage of input and output Hamming Weights [36,81].</p> <p><b>Modular Reduction:</b> The modular reduction function (e.g., Barrett reduction) is vulnerable to chosen-ciphertext clustering attacks [31]. It is possible to profile the leakage of reductions performed on controllable data (R1) and transfer this model to successfully attack reductions involving secret key material (R2/R3) without requiring a separate reference device [108].</p>   |

### 6.3 Attack Surface and Leakage Vectors in FALCON

FALCON, an NTRU-based digital signature, and its enhanced variants (such as Mitaka, Antrag, and SOLMAE) employ **\*\*Cumulative Distribution Table (CDT) sampling\*\*** to quickly generate arrays of random values from a discrete Gaussian distribution during the signature generation phase [23]. The attack surface centers on the leakage of these extracted random values, which, when used with secret key information, contributes to the **\*\*indirect leakage of secret information\*\*** [23]. **\*\*Single Trace Analysis (STA)\*\*** is successfully employed to recover the sampled values by exploiting comparison operations in various environments [23]. See Tables 6 and 7.

### 6.4 Attack Surfaces in Hash-based SPHINCS+

Although SPHINCS+ is stateless and hash-based, it is not inherently immune to side-channel leakage. However, at the time of this review, we found no published work that systematically studies SPHINCS+ under modern AI-assisted side-channel analysis. Consequently, no concrete AI-driven attack surfaces or leakage

**Table 4.** Channels, Leakage Models, and Countermeasures Weakness in **Dilithium**

| Physical Channels and Measured Leakages  |
|--|
| <p><b>Power Consumption (PA):</b> Power consumption is the primary channel used in profiling attacks, with experiments conducted successfully on ARM Cortex-M4 microcontrollers (e.g., ChipWhisperer STM32F3 or STM32F4 boards) [34,48,58,78,102,79]. The single-trace attack aims to recover the entire key from one power trace, regardless of optimization level (-O0, -O3, -Os) [34,48].</p> <p><b>Timing:</b> Execution time is noted as a side channel, although constant-time implementation aims to mitigate this risk [34,58].</p>  |
| Leakage Models and Profiles  |
| <p><b>Hamming Weight (HW):</b> Used as the assumed power consumption model in software implementations [34,102]. The Hamming Weight difference of intermediate values (e.g., in <code>mont_reduce</code>) determines the efficiency of distinguishing secret coefficients [34]. Also used in correlation analysis for the temporary values <code>r</code> and <code>y</code> in <code>polyz_unpack</code> [78].</p> <p><b>LSB Leakage:</b> The Least Significant Bit (LSB) of the <code>t0</code> coefficients is targeted specifically using t-test analysis, showing distinct leakage patterns [101].</p> <p><b>Zero/Non-Zero Distinction:</b> Crucial for the bit-unpacking attack on the <code>y</code> vector, where classifiers predict whether a coefficient is zero or non-zero, exploiting leakage visible via t-test [58].</p> <p><b>Bit Leakage (Linear Regression Model):</b> Used in profiled attacks on the <code>y</code> vector, where linear regression is employed to account for varying leakage weights of individual bits [78].</p>   |
| Vulnerability of Countermeasures   |
| <p><b>Masking Defeat:</b> Masked Dilithium implementations have been shown vulnerable to single-trace attacks targeting the multiplication operation, requiring a minimal number of traces for key exposure [48]. Applying masking to the signing procedure eliminates the advantage of NTT attacks, as input coefficients become uniformly distributed [34]. However, masking introduces a high performance overhead (estimated at around five times slower) [58].</p> <p><b>Determinism/Randomization:</b> The attack targeting the <code>y</code> generation via bit-unpacking applies to both the deterministic and non-deterministic (probabilistic) signing variants, as the target step (bit-fiddling) is common to both [58]. Single-trace attacks are effective even against randomized implementations because they extract information (<code>y</code>) per signature execution [58].</p> <p><b>Key Compression (t0):</b> The public key compression, which omits the low-order bits <code>t0</code> of vector <code>t</code>, is intended for performance optimization, not security [102]. However, knowledge of <code>t0</code> significantly enhances the efficiency of post-processing techniques used to recover <code>s1</code> and <code>s2</code> [102].</p> |

vectors specific to SPHINCS+ can be reported, highlighting a gap that warrants further investigation.

## 6.5 Attack Surface and Leakage Vectors in HQC

HQC, a code-based KEM, relies on the Fujisaki-Okamoto (FO) transformation to achieve CCA security. This construction exposes a critical attack surface during the re-encryption and validity-checking phase of decapsulation, where the implementation invokes a Pseudo-Random Function (PRF) or Pseudo-Random Number Generator (PRG). Ueno *et al.* demonstrate that these PRF/PRG computations leak sufficient information to build a plaintext-checking oracle via power/EM side-channel analysis, marking the first reported physical analysis of the FO transformation in code-based KEMs. Their framework applies generically to FO-based schemes, and HQC is included in the key-recovery cost evaluation.

However, it is important to note that the authors do *not* present HQC-specific power or EM leakage measurements; rather, they analytically include HQC in their evaluation of re-encryption-based side-channel vulnerabilities. See Tables 8 and 9.

### 6.6 Single-trace is now a reality

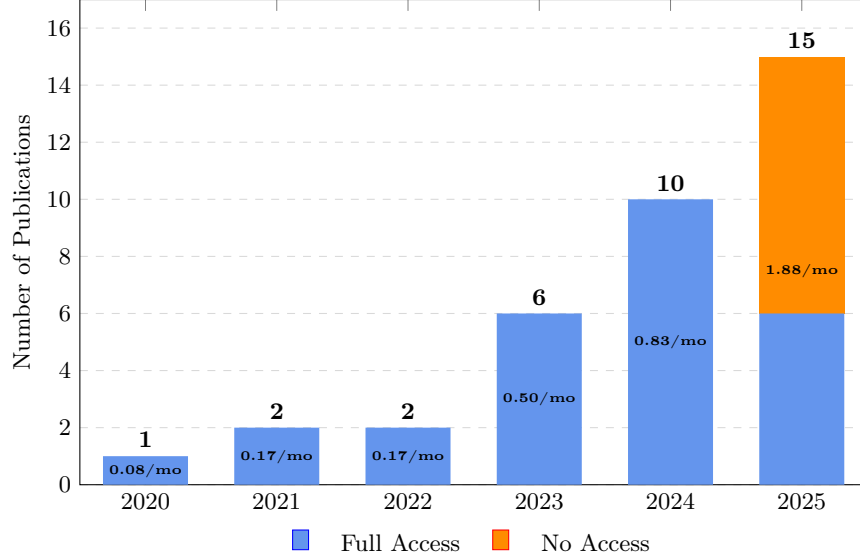
The effectiveness of advanced side-channel analysis (SCA) techniques, particularly those leveraging Deep Learning (DL), has transformed the feasibility of single-trace attacks (STA) from a theoretical threat into a practical reality across multiple Post-Quantum Cryptography (PQC) schemes. Choi et. al. [23] analytically analysis single trace attacks against FALCON. Jendral et. al. [43] shows against Kyber. And Kim et. al. [48,78,102,34] demonstrates against Dilithium. These STA methods demonstrate the capability to recover sensitive information, including full secret keys or shared keys, from implementations protected by common countermeasures, often requiring minimal computational resources. See Table 10 for more details.

### 6.7 Bibliometric Analysis of Extracted Data

The following analysis is a synthesis of the information collected from the 27 primary studies using the Data Extraction Form (defined in Table 19). This step builds upon the study selection process (Table 20) and the quality assessment outcomes (Table 21), providing a comprehensive picture of how the literature on AI-based side-channel attacks (SCAs) against PQC has evolved.

**Temporal Distribution** The analysis of the publication years reveals a clear and accelerating trend as shown in Figure 1. A small number of foundational papers were published between 2020 and 2022. However, a marked increase in publication volume is observed from 2023 onwards, with a significant peak in 2025. This surge not only directly correlates with the conclusion of the third round of the NIST PQC standardization process, but also aligns with the evidence that AI-driven SCAs are highly effective, achieving results that surpass traditional techniques. Such approaches have been demonstrated to break hardware implementations [58], to overcome protected implementations with countermeasures such as masking and constant-time design [25,102], and, in some cases, to succeed with as little as a single trace [102]. These results indicate that AI-based methods can outperform classical template and differential/correlation power analysis (DPA/CPA) techniques in both efficiency and robustness [82], underscoring their significance in the evaluation of post-quantum cryptographic implementations.

**Adopted AI Models in SCA Against PQC** Figure 2 illustrates the distribution of the most prevailing AI models employed in SCAs targeting PQC algorithms, based on the dataset extracted from the selected studies (Section 4). The analysis highlights that multilayer perceptrons (MLPs) and convolutional

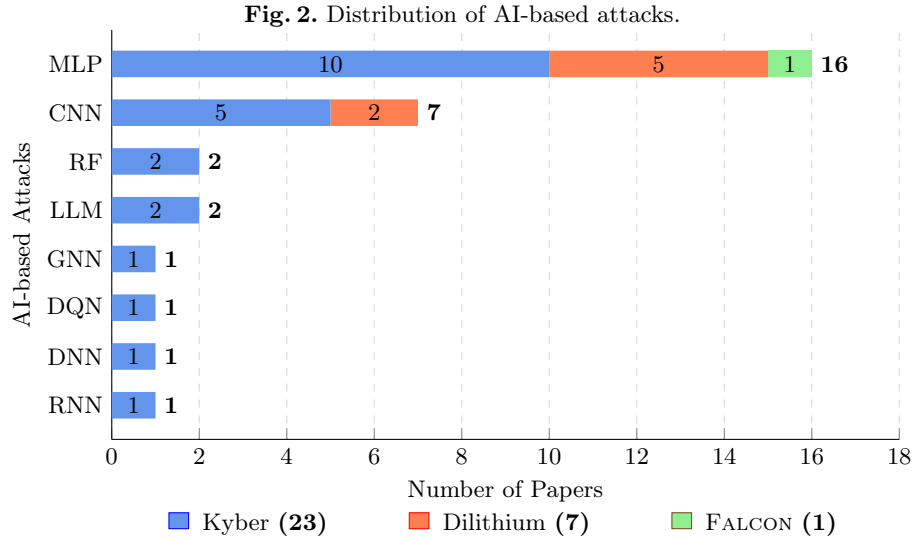
**Fig. 1.** Number of Publications per Year with Monthly Average (2021 to 08/2025)

neural networks (CNNs) dominate the landscape, reflecting their effectiveness in capturing and exploiting complex leakage patterns. Other approaches, such as recurrent neural networks (RNNs), Random Forests, and more recent explorations with graph neural networks (GNNs), deep reinforcement learning (DQN), adaptive sparse models (ASPN), and even large language models (LLMs), appear less frequently but signal an emerging diversification of techniques. This distribution indicates that while the community is converging on MLP- and CNN-based profiling as the de facto standard, there is also growing interest in testing novel architectures that may further advance AI-based side-channel analysis.

**Targeted PQC Algorithms** As shown in Figure 2, the landscape of AI-driven SCA research is heavily skewed toward *Kyber* and *Dilithium*, the primary NIST standards. Conversely, the alternative algorithms have received negligible attention. *FALCON* features in only one publication (restricted to leakage assessment), *HQC* in one analytical paper, and *SPHINCS+* has not been addressed in this context. This concentration of effort leaves the standardized fallback options under-evaluated. To guarantee the resilience of the full PQC ecosystem, future research must expand beyond the primary candidates to include these secondary schemes.

## 7 Conclusion

The only study applying **fault-injection** attack is [43]. It uses voltage-glitch an instruction (bne) in the Fisher-Yates shuffle, against Kyber. Definitely there is



open research opportunities to combine AI-based attacks with FI, not only using several other FI techniques but also targeting other points.

There are just two papers defeating shuffling countermeasure. None defeats shuffling with higher order masking applied together. None defeats masked Threshold Implementation. So, there is still room for research to defeat stronger countermeasures and combination of them.

This SLR set out to (i) map the state of peer-reviewed, AI-driven side-channel attacks (SCAs) on the NIST PQC standards and the prominent HQC candidate, (ii) classify attacks by linking learning techniques to specific implementation vulnerabilities, and (iii) identify trends, gaps, and open challenges. Our synthesis shows that *AI has reshaped the side-channel threat model*. First, single-trace (or single-signature) feasibility has moved from exception to expectation on embedded targets: Dilithium and FALCON admit high-accuracy single-trace recovery under realistic lab conditions, and Kyber—even with masking and shuffling—has been compromised in one-trace, fault-assisted settings. Second, protections once considered table-stakes (first-order masking, shuffling, constant-time coding, even anti-tamper covers) no longer guarantee safety against modern profiled analysis: MLPs/CNNs learn residual higher-order structure, fault injection erases desynchronization benefits, and tailored CNN variants sustain accuracy through physical shielding. Third, *hybrid pipelines*—learning-based leakage extraction plus algebraic post-processing (e.g., leveraging compressed public-key bits in Dilithium)—consistently outperform either approach alone, tightening attack budgets from thousands to handfuls of traces.

Answering **RQ1**, we catalogued 27 primary studies (cutoff: 16 Aug 2025) that span Kyber, Dilithium, FALCON, and HQC, with no practical AI-driven SCA yet on SLH-DSA (SPHINCS+). **RQ2** yielded a taxonomy aligning model families

to targets and countermeasures: classical ensembles (e.g., Random Forest) enable blind or weakly labeled attacks; deep models (MLP/CNN and lightweight variants like ASPN) break masked/shuffled code and tolerate misalignment; reinforcement learning automates segmentation and other “last-mile” steps; and LLM-assisted workflows lower the expertise barrier for SPA. For **RQ3**, three trends emerged: (1) the ascendancy of single-trace and few-trace practicality; (2) systematic neutralization of standard countermeasures by data-driven models; and (3) growing realism in assumptions (no reference device, blind inputs, physical covers), all of which compress the gap between lab and field.

**Gaps & risks.** The conspicuous absence of convincing, practical AI-based SCAs on SPHINCS+ suggests a research and evaluation deficit—not necessarily inherent resilience. Leakage around public-key compression (e.g., Dilithium’s  $t_0$ ) has outsized impact on post-processing and should be treated as a first-class attack surface. Finally, reproducibility remains uneven (limited code/datasets), complicating cross-paper comparisons and engineering takeaways. The same applies to HQC with just one paper doing just analytical analysis, and FALCON with just one paper doing leakage assessment (not an key/message recovery attack).

**Implications for defenders.** Defense must assume an *AI-equipped adversary*. We recommend: (i) layering countermeasures (higher-order masking *with* shuffling and hardened randomness paths), (ii) auditing compression/-packing/unpacking code paths as aggressively as core arithmetic, (iii) adopting design-time leakage discovery and patching (e.g., RTL-level analyses) before FPGA/ASIC sign-off, (iv) validating physical mitigations (e.g., covers) against modern CNNs/RL pipelines, and (v) standardizing evaluation: shared datasets, alignment-robust metrics (GE, SR), and mandatory implementation disclosures to improve reproducibility.

**Future work.** Priority directions include: scalable, composable leakage proofs that reflect ML adversaries; standardized, open corpora for PQC SCA benchmarking across devices and countermeasures; principled integration of fault + learning under realistic noise/fault budgets; and dedicated studies on SLH-DSA implementations. As our bibliometrics indicate, output in this area is doubling year-over-year, underscoring that the threat landscape—and the corresponding best practices—are rapidly evolving. This review provides a current map; keeping systems secure will require treating AI-aware SCA evaluation as a continuous process throughout the PQC deployment lifecycle.

**Table 5.** Key Algorithmic Attack Points in **Dilithium**

| Key Generation and Unpacking Procedures   |
|---|
| <p><b>Secret Key Unpacking (skDecode):</b> This procedure, executed at the start of the signing algorithm, is attacked to recover secret key coefficients <math>s_1</math> and <math>s_2</math> in the range <math>[-\eta, \eta]</math> [102]. The underlying function <code>small_polyeta_unpack</code> processes coefficients twice, resulting in unique Hamming Weight pairs that allow for distinguishability [102].</p> <p><b>t0 Unpacking (polyt0_unpack):</b> Targeted for recovering the LSB of <math>t_0</math> coefficients to reduce the number of signatures required to cryptographically reconstruct the full <math>t_0</math> vector [101].</p> <p><b>Key Generation Subroutines:</b> When NTT is protected, attacks must target sampling, addition, rounding, and packing operations to find the secret vector <math>s_2</math> [34]. The addition operation showed the best recovery results (SR &gt; 98%) among these subroutines in experiments [34].</p>  |
| Core Arithmetic and Polynomial Operations   |
| <p><b>Number Theoretic Transform (NTT):</b> A critical target in both the key generation (to recover <math>s_1</math>) and the signing procedure (to recover <math>s_1</math> and <math>s_2</math>) [34,48]. The attack specifically exploits leakage in the Montgomery reduction (<code>Mont_r</code> or <code>montgomery_reduce</code>) operation within the NTT, where the intermediate value <math>t</math> exhibits significant power consumption differences based on the input coefficient <math>p[j+\text{len}]</math> [34,48].</p> <p><b>Inverse NTT (INTT)/Montgomery Reduction:</b> The INTT operation, particularly the Montgomery reduction involved in computing <math>cs_1</math>, <math>cs_2</math>, and <math>ct_0</math>, is a significant leakage source [78,79]. The operations are identical for <math>cs_1</math>, <math>cs_2</math> and <math>ct_0</math>, enabling profiling on publicly available <math>ct_0</math> leakage to recover the private terms <math>cs_1</math> and <math>cs_2</math> [79]. Leakage arises from the shifting and storage operations in the final stages of the reduction [78].</p> <p><b>Sparse Multiplication:</b> Targeted in single-trace attacks against masked Dilithium implementations, where leakage occurs in the load, save, and multiplication operations involving the Boolean masked secret key shares [48].</p> <p><b>Polynomial Multiplication (<math>c \cdot s_1</math>):</b> The process of polynomial multiplication is susceptible to Correlation Power Analysis (CPA), regardless of whether schoolbook multiplication, NTT, or sparse multiplication is used [58]. The narrow range of the resulting product <math>cs_1</math> favors side-channel attacks [78].</p> |
| Signature Calculation and Randomness Leakage  |
| <p><b>Randomness Generation (y):</b> The bit-unpacking function used in <code>ExpandMask</code> to generate the random vector <math>y</math> leaks information about whether coefficients are zero or non-zero, making it a viable target for profiling attacks [58].</p> <p><b>Polynomial Addition (<math>z = y + cs_1</math>):</b> This critical step is exploited in attacks that aim to recover <math>cs_1</math> or <math>y</math> [58,78]. The attack exploits the inherent relationship between the known signature <math>z</math> and the addition of the large random number <math>y</math> (range <math>q</math>) and the smaller value <math>cs_1</math> (range <math>\beta</math>) to infer partial information about <math>y</math> (high bits) [78]. The results from individual attacks on <math>y</math> and <math>cs_1</math> can be integrated to significantly enhance the overall success rate of <math>cs_1</math> recovery [78].</p>  |



**Table 6.** Channels, Leakage Models, and Countermeasures Weakness in **FALCON**

| Physical Channels and Measured Leakages   |
|---|
| <p><b>Power Consumption (PA):</b> Power consumption traces are measured as the primary channel using the ChipWhisperer Lite (CW-Lite) platform for security evaluation [23].</p> <ul style="list-style-type: none"> <li>– <b>8-bit AVR MCU (XMEGA128D4):</b> Used to study the <b>visible vulnerability</b> (non-constant-time leakage) [23].</li> <li>– <b>32-bit Arm Cortex-M4 MCU (STM32F303):</b> Used to investigate and demonstrate the <b>invisible vulnerability</b> (Hamming Weight leakage) in a constant-time execution environment [23].</li> </ul> <p><b>Timing:</b> The variation in the number of clock cycles creates a <b>visible vulnerability</b> on 8-bit AVR MCUs [23].</p>  |
| Leakage Models and Vulnerabilities  |
| <p><b>Visible Leakage (Non-Constant Time):</b> This vulnerability occurs on 8-bit AVR MCUs because the compiler splits large numbers (e.g., 64-bit data used in Mitaka) into smaller units for comparison [23]. This operation is subject to <b>premature termination (early stop)</b>, causing variable execution times and rendering the implementation non-constant-time [23].</p> <p><b>Invisible Leakage (Hamming Weight Difference):</b> This vulnerability is found in the <b>constant-time</b> environment of the 32-bit Arm Cortex-M4 [23]. The leakage is caused by <b>Hamming Weight (HW) differences</b> that occur when the results of comparison operations (0 or 1) are accumulated using an add instruction in internal registers [23].</p> <p><b>Deep Learning Profiling:</b> A profiling analysis utilizing a Deep Learning (MLP) model is successfully employed to predict the CDT sampling output value from a <b>single power trace</b> on the Arm Cortex-M4, achieving a recovery accuracy of 99.97% and an F1 score of 1.0 [23].</p> |
| Vulnerability of Countermeasures  |
| <p><b>Defeat of Constant Time:</b> A comparison-operation-based CDT sampling algorithm was proposed as a countermeasure to eliminate branching instructions (using 'adc') and satisfy constant-time execution on AVR (defeating visible leakage) [23]. However, this approach does not mitigate the <b>invisible HW leakage</b> present on the Arm Cortex-M4 [23].</p> <p><b>Traditional Countermeasures:</b> Masking and shuffling are considered computationally slow and memory heavy, indicating that further research is required to design secure algorithms against DL-based SCA in the context of CDT sampling [23].</p>  |
| <p>Note: The authors did a leakage assessment only, but did not do a key/message recovery.</p>  |

**Table 7.** Key Algorithmic Attack Points in **FALCON**

| Gaussian Sampling Procedures  |
|---|
| <p><b>CDT Sampling (Cumulative Distribution Table):</b> This is the fundamental target operation in FALCON and its variants (Mitaka, Antrag, SOLMAE) [23]. It implements discrete Gaussian sampling using repeated <b>**comparison operations**</b> between a random input (rnd) and stored table values to determine the output sampled value (S) [23].</p> <p><b>Comparison Operation Leakage:</b> The specific comparison function ('Compare(Ai, Bi)') within the CDT implementation is the exact point of side-channel leakage [23].</p> <ul style="list-style-type: none"> <li>– <b>AVR (Visible):</b> Leakage is caused by the variable clock count of branch instructions, resulting from the comparison operation's susceptibility to <b>**premature termination**</b> when processing multi-byte data [23].</li> <li>– <b>Arm Cortex-M4 (Invisible):</b> Leakage occurs during the <b>**add instruction**</b> that accumulates the results (0 or 1) of the comparison operations due to resulting <b>**Hamming Weight (HW) differences**</b> within the registers [23].</li> </ul> <p><b>Target of Recovery:</b> The primary objective of the single trace attack is to recover the <b>**sampled value (S)**</b> produced by the CDT sampling, as the leakage of this value can indirectly lead to the exposure of secret information [23].</p> <p>Note: The authors did a leakage assessment only, but did not do a key/message recovery.</p> |

**Table 8.** Channels, Leakage Models, and Countermeasures Weakness in **HQC**

| Physical Channels and Measured Leakages   |
|---|
| <p><b>Power/EM Analysis:</b> The proposed generic attack is a power/Electromagnetic (EM) side-channel analysis methodology that exploits leakage during the re-encryption process [98]. This work constitutes the first reported power/EM analysis of the FO transformation in code-based KEMs such as HQC [98].</p> <p><b>Timing:</b> HQC is susceptible to timing attacks if the FO transformation is implemented in a non-constant-time manner, especially in the ciphertext-equality verification step (<math>c = c'</math>), which must be constant-time [98]. The proposed attack, however, focuses on power/EM leakage from the PRF execution, which allows it to bypass constant-time countermeasures applied to the equality check [98].</p> |
| Leakage Models and Profiles   |
| <p><b>PRF/PRG Leakage:</b> The fundamental leakage mechanism is the side-channel trace produced during the execution of the PRF or PRG in the re-encryption phase of KEM.Decaps [98]. This leakage is utilized to distinguish whether the decrypted plaintext (<math>m'</math>) is equivalent to the known reference plaintext (<math>m</math>) or some other random plaintext (<math>\hat{m}</math>) [98].</p> <p><b>Deep Learning Distinguisher:</b> The attack uses a Deep Learning (DL) based two-classification neural network (NN) to realize the plaintext-checking oracle [98]. This approach does not require specific knowledge about the target implementation, allowing the attack to be mounted in a black-box manner [98].</p>          |
| Vulnerability of Countermeasures  |
| <p><b>PRF/Masking Defeat:</b> The proposed SCA remains effective even when the PRF (e.g., SHAKE) is software-masked, as the NN distinguisher can still extract sufficient information for plaintext discrimination [98].</p> <p><b>Constant-Time Defeat:</b> The attack targets the PRF leakage, meaning it can achieve key recovery independently of the PKE.Dec implementation and remains effective even if the comparison operation (<math>c = c'</math>) is implemented securely using constant-time conditional move operations (cmov) [98].</p> <p>Note: The authors do not perform HQC-specific power/EM measurements; HQC is included analytically in the re-encryption attack framework.</p>  |

**Table 9.** Key Algorithmic Attack Points in **HQC**

| Plaintext Checking (PC) Oracle via FO Transformation   |
|--|
| <p><b>PRF/Hash Function Execution:</b> The leakage is generated during the execution of the PRF/PRG (denoted as <math>G</math> in the FO transformation) when the decrypted message (<math>m'</math>) is re-encrypted (<math>c'</math>) to check equality with the received ciphertext (<math>c</math>) [98].</p> <p><b>HQC PRF Instantiation:</b> HQC specifically employs SHAKE (the <math>\text{SHAKE256}_{512}(m, 0x03)</math> function) in its KEM.Decaps re-encryption phase to obtain the decrypted plaintext [98]. The leakage from this SHAKE execution is used to enact the plaintext-checking oracle [98].</p> <p><b>KR-PCA Methodology:</b> The Key-Recovery Plaintext-Checking Attack (KR-PCA) can be performed on HQC following a strategy similar to lattice-based KEMs (like FrodoKEM), leveraging the fact that the decrypted plaintext (<math>m'</math>) will be <math>0^\ell</math> or a vector with a single '1' bit (<math>0^{i-1}  1  0^{\ell-i-1}</math>) [98].</p> <p><b>Attack Complexity:</b> The attack requires a feasible number of oracle accesses to achieve full-key recovery, albeit generally more complex than lattice-based KEMs [98].</p> <ul style="list-style-type: none"> <li>– hqc128 requires approximately 18,111 oracle accesses [98].</li> <li>– hqc256 requires approximately 58,536 oracle accesses [98].</li> </ul> <p><b>PKE Short Randomness:</b> The underlying PKE scheme used by HQC internally utilizes the XOF function <math>\text{SHAKE256}(r, 0x02)</math> for short randomness generation [98].</p> <p>Note: The authors do not perform HQC-specific power/EM measurements; HQC is included analytically in the re-encryption attack framework.</p> |

**Table 10.** Single-Trace Attacks

| <b>Kyber: Defeating Masking and Shuffling</b>   |
|---|
| <p><b>Combined Attack:</b> A single-trace message recovery attack was successfully demonstrated against a Kyber KEM software implementation that was protected by both masking and shuffling [43].</p> <p><b>Methodology:</b> The attack combines voltage <b>fault injection</b> (to bypass the shuffling) with <b>MLP</b> profiled power analysis based on the Hamming weight leakage model [43].</p> <p><b>Efficiency Gain:</b> This approach recovers the shared key from a single power trace, marking a significant improvement over previous pure SCA methods that required approximately 3K power traces to attack a masked and shuffled Kyber KEM [43]. The success of STA in this context suggests that encapsulation algorithms in Kyber, which are typically unprotected, also require security measures [43].</p>   |
| <b>Dilithium: Single Signature Key Recovery</b>   |
| <p><b>NTT Operation:</b> Profiling attacks based on <b>MLP</b> targeting the Number-Theoretic Transform (NTT) operation during signature generation and key generation can expose the full secret key vectors (s1 and s2) [34,48]. It has been shown experimentally that a single-trace attack can find the full key with a 100% success rate regardless of the optimization level (-O0,-O3,-Os) by targeting the NTT encryption [48].</p> <p><b>Defeating Masking:</b> Even when Dilithium is protected by masking, STA remains possible by targeting sparse multiplication, achieving 100% full key exposure regardless of the optimization level [48].</p> <p><b>Unpacking Leakage:</b> A single-trace attack exploiting leakage during the secret key unpacking procedure of the signing algorithm can recover the full secret vector s1 with a non-negligible probability (9%) when the low-order bits of the public key vector t (t0) are known. Previous attacks generally required over 100 traces [102].</p> <p><b>Single Signature Success:</b> By targeting the polynomial addition <math>z=y+cs1</math> during signing, practical attacks on Dilithium2 can achieve private key recovery (s1) with a 60% success probability within one hour using leakage from a single signature [78]. Techniques like Linear Regression (LR)-based profiling successfully recover the random vector y with a 40% success rate from one trace, and CNN-based methods targeting Montgomery-reduction to recover cs1 with a 74% success rate from a single trace [78]. In hybrid attack scenarios, the private key s1 was successfully recovered using as few as 2 signatures, or optimally, a single signature [78].</p> |
| <b>FALCON: High-Accuracy Leakage in CDT Sampling</b>  |
| <p><b>Visible Vulnerability:</b> In the 8-bit AVR environment (non-constant-time implementation), STA can recover sampled values using a single trace by exploiting a visible vulnerability (premature termination of comparison operations) [23].</p> <p><b>Invisible Vulnerability (Defeating Constant Time):</b> Even when operating on a modern Arm Cortex-M4 chip in a constant-time environment, STA successfully recovers the CDT sampling value [23]. This attack exploits an invisible vulnerability related to Hamming weight differences that occur during the addition of comparison results [23]. The proposed <b>MLP</b> model achieves a recovery success rate of 99.97% using just a single trace [23].</p>   |

**Table 11.** Summary of MLP SCAs against Kyber

| MLP (Multi-Layer Perceptron neural network)   |
|---|
| <p><b>Kyber-512 (M,HW)</b>: Decapsulation; HD(<math>m[i-1], m[i]</math>) labeling; 1000 profiling traces <math>\rightarrow</math> 255k segments; MLP architecture with BN/Dense/ReLU layers; single-trace HD accuracy <math>\approx 0.615</math>; full-message success: 97.7% with 299 traces, 99.7% with 399; cross-device profiling reduces success to 94.8%; training hyperparameters explicitly listed [44].</p> <p><b>Kyber-768 (M,SW)</b>: Decapsulation; <math>m[i]</math> labeling; <math>\omega = 1</math>st-5th-order masking broken via recursive-training (weight transfer across <math>\omega</math>). 30k profiling traces <math>\rightarrow</math> 960k; cyclic rotations amplify determiners (0x0000/0xFFFF). Single-trace bit accuracy 0.999<math>\rightarrow</math>0.979 (<math>\omega = 1 \rightarrow 5</math>); message success 78.9%<math>\rightarrow</math>0.56%. For <math>\omega = 5</math>, majority voting with 5 power traces reaches 87%. Encapsulation attack, labeling HW(<math>m[i]</math>), failed even unmasked [25].</p> <p><b>Kyber-768 (M,SW)</b>: Decapsulation; polytomsg(<math>m</math>)[<math>i</math>] labeling; first-order masked poly tomsg attacked. 50k traces; bitwise segmentation (<math>256 \times 3 \times 225</math>); MLP achieves 90% validation accuracy and 99% <b>key-coefficient recovery</b> in 65h; Attack requires chosen ciphertexts, preprocessing and ECT-based error correction [31]. See Table 14 for failing RNN attack of this paper.</p> <p><b>Kyber-768 (M,S,SW)</b>: Decapsulation; HW(msg byte) labeling; countermeasures evaluated: masking, noise, random delay, clock jitter. classifier with several hidden layers; trimming Masked_Encode boosts accuracy 30%<math>\rightarrow</math>88%; Decode DL accuracy drops to 0.37% under N0.1; Masked_Decode &lt;1% accuracy with noise or AR200 jitter [38].</p> <p><b>Kyber-768 (M,S,SW)</b>: Decapsulation; HD(<math>m[i-1], m[i]</math>) labeling; Single-trace DL-SCA + <b>Fault Injection</b> voltage-glitch bypass of Fisher-Yates shuffle; 10k profiling traces <math>\rightarrow</math> 2.56M segments, one attack trace; MLP (320-dim input, Nadam, <math>\leq 250</math> epochs) predicts message bits; full-message success 0.122<math>\rightarrow</math>0.887 (32-bit enumeration)<math>\rightarrow</math>0.969 (64-bit enumeration). [43].</p> <p><b>Kyber-768 (HW)</b>: Decapsulation; <math>m[i]</math> labeling; Profiling uses 200k traces <math>\rightarrow</math> 6.4M samples; MLP (BN-Dense-BN-ReLU-Dense-Softmax, 21k params) trained with Nadam (100 epochs). Attack uses <math>256 \times 4 \times 5</math> traces via 4-sliced multi-bit error injection, recovering all messages with byte-rank <math>\leq 3</math> (enumeration <math>\leq 4^{32} = 2^{64}</math>). Recovering messages from 7 chosen ciphertexts enables long-term <b>secret-key extraction</b> [45].</p> <p><b>Kyber-768 (SW, Masked)</b>: Decapsulation; first-order masked message encoding. Profiling (BN-Dense-BN-Dense-BN-Dense, ReLU, Softmax, Dropout 0.5) on joined-share traces (input=380). 1000 traces <math>\rightarrow</math> 32k samples; Adam, CE loss; val. acc. reaches 100%. Single-trace message recovery; 2-stage CCA-assisted attack recovers <b>full secret key</b> with 9 traces (<math>\approx 100\%</math> success), or 18 traces with negligible brute-force. [100]</p> <p><b>Kyber512 (SW)</b>: Decapsulation; labels=HW(R1 reduction, profiled)<math>\rightarrow</math>applied to R2 (key-dependent). MLP 160-100-40-12, ReLU/Softmax, trained on <math>10^6</math> R1 sub-traces (SGD, lr=0.01). Validation accuracy 68.6%. <b>Key-recovery</b> via probabilistic accumulation: 76.9% (5 traces)<math>\rightarrow</math>100% (11 traces). [108]</p> <p><b>Kyber (SW)</b>: Decapsulation; Blind SCA via DL-BSCA using MLP classifier with MC-labeling (GMM on 100 PoIs). 80k profiling traces; 20k attack; label encoding <math>(B+1)h_m + h_y</math> with <math>\approx 1.2\%</math> correct labels. Hyperparameter search over 100 models (dropout/no-dropout). Achieves GE=9.2 for <math>s_0</math> recovery under blind conditions. Has been outperformed by its CNN version [82].</p> <p><b>Kyber (SW, Masked)</b>: Decapsulation; FO-based re-encryption leakage through masked software PRF. Attack uses a 5-layer MLP (FC1-FC5) with Sigmoid output for two-class (label) distinguishing of PRF inputs. Fully-connected design chosen to capture multivariate leakage of masked SW. Produces a reliable plaintext-checking oracle enabling adaptive re-encryption; key recovery succeeds within <math>\leq 60k</math> oracle traces. [98]. <b>HQC</b> was included analytically, but it wasn’t really attacked.</p> |

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(SW)Firmware, (HW)FPGA, (R)RTL, (M)Masked, (S)Shuffled, (A)Anti-Tampering Cover

**Table 12.** Summary of MLP SCAs against Dilithium and FALCON

| MLP (Multi-Layer Perceptron neural network)  |
|--|
| <p><b>Dilithium (SW)</b>: Signing + key-gen; secret-coefficient labeling (s1,s2). single-trace profiling attack on NTT (s1: 100%) and sampling/addition/rounding/packing (s2: <math>\leq 98\%</math>). 60k traces (45k train/5k val/10k attack). NN: BN-Dense-BN-ReLU-Dense-Softmax (Nadam). Full s1 recovery; s2 up to 98% per op. [34].</p> <p><b>Dilithium (SW)</b>: Signing; <math>y_{i,j}</math> zero/nonzero labeling; Four MLP classifiers (Dense/Dropout stacks; up to <math>\approx 3.7 \times 10^5</math> params) tuned via Hyperband; 548-sample trace windows. Profiling on Device A; attack uses <math>\sim 100k</math> signing traces; classifier accuracy <math>\approx 0.999</math>. Full <math>s_1</math> recovery and end-to-end <b>key extraction</b>; Level-2 break requires <math>\approx 10</math> min of signatures. [58]</p> <p><b>Dilithium-2 (SW)</b>: Signing; coefficient-value labeling (<math>-2..2</math>) during <code>skDecode</code>. Eight MLP classifiers (440-sample input; 5 classes; Nadam, 100 epochs) trained on 3.2M segments (<math>&lt; 10h</math> profiling). Single-trace coefficient recovery <math>\approx 0.92</math>; full <math>s_1</math> recovery with single trace succeeds with 9% (linear-equation variant). 1000 traces <math>\rightarrow</math> 100% <b>key recovery</b>; BKZ variant also feasible without <math>t_0</math>. [102]</p> <p><b>Dilithium-2 (SW)</b>: Signing; <math>LSB(t_0[i])</math> labeling during <code>polyt0_unpack</code>. 168 models (8 coeff.-types; 512-256-256 FC layers); trained on 2.56M segments (550-sample windows). Majority voting yields <math>\approx 57</math> wrong LSBs; unanimous voting reduces to <math>\approx 15</math> with <math>\approx 126</math> undecided. With 960 averaged traces, 80.5% of <math>(s_1, s_2)</math> coefficients recovered with 0.875 probability; combined with partial <math>t_0</math> recovery gives <math>\approx 0.7</math> full <math>s_1</math> <b>key recovery</b>. [101]</p> <p><b>Dilithium-2 (M, SW)</b>: Signing; NTT-encryption (unmasked) and sparse multiplication (masked). Boolean masking defeated. Profiling via MLP (BN-Dense-BN-ReLU-Dense-Softmax; Adam, CE loss; inputs normalized to <math>[-1,1]</math>). Labels = 3Byte of <math>s_1[i]</math> (unmasked) or all 256 byte-values of masked shares. Unmasked: 2000 profiling traces, 8000 attack traces <math>\rightarrow</math> 100% full-key recovery. Masked: 9000 profiling, 8000 attack traces <math>\rightarrow</math> 100% <b>full-key recovery</b> (single-trace). [48]</p> <p><b>FALCON (SW)</b>: CDT sampling; Visible early-stop leakage on AVR and invisible Hamming-weight leakage on ARM defeat comparison-op "constant-time" CDT. BN-Dense-BN-Dense-BN-Dense trained on 112k traces (736-sample windows; Adam lr= <math>10^{-4}</math>; 10 epochs). Test accuracy 99.97%, F1=1.0. Single-trace recovery of CDT output (14 classes). Leakage assessment only, no key/message recovery. [23]</p> |
| (SW)Firmware, (HW)FPGA, (M)Masked  |

**Table 13.** Summary of CNN SCAs

| CNN (Convolutional neural network)  |
|---|
| <p><b>Kyber (SW):</b> Decapsulation; Blind SCA via DL-BSCA using CNN trained with MC-labeling (GMM on 100 PoIs). 80k profiling traces; 20k attack; same noisy-label encoding <math>(B+1)h_m+h_y</math>. Hyperparameter search over 100 architectures. Best CNN+MC configuration reaches GE=2.01, successfully recovering <math>s_0</math> under blind conditions. Has outperformed its MLP version [82].</p> <p><b>Kyber (M,SW,HW):</b> Decapsulation; FO-based re-encryption leakage via PRF executions. CNN distinguisher (Conv1-Conv6 + FC, Sigmoid) trained to classify PRF inputs as reference vs. random (label). CNN attains <math>\approx 99.8</math>-99.9% accuracy on unprotected SW/HW, enabling a strong plaintext-checking oracle and full key recovery within <math>\leq 60</math>k traces. Under TI-masked HW, accuracy collapses to <math>\approx 51\%</math>, rendering the oracle ineffective [98].</p> <p><b>Kyber (SW):</b> Decapsulation; labels = secret-key coefficient <math>s_0</math> (0-3328). CNN with ciphertext-knowledge (5 Conv layers + Dense fusion + 5-layer classifier; RMSprop, lr <math>10^{-6}</math>) trained on 200k-500k traces (600 samples/trace). Attack phase requires <math>\leq 50</math> traces (500k profiling) to reach mean-rank 0 and recover targeted 12-bit key coefficients. Ciphertext knowledge (2 or 4 coeffs) reduces profiling/attack traces needed. No masking/shuffling; <b>full key recovery</b> achieved by iterating per-coefficient attack. [36]</p> <p><b>Kyber (SW):</b> Decapsulation; FO re-encryption leakage from PRF/RO execution (software, power/EM). Multi-valued PC oracle via deep NN (6 Conv layers + BN/AvgPool + FC1-FC3 with SELU/Softmax). Training uses <math>\mu</math>-class labels from valid ciphertexts. Achieves full key recovery with <b>576 traces</b> (vs. 3,072 baseline), an <b>81%</b> reduction at 99.9999% success probability. [93]</p> <p><b>Kyber (A, SW):</b> Decapsulation; Adaptive Slimmed Pyramid Network (ASPN) distinguisher. Binary classification/labeling of <math>m_0/m_1</math> from re-encryption EM traces; 100k-sample traces; 800 train / 200 test per class; accuracies 0.99 (18 mm) to 0.893 (24 mm with clone); full key recovery with 5818.5 traces; ASPN3 optimal; train 39.41 s / test 0.49 s; T-test reduces model size by 94% while reaching 0.973 accuracy [22].</p> <p><b>Dilithium-2 (SW):</b> Targets Montgomery-reduction leakage to recover <math>cs_1</math> (labeled). Architecture: 3 Conv layers (filters 12/24/48; kernels 64/128/256; strides 1/6/1), BN + avg-pool after each, followed by one Dense-Softmax layer (Adam, lr <math>= 4 \times 10^{-4}</math>, 150 epochs, batch=1600). Trained on 200k segments; single-trace <math>cs_1</math> recovery SR=70.5-74.3%. When fused with LR-based <math>y</math> recovery and COBRA/ILP equation solving, the attack achieves full <math>s_1</math> <b>key recovery</b> in 1-3 signatures [78].</p> <p><b>Dilithium-2/3/5 (SW):</b> Signing; leakage during INTT and Montgomery reduction. Labels = <math>ct_0</math> coefficients (sign and value). CNN distinguisher: (i) 1-layer CNN for sign; (ii) <math>2 \times</math> Conv (32,64, <math>3 \times 3</math>) + BN + MaxPool + FC(128,64,ReLU) for value (Adam, CrossEntropyLoss). Trained on 40k <math>ct_0</math> traces; tested on 10k <math>cs_1/cs_2</math> samples. Success rates (range [-31,31]): <math>cs_1/cs_2 = 26.7/36.2\%</math> (Dilithium2), 20.5/28.8% (Dilithium3), 22.4/31.5% (Dilithium5). <b>Full <math>s_1, s_2</math> recovery</b> in 10/27/18 traces using residual-analysis solver [79].</p> |

(SW)Firmware, (HW)FPGA, (M)Masked, (A)Anti-Tampering Cover

**Table 14.** Summary of othes AI-based Techniques SCAs

|   |
|---|
| <b>RF (Random Forest)</b>   |
| <b>Kyber (SW):</b> Decapsulation; labels = HW(m), HW(y) from basemul/fqmul in pointwise multiplication. Random Forest HW classifier trained on clone-device PoIs (CPA-selected); O0 HW accuracy 69–80% (input), 81–85% (output); O3 accuracy $\approx$ 70–72%. Simulated attack achieves coefficient GE=0 in $\approx$ 671 traces (perfect HW) or $\approx$ 8524 traces (95% HW). Practical evaluation recovers 20 secret coefficients with 35–5000 traces. <b>Full-key recovery</b> feasible under blind setting. [81]   |
| <b>Kyber512/768/1024 (O):</b> Decapsulation; decryption-failure oracle exploited to build bilateral linear inequalities on secret key coefficients. Oracle remains extractable even with masked/constant-time ciphertext-comparison and sanity-check countermeasures. ML-based contraction uses a Random Forest (300 trees, depth 30) trained on JSD sequences over BP iterations. Filtered/unfiltered cases need 5.2k/8k inequalities. <b>Full key recovery</b> with $\approx$ 5.5k (512), 7k (768), 6.75k (1024) inequalities; query factor $\approx$ 2.5–2.6 and $\approx$ 40–48% query-complexity reduction [77]. |
| <b>LLM (Large Language Model)</b>   |
| <b>Kyber-256 (SW):</b> Decapsulation; <i>operation-type</i> labeling for SPA (256 ops). ChatGPT used as zero-shot classifier (no NN training); segmented traces (max 36 ops/query). 0-shot/general prompts give OPSR=0, but expert prompting (strategies 1&2) enables <b>full private-key recovery</b> , with OPSR $\approx$ 0.5 over 10 runs [111].  |
| <b>Kyber (R):</b> Post-analysis fortification; SCAR employs an LLM (Falcon-7B) to automatically generate leakage-mitigated RTL by inserting Boolean masking into vulnerable lines flagged by the GNN. The LLM rewrites RTL modules (e.g., <code>intmul</code> ) into masked variants; validation via post-synthesis dynamic-power analysis confirms reduced leakage at the modified sites. No AI-based attack performed; LLM used solely for countermeasure synthesis. [91]   |
| <b>GNN (Graph Neural Network)</b>   |
| <b>Kyber (R):</b> Leakage assessment (not attacking); SCAR analyzes Kyber’s RTL CDFG using a GNN (GCN1-GCN2 + FC + sigmoid), trained on AES-Comp (lr=0.01, batch=20, dropout=0.3, 32 epochs). Node-features: vulnerable-path count, degree, Hamming-distance, AND/OR/XOR/MUX ops. Achieves 88.89% accuracy, 95.38% precision, 91.20% recall for Kyber; identifies the <code>intmul</code> module as leakage-prone. [91]   |
| <b>DQN</b>  |
| <b>Kyber (SW,HW):</b> Decapsulation; <i>no leakage labeling</i> (RL is label-free). SPA-GPT uses Deep Q-Network (DQN) with a 4-layer <b>MLP</b> (BN + Dense(40) $\times$ 3) to automatically segment a <i>single</i> power trace, since MLP-based Q-networks approximate the Q-table efficiently for large action spaces. Reward = negative mean Euclidean distance between segments. Achieves <b>100% segmentation accuracy</b> on Kyber traces and recovers the <i>intermediate value crucial for deriving the secret key</i> ; no masking defeated, no full-key recovery demonstrated. [104]                       |
| <b>DNN (Deep Neural Network)</b>  |
| <b>Kyber512/768/1024 (M, SW):</b> Decapsulation; ( $m \neq m'$ ) labeling. Neural-network-based multi-valued PC-oracle built from a fully connected feed-forward NN (3 hidden layers, 1024 units each; ReLU; Softmax; Adam, lr=0.001; batch=64; 10 epochs). Input uses $t_1 = 3$ PoIs; each oracle query uses $t_1 t_2 = 9$ traces. NN accuracy $\approx$ 98.97%, rising to $\geq$ 99.96% with majority voting. <b>Full-key recovery</b> requires 432 / 648 / 864 traces for Kyber512/768/1024. [103]   |
| <b>RNN (Recurrent Neural Network)</b>   |
| <b>Kyber-768 (M,SW): Fails to recover key.</b> Decapsulation; polytomsg(m)[i] labeling; first-order masked poly tomsg attacked. 50k traces; bitwise segmentation ( $256 \times 3 \times 225$ ); <70% accuracy and fails to converge. Attack requires chosen ciphertexts, preprocessing and ECT-based error correction [31]. See Table 11 for successfully MLP attack of this paper.   |
| (SW) Firmware, (HW) FPGA, (R) RTL, (M) Masked, O(Oracle Based)  |
| <div><div>Neural / Deep</div><div>Classical</div><div>Reinforcement / Decision</div></div>  |



## A Full Methodology Details

This chapter delineates the systematic methodology employed to identify, select, appraise, and synthesize the existing body of literature concerning the application of Artificial Intelligence (AI) techniques to conduct Side-Channel Attacks (SCAs) against the cryptographic algorithms standardized or finalized by the NIST Post-Quantum Cryptography (PQC) project. The protocol is designed to be transparent, replicable, and rigorous, following the guidelines for systematic literature reviews proposed by Kitchenham and Charters [50,51], ensuring the resulting review is comprehensive and unbiased. The review process is structured into three primary phases: planning, conducting, and reporting [50]. This section details the first two phases.

### A.1 Search Strategy

A multi-stage search strategy was executed to ensure a comprehensive and unbiased collection of relevant primary studies. The search was concluded on the cutoff date of August 16, 2025.

**Database Selection** The selection of information sources is critical for capturing high-quality, peer-reviewed research in this fast-moving field. The following digital libraries and archives were chosen based on their prevalence as publication venues for top-tier cryptography, hardware security, and computer security research:

- **IEEE Xplore:** A comprehensive database covering key conferences and journals in hardware security and electronics [40].
- **ACM Digital Library:** The main source for publications from the Association for Computing Machinery, including flagship security conferences like CCS and ASIACCS [7].
- **Engineering Village:** A comprehensive engineering database covering a wide range of technical literature, also provided by Elsevier [26].
- **ScienceDirect & Scopus:** Major interdisciplinary databases from Elsevier, providing broad coverage of scientific and technical literature [28,27].
- **SpringerLink (including LNCS):** Publisher of major cryptography conference proceedings, most notably the *Transactions on Cryptographic Hardware and Embedded Systems* (TCHES), the flagship journal for side-channel research [90].
- **IACR ePrint Archive:** The primary repository for pre-print cryptographic research, essential for capturing the latest findings [41].

**Search String Construction** To ensure the search query is systematic and directly addresses the research questions, the PICOC (Population, Intervention, Comparison, Outcome, Context) framework was used to deconstruct the query into its core concepts and corresponding keywords [51]. This structured approach is vital for achieving high recall, as the terminology in this interdisciplinary field is highly specific.

**Search String** The keywords from the PICOC framework (Table 15) were combined using Boolean operators (AND, OR) to form the final search query (Fig. 3). It is a standard and necessary step in a systematic review to refine the comprehensive list of concepts from the PICOC framework into a practical search string optimized to balance recall (finding all relevant studies) and precision (excluding irrelevant studies) [51].

This refinement involves three key adjustments:

1. **Ensuring Comprehensive Coverage:** The PICOC list contains both the submission names (e.g., “Kyber”) and the final FIPS standard names (e.g., “ML-KEM”). While the majority of the research literature still refers to the submission names, especially in works published before or shortly after standardization in 2024 [69], newer publications increasingly adopt the FIPS designations. To capture both historical and future works, our final search string explicitly included *both* forms (e.g., “Kyber” OR “ML-KEM”), thereby maximizing recall and ensuring consistency across publication timelines.
2. **Improving Precision:** The PICOC list includes broad categorical terms like “lattice-based” or “hash-based.” Including these in the primary search string would retrieve a vast number of articles on cryptographic schemes that are not part of the NIST PQC final selection, significantly reducing the precision of the search. The final string was therefore focused on the specific names of the selected algorithms to anchor the search to the core population of this review.
3. **Excluding Unused PICOC Dimensions:** Although the PICOC framework also defines *Comparison* and *Outcome*, these elements were not included in the final search string. This decision improved precision, since they either introduced excessive noise (e.g., generic countermeasure terms) or were already implicitly captured by the selected *Population*, *Intervention*, and *Context* dimensions.

To enhance the precision of the search and ensure the relevance of the retrieved studies, the query was executed against the title, abstract, and keyword fields of the articles within the selected databases. The final string was designed to be adaptable to the specific syntax of each database.

**Gold Set Validation** To validate the effectiveness of the search query, a “Gold Set” of 8 highly relevant articles, identified during the initial exploratory phase and detailed in Table 16, was used. This set serves as a ground truth — often referred to as a quasi-gold standard in SLR methodology [110] — to empirically test the search string’s ability to retrieve cornerstone papers in the field. The search query was considered effective as it successfully retrieved all 8 articles, confirming its high recall.

## A.2 Study Selection Criteria and Process

A systematic, multi-stage screening process was applied to the results of the literature search to identify studies that meet the predefined eligibility criteria.

**Table 15.** PICOC Framework for Search Query Definition

| PICOC Element       | Definition for this SLR  | Keywords and Synonyms  |
|---------------------|--|--|
| <b>Population</b>   | NIST Post-Quantum Cryptography (PQC) Round 4 finalists and standardized algorithms.  | "post-quantum cryptography", "PQC", "quantum-resistant", "Kyber", "ML-KEM", "Dilithium", "ML-DSA", "SPHINCS+", "SLH-DSA", "FALCON", "FN-DSA", "HQC"  |
| <b>Intervention</b> | The application of AI, Machine Learning, or Deep Learning models as the primary analysis tool or distinguisher in an attack. | "artificial intelligence", "AI", "machine learning", "ML", "deep learning", "DL", "neural network", "CNN", "recurrent neural network", "RNN", "generative adversarial network", "GAN", "transformer", "reinforcement learning", "graph neural network", "GNN", "support vector machine", "SVM", "random forest", "gradient boosting", "computational intelligence", "data mining", "predictive modeling" |
| <b>Context</b>      | Side-channel analysis of physical cryptographic implementations.   | "side-channel", "side channel", "SCA", "power analysis", "timing attack", "electromagnetic analysis"   |

**Inclusion and Exclusion Criteria** The inclusion and exclusion criteria provide the explicit rules that govern the selection of studies. This formalization is essential to eliminate subjective bias and ensure the process is transparent and replicable [50,51]. The criteria is detailed in Table 17.

The screening process was conducted in two main phases by two independent reviewers: a title and abstract screening, followed by a full-text screening. Any disagreements were resolved through discussion to reach a consensus.

### A.3 Quality Assessment and Data Extraction

For each study that passed the screening process, a quality assessment was performed, and a structured data extraction process was initiated.

**Quality Assessment Checklist** The quality of each included study was appraised to assess its rigor, validity, and risk of bias. This is a critical step in the systematic review process, as recommended by established guidelines [50], to ensure a robust synthesis of the literature. The specific criteria used for this appraisal are detailed in the quality assessment checklist presented in Table 18.

To systematically evaluate each study against these questions, a three-point scoring scale was used: a score of **1.0 for 'Yes'** if the criterion was fully met, **0.5 for 'Partially'** if the criterion was addressed but lacked sufficient detail, and **0.0 for 'No'** if the criterion was not met. This approach provides a quantitative measure of each study's methodological rigor and transparency [51].

It is important to note that while this scoring allows for a quantitative comparison, no quality cutoff score was used to exclude studies. The primary purpose of the quality assessment in this review is not to filter the literature but to aid in

**Fig. 3.** Base search string.

```

("post-quantum cryptography" OR "PQC" OR "quantum-resistant"
  ↳ OR "Kyber" OR "Dilithium" OR "SPHINCS+" OR
  ↳ "F\textsc{alcon}" OR "Hamming-Quasi-Cyclic" OR
  ↳ "ML-KEM" OR "ML-DSA" OR "SLH-DSA" OR "FN-DSA" OR
  ↳ "HQC")
AND
("artificial intelligence" OR "AI" OR "machine learning" OR
  ↳ "ML" OR "deep learning" OR "DL" OR "neural network"
  ↳ OR "CNN" OR "reinforcement learning" OR "generative
  ↳ AI" OR "transformer" OR "generative adversarial
  ↳ network" OR "GAN" OR "recurrent neural network" OR
  ↳ "RNN" OR "support vector machine" OR "SVM" OR
  ↳ "gradient boosting" OR "random forest" OR "graph
  ↳ neural network" OR "GNN" OR "computational
  ↳ intelligence" OR "data mining" OR "predictive
  ↳ modeling")
AND
("side-channel" OR "side channel" OR "SCA" OR "power
  ↳ analysis" OR "timing attack" OR "electromagnetic
  ↳ analysis" )

```

the synthesis and interpretation of the results [50]. The scores are used to contextualize findings and weigh the strength of evidence, particularly when comparing studies with conflicting results.

**Data Extraction Form** A data extraction form was designed to systematically collect relevant information from each included study. This structured approach, detailed in Table 19, ensures consistency and facilitates the aggregation and comparison of data across studies to answer the research questions. Systematically documenting study characteristics and findings in this manner is a crucial step that supports clarity, precise reporting, and the ability to replicate the examination, as mandated by SLR guidelines [50].

**Table 16.** Revised Gold Set for Database Search Validation

| # | Article Title / Reference  | Justification for Inclusion   |
|---|--|---|
| 1 | <b>Deep learning enhanced side channel analysis on CRYSTALS-Kyber</b> (Hoang et al., 2024) [36]                                    | This paper presents the first known side-channel attack on Kyber using a Convolutional Neural Network (CNN), recovering the private key with only 50 traces in a practical, black-box scenario.   |
| 2 | <b>Breaking the Blindfold: Deep Learning-based Blind Side-channel Analysis</b> (Rezaeezade et al., 2024) [82]                      | A cutting-edge paper from a top-tier venue (USENIX Security) that introduces a novel deep learning framework and validates it with a successful attack on Kyber.  |
| 3 | <b>Single-Trace Side-Channel Attacks on CRYSTALS-Dilithium: Myth or Reality?</b> (Wang et al., 2023) [102]                         | This paper details a profiled deep learning-assisted power analysis attack against CRYSTALS-Dilithium using a Multilayer Perceptron (MLP), achieving the first successful full key recovery from a single power trace by targeting the secret key unpacking procedure.                      |
| 4 | <b>A side-channel attack on a masked hardware implementation of CRYSTALS-Kyber</b> (Ji & Dubrova, 2025) [44]                       | This journal paper describes a profiled deep learning-assisted attack using a Multilayer Perceptron (MLP) against a first-order masked hardware (FPGA) implementation of Kyber, demonstrating the first practical message recovery attack on a protected hardware version of the algorithm. |
| 5 | <b>Breaking a Fifth-Order Masked Implementation of CRYSTALS-Kyber by Copy-Paste</b> (Dubrova et al., 2023) [25]                    | This highly significant paper from APKC'23 demonstrates a neural network attack that breaks a very high-order (fifth-order) masked implementation of Kyber using a Multilayer Perceptron (MLP) trained with a novel "recursive learning" method, pushing the state-of-the-art.              |
| 6 | <b>Curse of Re-encryption: A Generic Power/EM Analysis on Post-Quantum KEMs</b> (Ueno et al., 2022) [98]                           | This foundational paper details a generic side-channel attack against numerous post-quantum KEMs, including Kyber, and validates its practicality by using a deep learning-based (CNN/MLP) distinguisher to create a plaintext-checking oracle.   |
| 7 | <b>Single Trace Analysis of Visible vs. Invisible Leakage for Comparison-Operation-Based CDT Sampling</b> (Choi et al., 2024) [23] | This paper proposes a deep learning-based side-channel attack using a Multilayer Perceptron (MLP) to recover CDT sampling values from the FALCON signature scheme on an ARM Cortex-M4 by exploiting a novel "invisible" vulnerability in constant-time comparison operations.               |

**Table 17.** Inclusion and Exclusion Criteria

| Criteria                | Inclusion<br>(Must Have)   | Exclusion<br>(Must Not Have)  |
|-------------------------|--|---|
| <b>Subject Matter</b>   | The study must describe a side-channel attack that uses an AI/ML/DL model as the primary distinguisher or analysis tool.                                       | The study describes a conventional SCA (e.g., CPA, TA) without an AI/ML component. The study describes a different type of attack (e.g., fault injection, cryptanalysis). The study is about AI security in general, not SCA. |
| <b>Target Algorithm</b> | The target of the attack must be an implementation of a NIST PQC Round 4 finalist or a standardized algorithm (Kyber, Dilithium, SPHINCS+, FALCON, HQC, etc.). | The target is exclusively a pre-quantum algorithm (e.g., AES, RSA) UNLESS the technique is explicitly presented as a foundational method later applied to PQC in other works.   |
| <b>Publication Type</b> | The study must be a full-text, peer-reviewed research paper (journal, conference, or workshop) or a relevant technical pre-print (e.g., from IACR ePrint).     | The publication is a poster, abstract, presentation slide deck, editorial, or summary without sufficient technical detail.  |
| <b>Language</b>         | The study must be written in English.  | The study is written in any language other than English.  |
| <b>Availability</b>     | The full text of the study must be accessible.   | The full text cannot be obtained.   |

**Table 18.** Quality Assessment Checklist

| QA#        | Quality Assessment Question  |
|------------|--|
| <b>QA1</b> | Are the research objectives and attack goals clearly stated?   |
| <b>QA2</b> | Is the target PQC algorithm, implementation (software/hardware), and platform described in sufficient detail to allow for replication?           |
| <b>QA3</b> | Is the side-channel data acquisition setup (e.g., equipment, sampling rate, number of traces) clearly documented?                                |
| <b>QA4</b> | Are the AI/ML model architecture, hyperparameters, and training process adequately specified?  |
| <b>QA5</b> | Are the results evaluated using standard, appropriate metrics for both SCA (e.g., Guessing Entropy, Success Rate) and ML (e.g., accuracy, loss)? |
| <b>QA6</b> | Does the study discuss its limitations and potential threats to the validity of its findings?  |
| <b>QA7</b> | Is the source code or dataset made publicly available to support reproducibility?  |

**Table 19.** Data Extraction Form

| Category                    | Data Field              | Description   |
|-----------------------------|-------------------------|---|
| <b>Bibliographic Data</b>   | Study ID                | Unique identifier for the study within this SLR.                                  |
|                             | Authors & Year          | Authors and publication year.   |
|                             | Title & Venue           | Full title and publication venue (e.g., TCHES 2025).                              |
| <b>PQC Target Details</b>   | PQC Algorithm           | Kyber, Dilithium, SPHINCS+, FALCON, HQC.  |
|                             | Security Level          | e.g., NIST Level 1 (Kyber-512), Level 3, Level 5.                                 |
|                             | Attack Vector           | e.g. Re-encryption of Decryption, Fujisaki-Okamoto (FO) transformation, etc...    |
|                             | Implementation Type     | FPGA, ASIC, RTL, Software.  |
|                             | Target Platform         | 8-bit AVR, ARM Cortex-M4, Artix-7 FPGA.   |
|                             | Target Platform Details | The specific processor, board or setup.   |
|                             | Countermeasures         | None, Masking, Shuffling, Hiding, Constant-Time, Anti-tampering or a combination. |
|                             | Countermeasures Details | e.g. Masking order.   |
| <b>Side-Channel Context</b> | Side-Channel Type       | Power, Electromagnetic (EM), Timing.  |
|                             | Fault Injection         | Yes or No.  |
|                             | Tool                    | Setup. e.g. ChipWhisperer, Oscilloscope, etc...                                   |
| <b>AI/ML Intervention</b>   | Model Type              | e.g., CNN, MLP, SVM, RNN.   |
|                             | Model Details           | Key details (e.g., number of layers, filter sizes for CNNs).                      |
|                             | Profiling Traces        | Number of traces used for training the model.                                     |
|                             | Attack Traces           | Number of traces used to perform the key recovery attack.                         |
| <b>Attack Outcome</b>       | Primary Metric          | Guessing Entropy (GE), Success Rate (SR), Key Rank.                               |
|                             | Result                  | The reported value of the primary metric (e.g., "GE drops to 0 after 50 traces"). |
|                             | Key Findings            | A qualitative summary of the main conclusions drawn by the authors.               |

## B Study Selection

This section presents the findings obtained by executing the review protocol detailed in the previous section. We begin by reporting the results of the literature search and selection process, followed by a summary of the quality assessment. Then we synthesize the key findings from the selected primary studies, focusing on the most relevant trends and insights regarding AI-based side-channel attacks against post-quantum cryptographic algorithms. The section concludes with a bibliometric analysis derived from the data systematically collected from the final set of primary studies.

### B.1 Literature Search and Initial Results

The initial phase of the systematic literature review consisted of querying seven electronic databases: *IEEE Xplore*, *ACM Digital Library*, *Engineering Village*, *ScienceDirect*, *Springer Link*, the *IACR ePrint Archive*, and *Scopus*. This comprehensive search strategy yielded a total of **1,495 articles**. A subsequent de-duplication process eliminated 157 duplicate records, resulting in a set of **1,338 unique primary studies**.

These studies were then assessed according to the predefined inclusion and exclusion criteria. As shown in Table 20, the majority of records originated from *Springer Link* (1,000), followed by *ScienceDirect* (175) and *Engineering Village* (89). After de-duplication, *Springer Link* contributed 990 unique entries, while *IEEE Xplore* and *ScienceDirect* accounted for 60 and 175 unique entries, respectively.

**Table 20.** Summary of the Study Selection Process by Source

| Source              | Found       | Unique*     | No Access | Potentially Relevant | Selected  |
|---------------------|-------------|-------------|-----------|----------------------|-----------|
| IEEE Xplore         | 61          | 60          | 0         | 10                   | 10        |
| ACM Digital Library | 5           | 3           | 0         | 1                    | 1         |
| Engineering Village | 89          | 54          | 4         | 3                    | 3         |
| ScienceDirect       | 175         | 175         | 0         | 1                    | 1         |
| Springer Link       | 1000        | 990         | 5         | 2                    | 2         |
| IACR ePrint Archive | 56          | 36          | 0         | 7                    | 7         |
| Scopus              | 109         | 20          | 0         | 3                    | 3         |
| <b>Total</b>        | <b>1495</b> | <b>1338</b> | <b>9</b>  | <b>27</b>            | <b>27</b> |

(\*) After de-duplication process

Cutoff date of August 16, 2025

During the screening process, **nine articles were excluded due to lack of access**, leaving **1,329 accessible papers** for further evaluation. Of these, **27 were identified as potentially relevant** and subsequently confirmed as the final set of **27 selected studies** for detailed analysis.



## B.2 Quality Assessment Results

The Quality Assessment Checklist defined in the methodology (Table 18) have been applied to all 27 selected primary studies. The purpose of this step was not to exclude low-quality papers, but to provide a systematic appraisal of the rigor of the included research and to inform the data synthesis phase.

The overall score is reported in Table 21. The results show that a large majority of papers (23 out of 27) scored 5.0 or higher. The most common reason for a score below 7 was the missing of source code or dataset to allow replication (QA7).

**Table 21.** Distribution of Quality Assessment Scores

| Quality Score | Number of Papers |
|---------------|------------------|
| 7.0           | 1                |
| 6.5           | 2                |
| 6.0           | 2                |
| 5.5           | 9                |
| 5.0           | 8                |
| < 5.0         | 5                |
| <b>Total</b>  | <b>27</b>        |

## C Additional Tables

**Table 22.** NIST Standardized and Forthcoming PQC Algorithms

| Standard Name | FIPS Standard    | FIPS Date / Status | NIST IR | NIST IR Date   | Underlying Algorithm | Cryptographic Family | Primary Function               |
|---------------|------------------|--------------------|---------|----------------|----------------------|----------------------|--------------------------------|
| ML-KEM        | FIPS 203         | 2024-08-13 [69]    | 8413    | 2022-07-05 [3] | CRYSTALS-Kyber       | Lattice-Based (MLWE) | Key Encapsulation (Primary)    |
| ML-DSA        | FIPS 204         | 2024-08-13 [69]    | 8413    | 2022-07-05 [3] | CRYSTALS-Dilithium   | Lattice-Based (MLWE) | Digital Signature (Primary)    |
| SLH-DSA       | FIPS 205         | 2024-08-13 [69]    | 8413    | 2022-07-05 [3] | SPHINCS+             | Hash-Based           | Digital Signature (Backup)     |
| HQC           | Pending          | Expected 2027 [71] | 8545    | 2025-03-11 [4] | Hamming-Quasi-Cyclic | Code-Based           | Key Encapsulation (Backup)     |
| FN-DSA        | FIPS 206 (Draft) | Expected 2025 [72] | 8413    | 2022-07-05 [3] | FALCON               | Lattice-Based (NTRU) | Digital Signature (Additional) |

**Table 23.** Notable PQC Candidates Not Selected for Standardization

| Algorithm        | Family        | NIST IR | NIST IR Date       | Reason for Not Being Standardized / Dropped   |
|------------------|---------------|---------|--------------------|---|
| SIKE             | Isogeny-Based | 8545    | March 11, 2025 [4] | A practical key recovery attack was demonstrated using a classical computer, rendering the scheme insecure and leading to its withdrawal from the standardization process [19,86,87].                           |
| Classic McEliece | Code-Based    | 8545    | March 11, 2025 [4] | Not selected for standardization due to concerns about very large public key sizes, skepticism about widespread adoption, and a desire to avoid creating standards incompatible with an ongoing ISO effort [4]. |
| BIKE             | Code-Based    | 8545    | March 11, 2025 [4] | A Round 4 finalist, but not selected in favor of HQC, which had a more mature and stable security analysis [4,75].  |
| Rainbow          | Multivariate  | 8413    | July 5, 2022 [3]   | A practical attack in early 2022 broke the scheme, undermining confidence in multivariate signatures and leading to its withdrawal [15,96].   |

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