

Time Domain Input  
Frame

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graph TD; A[Time Domain Input Frame] --> B[FFT]; B --> C[Magnitude]; B --> D[Phase]; C --> E[U-Net]; E --> F[IFFT]; D --> F; F --> G[Time Domain Output Frame];
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The diagram illustrates a signal processing pipeline. It begins with a 'Time Domain Input Frame' (red box), which undergoes a 'FFT' (red box). The output of the FFT is split into two paths: 'Magnitude' and 'Phase'. The 'Magnitude' path leads to a 'U-Net' (red box), which then feeds into the 'IFFT' (red box). The 'Phase' path also leads directly to the 'IFFT'. Finally, the 'IFFT' produces the 'Time Domain Output Frame' (red box).

FFT

Magnitude

Phase

U-Net

IFFT

Time Domain Output  
Frame