Name: Erick Zetina Muciño
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#### Education

Instituto Tecnológico Autónomo de México (graduation date: 12.2018)

Bachelor Mechatronics (GPA: 90/100)

## Technische Hochschule Ingolstadt (Germany) as International student

Bachelor Mechatronik/Informatik (8.2017 – 08.2018)

# Work experience

**AUDI AG** (2.2018 – 8.2018)

Ingolstadt, Germany

Software development, Tools/Methods

- Development and training of a deep neural network to estimate depth and relative position based on simulated data, which was used to feed the audi's main neural networks for autonomous driving with data.
- Application of statistical methods for the analysis of real and simulated images of same scenes as basis for improved transfer learning.
- Simulation of artificial environments.

## Schanzer Racing Electric eV (10.2017 – 12.2017)

Ingolstadt, Germany

Research intern in Autonomous Systems

- Creation of a labeling-helper GUI using classic **computer vision** algorithms to train our **convolutional neural network** to recognize paths made of traffic cones.
- Moved the old system for distributed computing to ROS.

## **Member of the ITAM Robotics Laboratory** (08.2016 – 10.2018)

Mexico City, Mexico

Science Research Intern

- A scaled model of an autonomous car as bachelor thesis using convolutional neural-networks (watch it in action overtaking moving obstacles in my GitHub).
- Applications development with ROS (Robot Operating System) in C++ for the control
  of distributed Processes.
- Implementation and improvement of algorithms of **artificial intelligence** in Self-localization methods using **OpenCV** and **Tensorflow**.

## ■ Awards ■ Language skills

Torneo Mexicano de Robótica (TMR 2017)

Third place at the national level (04.2017)

Spanish (mother tongue)

German (fluent)

English (fluent) (567 points. TOEFL)

Computer Skills

Software: ROS, Tensor Flow, Linux, Docker, Visual Studio, CMake, Git/SVM, Conan, Jira, Nx, Microsoft Office.

Programming languages: C/C++, Python (3 years experience)

Java, Matlab (1 year experience)
Javascript, Clisp, VHDL (6 months experience)