1-

1	diameter	Cost per woler	Dies per woler	Delects/cm^2
Wafer-X	16cm	15	64	0.02
Wafer-Y	20cm	24	100	0.03

A) Wafer Area = TI x (diameter /2)2 GFor Wafer-X = $\pi \times (16/2)^2 = \pi \times 64 = 201.06 \text{ cm}^2$ L) for Wafer- $Y = \pi \times (20/2)^2 = \pi \times 100 = 314, 15 \text{ cm}^2$ Die Area = Wafer area / dies per wafer

() for Wafer-X = 201,06/64 = 3.14 cm2

L) for wafer- $Y = 314.15/100 = 3.14 cm^2$

B) Mield = 1 (1+(Defects per area x Die area/2))2

George Wafer = $\frac{1}{(1+(0.02 \times (3.14/2)))^2} = 0.940$

6) for Wafer- $Y = \frac{1}{(1+(0.03\times(3.14/2)))^2} = 0.912$

Cost per die = Cost per wafer X vield

6 For Wafer_x = $\frac{15}{60 \times 0.000} = 0.24$

L) For Wafer $Y = \frac{24}{100 \times 0.912} = 0.26$

- C) Water cost decreases by 20%.

 Dies per water increased by 10%.

 Defects per area unit increased by 15%.
- 6 For Wafer-X;

Cost per wafer =
$$15 - \frac{15.20}{100} = 12$$

Defects per area =
$$0.02 + 0.02 \times 15 = 0.023$$

Wafer area =
$$\pi \times (16/2)^2 = \pi \times 64 = 201,06 \text{ cm}^2$$

$$\text{Yield} = \frac{1}{(1+(0.023\times(2.85/2)))^2} = 0.937$$

Cost per die =
$$\frac{12}{70.4 \times 0.937} = 0.18$$

-> So, the wafer-x cost per die is decreases by 25% according to the before year.

() For Wafer_4;

Cost per wafer =
$$24 - \frac{24.20}{100} = 19.2$$

Wafer area =
$$\pi \times (20/2)^2 = \pi \times 100 = 314.15 \text{ cm}^2$$

$$\text{Yield} = \frac{1}{(1+(0.034\times(2.85/2)))^2} = 0.909$$

Cost per die =
$$\frac{19.2}{110 \times 0.909} = 0.19$$

-> So, the water-4 cost per die is decreases by 26,9% according to the before year.

2-The clock rate of P1 is 3GHz and P2 is 1.5 GHz.
Given a program that has one billion instructions divided into classes as follows: 30%R-type, 50%I-type, 20%J-type.

Required	R tope	I type	J type
P1	, 2	4	3
Pz	3	3	3

4,

$$6 \text{ For P1} = 2 \times \frac{30}{100} \times 10^9 + 4 \times \frac{50}{100} \times 10^9 + 3 \times \frac{20}{100} \times 10^9$$

$$= 6 \times 10^8 + 20 \times 10^8 + 6 \times 10^8 = 32 \times 10^8$$

B) Average clock cycles per instruction (CPI)

CPI = CPU clock cycles

Instruction count

$$L_{3}$$
 for $P_{1} = \frac{32 \times 10^{8}}{10^{3}} = 3.2$

$$L_{1}$$
, For $P_{2} = \frac{30 \times 10^{8}}{10^{9}} = 3$

C) Execution time = Instruction count x CPI , 3 GHz=3×109Hz

Clock rate , 3 GHz=3×109Hz

$$\frac{6}{3 \times 10^9} = \frac{10^9 \times 3.2}{3 \times 10^9} = 1.06$$

$$\frac{6}{5} \text{ For } P_2 = \frac{10^9 \times 3}{1.5 \times 10^9} = 2$$

$$\frac{2}{1.06} = 1.88$$

-> P1 is faster than P2 by 1.88 times.