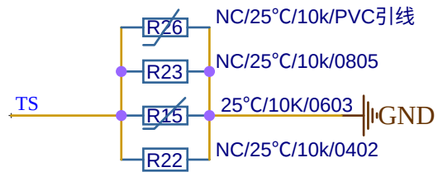
TS不用时请接地处理，否则芯片检测电池温度错误不开机也充不进去电

外围

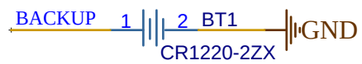
模块



VBAT/ACIN/VBUS
电源处可并联一个齐纳二极管用于保护芯片

R4/R6用以防止震荡，降低电容的充电速度，
更好地吸收电源的噪声

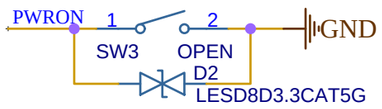
BACKUP为外部备用电池正极，可充电
接常规RTC/BIOS电池



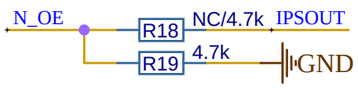
R8为BIAS电阻，用以测量内部电量数据，
精度务必≤1%

以下两个GPIO除了具有常规输入输出能力以外
还具有其他功能：
GPIO0:ADC/LDO5(输出驱动能力为50mA)
GPIO1:ADC

PWRON为电源芯片开机脚，低电平有效
R24上拉至LDO1防止误触发

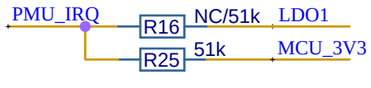


N_OE可以用来通过外部电源接入直接开机设备

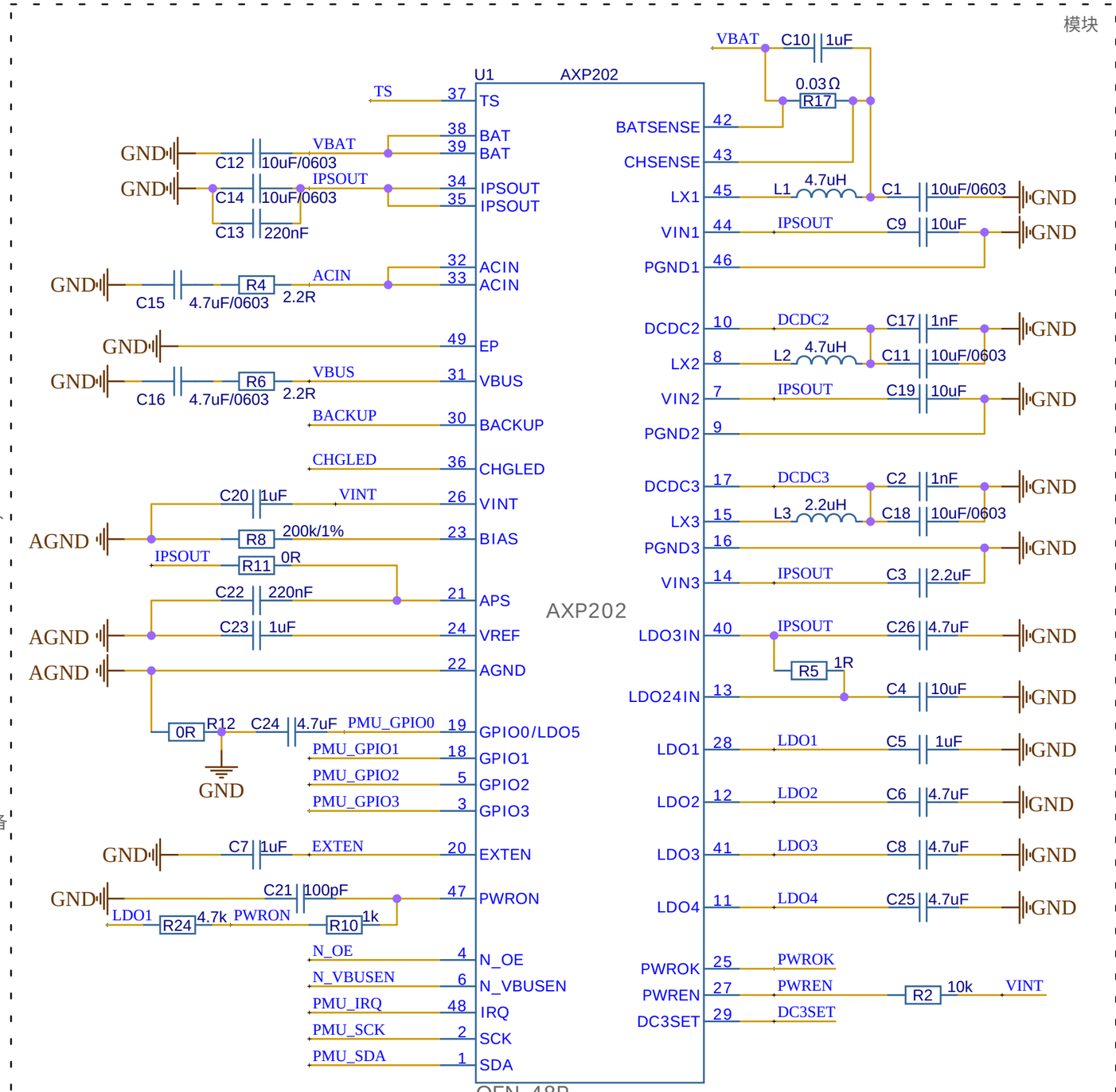


N_VBUSEN用以选择是否使能VBUS供电

IRQ中断信号引脚，可选上拉至LDO1或
MCU_3V3



I2C上拉至MCU_3V3



R17务必为30mΩ/±1%/1206电阻
PCB布线注意芯片42/43脚务必接R17电阻PAD
以确保直接从电阻PAD检测到信号

DCDC1用于PWM Charger所以没有DCDC1

如果应用中不需要用到某个 DC-DC，
只需要将对应的 LX 管脚悬空即可

DCDC2:0.7-2.275V
输出电压2.5V以下时推荐4.7uH电感
电感饱和电流需大于最大需求电流50%

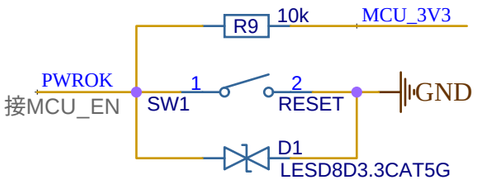
这里4.7uH有0806封装所以全用4.7uH了

DCDC3:0.7-3.5V
输出电压2.5V以上时推荐2.2uH电感
电感饱和电流需大于最大需求电流50%

CHGLED:电源状态/充电状态指示灯

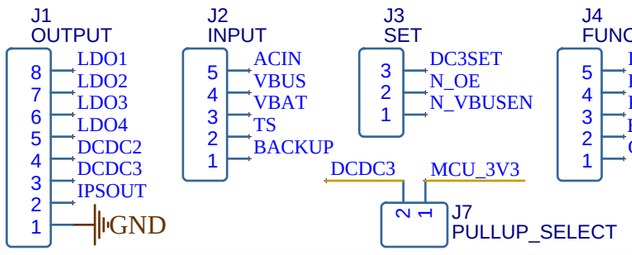
LDO1为RTC时钟供电，始终开启不可关闭

PWROK为MCU使能引脚，RESET按键接在MCU的EN
管脚上可以实现MCU的重启，上拉至MCU供电脚



DC3SET为DCDC3电压设置管脚，接APS或LDO1
为3.3V/2.5V，接地为1.8V，悬空为1.2V/1.5V

推荐DCDC3作为MCU供电，此时MCU_3V3等于DCDC3



仅代表上拉源选择，不代表电源通路选则：
1.若选择DCDC3为MCU电源则短路子接1&2
2.若选择其它作为MCU电源则将1连在其它电源上

TITLE: AXP202模块

REV: 1.0

Company: Not Better

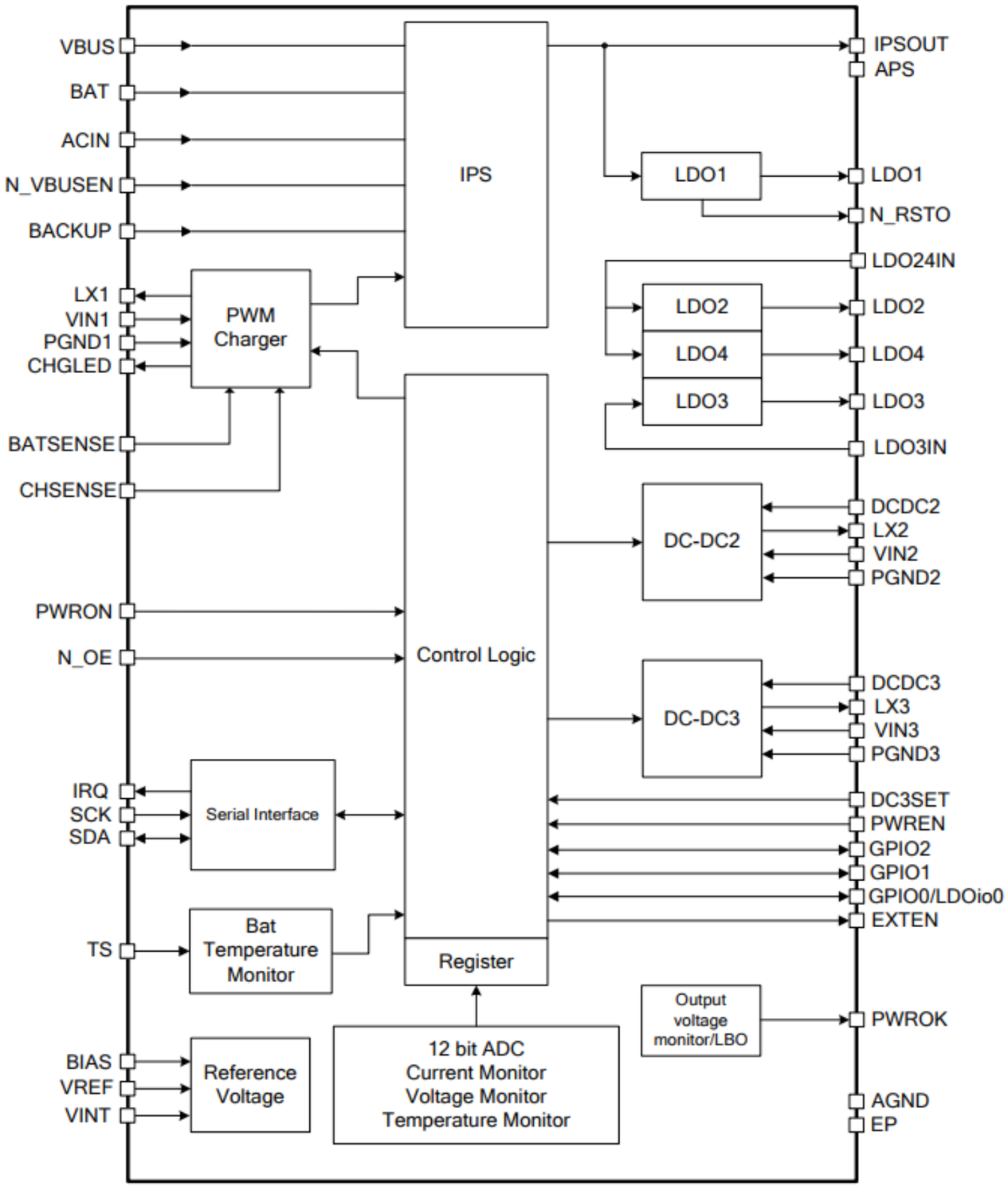
Sheet: 1/1

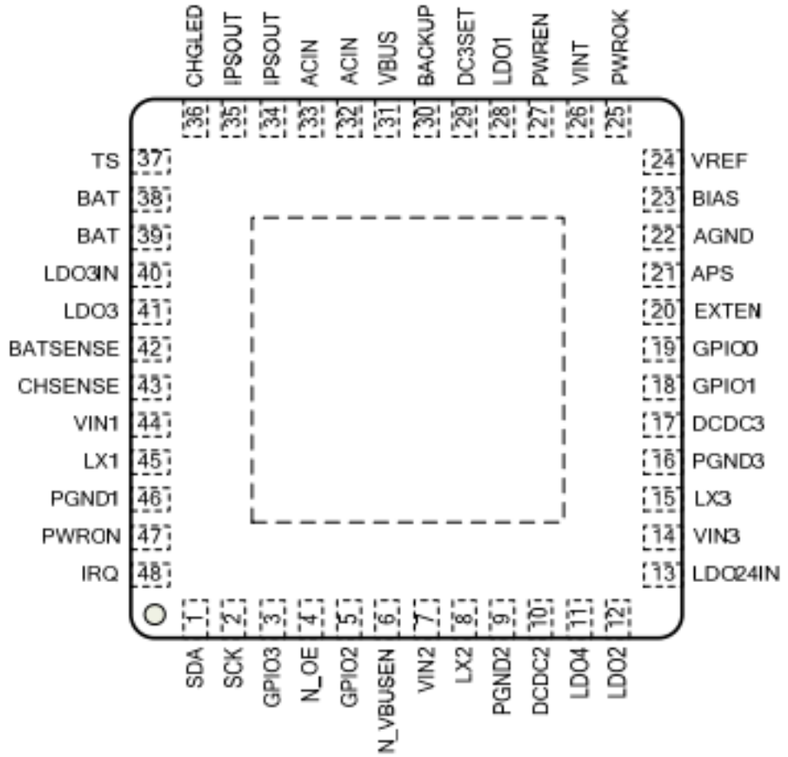
Date: 2023-03-06

Drawn By: mondraker

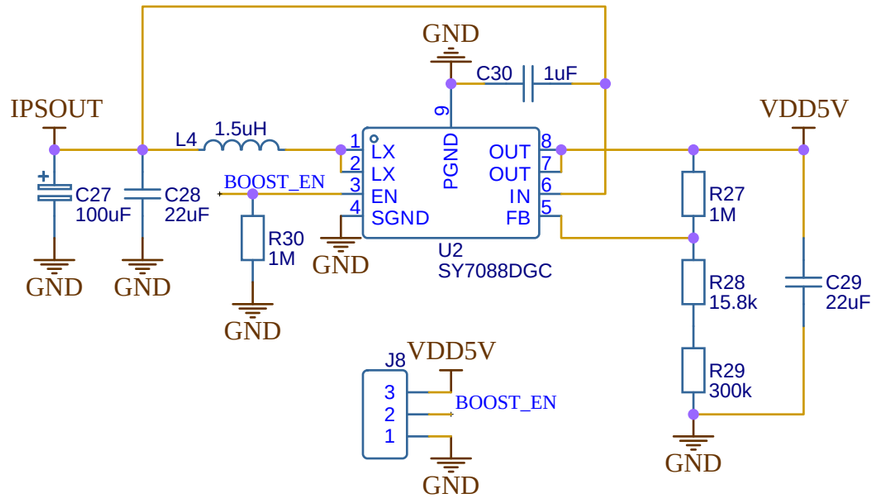
嘉立创EDA

8．功能框图(Functional Block Diagram)



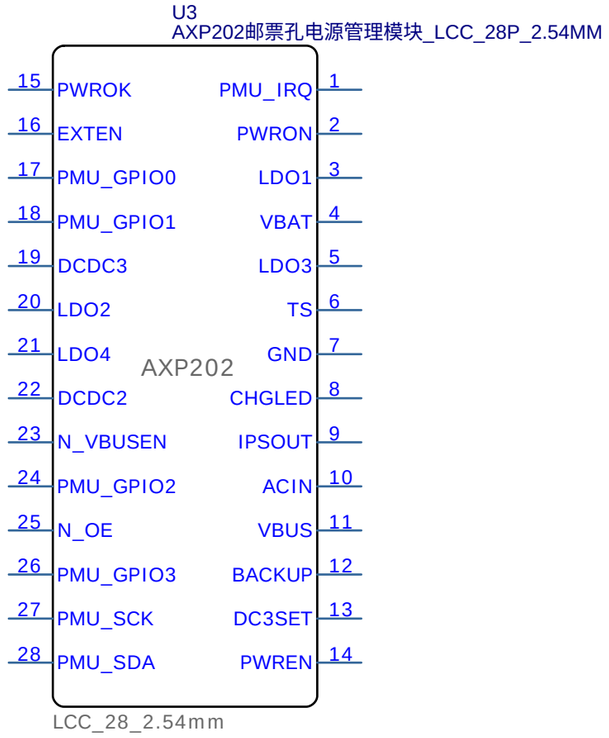


TITLE: 功能框图(Functional Block Diagram)		REV: 1.0
嘉立创EDA	Company: Fuck you	Sheet: 1/1
	Date: 2023-03-29 Drawn By: mondraker	



BOOST_EN推荐由AXP202的EXTEN管脚控制开关

TITLE: DCDC升压 Vin=2.3V~5V Vout=2.5V~5.5V 3A		REV: 1.0
嘉立创EDA	Company: Fuck you	Sheet: 1/1
	Date: 2023-03-30 Drawn By: mondraker	



TITLE: 邮票孔模块原理图与封装(含3D模型)		REV: 1.0
嘉立创EDA	Company: Fuck you	Sheet: 1/1
	Date: 2023-04-08 Drawn By: mondraker	