|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | RegDst | RegWrite | ALUSrc | ALUOp | MemWrite | RegSrc | ExtOp | NPCOp | Zero |
| LW | RT | 1 | 1 | ADD | x | MEM | UNSIGNED | NEXT | x |
| SW | x | 0 | 1 | ADD | 1 | x | UNSIGNED | NEXT | x |
| J | x | 0 | 0 | x | x | x | x | JUMP | x |
| BEQ | x | 0 | x | x | x | x | OFFSET | 1 |  |
| BEQ | x | x | 0 | x | x | x | x | NEXT | 0 |
| BNE | x | 0 | x | x | x | x | OFFSET | 0 |  |
| BNE | x | x | 0 | x | x | x | x | NEXT | 1 |
| SLT | RD | 1 | 0 | SLT | x | ALU | x | NEXT | x |
| SLL | RD | 1 | 0 | SLL | x | ALU | x | NEXT | x |
| SRL | RD | 1 | 0 | SRL | x | ALU | x | NEXT | x |
| ADD | RD | 1 | 0 | ADD | x | ALU | x | NEXT | x |
| ADDI | RT | 1 | 1 | ADD | x | ALU | SIGNED | NEXT | x |
| ADDU | RD | 1 | 0 | ADD | x | ALU | x | NEXT | x |
| SUB | RD | 1 | 0 | SUB | x | ALU | x | NEXT | x |
| AND | RD | 1 | 0 | AND | x | ALU | x | NEXT | x |
| OR | RD | 1 | 0 | OR | x | ALU | x | NEXT | x |
| NOR | RD | 1 | 0 | NOR | x | ALU | x | NEXT | x |
| XOR | RD | 1 | 0 | XOR | x | ALU | x | NEXT | x |