

BSP-01.03.00.07 ReleaseNotes

BSP Version 01.03.00.07

Release Notes

7th July 2015

Important Note

This release is for TDA3xx, TDA2xx, TDA2Ex and TI814x (for serial drivers only) platforms

Introduction

This release notes provides important information that will assist you in using the BSP software package. This document provides the product information and know issues that are specific to the BSP software package.

New in this Release

- Common
 - Removed Dynamic allocation
 - Descriptor memory and BSS memory in video drivers are statically allocated using compile time macro
 - All drivers (Video, Serial and OSAL) uses construct API of BIOS instead of using Create API which results in dynamic allocation (internally done by BIOS)
 - All compile time configurable macros are in files:
 - <bsp_driver_path>/include/bsp_cfgMem.h
 - <starterware_path>/include/vps/vps_cfgMem.h
 - <starterware_path>/include/osal/bsp_osalCfg.h
 - ISS
 - Added support for Aptina's AR0132 and AR140 sensors
 - Added support for On The Fly Capture (OTF)
 - Added utility functions to configure UB960 EVM with TIDA00262 modules
 - Enhancements & defect fixes
 - DSS Display
 - Integrated Video PLL lib for PLL configuration.
 - Added IOCTLs to get and set the sd venc paramaters.
 - Added IOCTL to configure CSC coefficients in display pipelines.
 - Added Low latency display support for overlays other than LCD1 also. Earlier it was supported only on LCD1.
 - I2C
 - Added IOCTL to recover I2C bus. Workaround to Silicon Errata "System I2C hangs due to miss of bus clear support".
 - Added IOCTLs to get and set I2C bus frequency.
 - Bug Fixes
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Installation and Usage

Installation and Usage of the BSP package could be found at BSP_UserGuide

Upgrade and Compatibility Information

- **Common**
 - **IMPORTANT: Removal of dynamic allocation of descriptor memory has resulted in a new section `".bss:extMemNonCache:vpdma"`**
 - The application needs to define this memory section and place it in the non cached DDR location.
 - The earlier implementation of getting this memory pointer through Bsp_commonInit() API is not required. Hence the relevant members of Bsp_CommonInitParams are removed.
 - The driver will not support the APIs BspUtils_allocBssMem () and BspUtils_allocDescMem ().
 - Made bsp_osal as a separate component. Application needs to include this component separately. Earlier it was part of 'bsp_common' component.
- **VIP Capture**
 - None
- **ISS Capture**
 - Please refer the demo application for expected usage
 - On The Fly (OTF) Capture is supported in this release. *vpsissCalOfStreamMapCfg_t* used to identify different possible streams
- **ISS M2M**
 - Please refer the demo application for expected usage
 - ISP Sub module ISIF *vpsissIsifGainOfstConfig_t* couple of members are renamed to reflect functionality
 - ISP Sub module ISIF Updated to support LSC sub block *vpsissIsifCtrl_t* & *vpsissIsif2DLscConfig_t*
 - ISP *vpsissIspParams_t* added control to select the input source to IPIPE IF
 - ISP Sub module NSF3 removed redundant frame width / height specifier
 - ISP Re Sizer
 - Added ability to work with LUMA Only, CHROMA Only & LUMA CHROMA modes (*vpsissRszInstConfig_t*)
 - Added ability to change data type while configuring the input parameters (*vpsissRszInputConfig_t*) (typically data type is specified with SET FORMAT interface)
- **VPE M2M**
 - None
- **DSS Display**
 - Integrated Video PLL lib for PLL configuration. PLL lib is present in starterware
 - Application should include modules starterware_pm_lib and starterware_pm_hal libraries.
- **UART**
 - None
- **McSPI**
 - None
- **I2C**
 - None
- **McASP**

- None
- **Audio**
- None

Dependencies

This release requires following tools/packages to be installed.

- Starterware Package: 01.03.00.09
 - **ISS WDR & LDC Requires additional starterware add on package.**
- Code Composer Studio Version: 5.5.0.00077
- XDC Tools Version: 3.30.06.67_core
- BIOS Version: 6.41.04.54
- CG Tool (TMS470) Version: 5.2.4
- CG Tool (C6000) Version: 7.4.2
- EDMA LLD: 02.12.00.20

Devices Supported

- TDA3xx ES1.0
- TDA2xx ES1.0, ES1.1
- TDA2Ex ES1.0
- TI814x (for Serial Drivers only)

Application Boards Supported

- TDA3xx Base board + LCD board
- TDA3xx Multi-deserializer board
- TDA3xx UB960 EVM
- TDA2xx/TDA2Ex Base board + LCD board
- TDA2xx/TDA2Ex Vision application board
- TDA2xx/TDA2Ex Vision application board with Multi-deserializer board
- TDA2xx MonsterCam board
- TDA2xx/TDA2Ex JAMR3 application board

What is Supported

Common

- Supports TDA3xx, TDA2Ex and TDA2xx EVM
 - Supports FVID2 interfaces for all the supported drivers
 - Package includes BSP driver sources, sample applications that demonstrate use of drivers and sample applications executables
 - SYS BIOS mode is tested
 - Benelli M4 (IPU1) Core 0
 - Virtual to physical address translation for VPDMA descriptor memory is supported
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VIP Capture Driver

- Supports VIP capture driver (12 instance on TDA2xx, 4 instance on TDA3xx/TDA2Ex)
- Support for OV10635 sensor
- Support for Aptina AR0132
- Support for HDMI ADV7611 receiver
- Support for HDMI SIL9127 receiver
- Support for 6-channel LVDS capture from TDA2xx multi-deserializer board
- Support for 4-channel LVDS capture from TDA3xx/TDA2Ex multi-deserializer board
- Support for LI sensor
- Support for TVP5158 decoder

VPE M2M Driver

- Supports VPE1 path
- Supported Input Formats: YUV422I, YUV420SP and YUV422SP
- Supported Output Formats: YUV422I, YUV420SP, YUV422SP, RGB888 and YUV444
- Supports SC and DEI
- Supports sliced based scaling

DSS Display Driver

- Supports DSS display driver with all pipelines going to any LCD, blended or without blending
- Supports display controller driver to set the display paths and VENC resolution
- Supports LCD and On-Chip HDMI display(only for tda2xx) (only 1080p,720p,1080i and 480P HDMI modes supported)
- Supports Off-Chip HDMI (SIL9022a, supports 1080P60, 720P60. Only for TDA3xx)
- Supports On Chip SD-VENC output (NTSC and PAL formats - only for TDA3xx)
- Supports DSS Write-back pipeline while display in progress (Capture Write-back)

UART Driver

- Device Driver for UART on IPU core 0
- Sample Application that demonstrate the use of driver for UART - Echo Test.

McSPI Driver

- Device Driver for McSPI on IPU core 0
- Sample Applications that demonstrate the usage of Driver:
 - Writes to On Board Serial Flash in case of TI814X
 - EVM to EVM Communication for TDA3xx, TDA2xx and TI814x
 - Loopback Testing for TDA3xx and TDA2xx

I2C Driver

- Device Driver for I2C on IPU core 0
- GIO and IOM Model APIs are supported for Application
- Sample Application that demonstrate the usage of Driver:

McASP Driver

- Device Driver for McASP on DSP Core
- Sample Application that demonstrate the usage of Driver:
- Driver expects the data (samples) to be in a specific format when requesting for an IO transfer based on below configurations
 - Single Serializer
 - Multiple Serializer
 - BurstMode
 - Multislot TDM/I2S
 - DIT

Audio Driver

- Device Driver for Audio on DSP Core
- Every Instance can support multiple codecs
- Sample Applications that demonstrate the usage of Driver
 - Sine Tone Generation
 - Loopback Application

ISS Capture

- Supports CAL based capture driver
- Supports capture via CSI2 and parallel interfaces
- Support for sensor OV10640 - Both CSI2 and Parallel
- Support for sensor AR0132 - Parallel interface
- Support for sensor AR0140 - Parallel interface
- Support for sensor IMX224 - CSI2 interface
- Supports On The Fly Capture Driver (OTF) based on CAL & ISP
- Supports Multiple virtual channel capture via CSI2 interface

ISS M2M

- Supports 12bit Bayer RAW Input
 - Supports YUV422/YUV420 SP (NV12) Input - Only in re-sizing mode
 - Supports YUV422/YUV420/Bayer RAW as output
 - Supports decompanding of companded input
 - Supports Wide Dynamic Range merge of two long and short exposure inputs and then converts output of WDR operation into YUV422/YUV420
 - Supports configuring all ISP Submodules
 - Supports Two outputs from Resizer
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ISS M2M SIMCOP

- Supports SIMCOP Sub-block of ISS
- LDC
 - Meshtable programmable for every frame
 - Output Data Format same as input format
 - Supported Input Formats: YUV422I & YUV420SP (NV12)
 - Supported Output Formats: YUV422I & YUV420SP (NV12)
- VTNF
 - Reference frame programmable for every frame
 - Supports YUV420 as input & output data format

AIC31 Driver

- Device Driver for AIC31 on DSP Core
- Appropriate interfaces to configure the initial values of gain, sample rate
- Interfaces to control the codec specific features like sample rate etc

Features

VIP Capture Driver Features

Feature	Supported	Tested on TDA2xx EVM	Tested on TDA2Ex EVM	Tested on TDA3xx EVM
12 Ports for TDA2xx, 4 Ports for TDA3xx/TDA2Ex	YES	YES	YES	YES
8-bit Embedded Sync (BT.656)	YES	YES	YES	NO
16-bit Embedded Sync (BT.1120)	YES	NO	NO	NO
24-bit Embedded Sync	YES	NO	NO	NO
8-bit Discrete Sync	YES	YES (only VSYNC/HSYNC mode)	YES (only VSYNC/HSYNC mode)	YES (only VSYNC/HSYNC mode)
16-bit Discrete Sync	YES	YES (only VSYNC/HSYNC mode)	YES (only VSYNC/HSYNC mode)	YES (only VSYNC/HSYNC mode)
24-bit Discrete Sync	YES	YES	NO	NO
8-bit YUV422 Input	YES	YES	YES	YES
16-bit YUV422 Input	YES	YES	YES	YES
24-bit YUV444 Input	YES	NO	NO	NO
16-bit RGB656 Input	YES	NO	NO	NO
24-bit RGB888 Input	YES	YES	NO	NO
12-bit RAW Input	YES	YES	YES	YES
16/24-bit RAW Input	YES	NO	NO	NO
YUV422I, YUV420SP, YUV422SP, RGB888, YUV444 and RAW output formats	YES	YES	YES	YES
Embedded Sync Multiplexed Modes	YES	YES	YES	NO
Ancillary (VBI) data capture	NO	NO	NO	NO
Bypass mode (RAW to RAW - no processing)	YES	NO	NO	NO

Inline SC Support (cropping, down scaling)	YES	YES	YES	YES
Inline CSC	YES	YES	YES	YES
Configurable VPDMA Line Limit Feature	YES	YES	YES	YES
Tiled (2D) output	NO	NO	NO	NO
Dual stream output (scaled/non-scaled)	YES	YES	YES	YES
Sub-frame based capture	YES	YES	YES	YES
Sub-frame based OTF use case	YES	YES	NO	YES
Re-packer	YES	YES	NO	NO
VIP Parser Crop	YES	YES	YES	YES
Buffer Capture Modes - drop frame, last frame repeat, circular frame repeat	YES	YES	YES	YES
Frame Drop IOCTL	YES	YES	YES	YES
Instance and channel status	YES	YES	YES	YES

VPE M2M Driver Features (Only on TDA2xx)

Feature	Supported	Tested on EVM
VPEI instance	YES	YES
YUV422I, YUV420SP, YUV422SP input formats	YES	YES
YUV422I, YUV420SP, YUV422SP, RGB888, YUV444 output formats	YES	YES
Tiler (2D) input/output	YES	YES
Tiler Rotation/Mirroring	YES	YES
SC (cropping, scaling)	YES	YES
DEI (bypass and in deinterlacing mode)	YES	YES
VC1 range mapping and reduction	NO	NO
DEI FMD mode	NO	NO
Multi-Handle	YES	YES
Multi-Channel	YES	YES
Lazy loading of SC coefficient	YES	YES
Slice based scaling (only horizontal slices)	YES	YES
Runtime parameter change	YES	YES

DSS Display Driver Features

Feature	Supported	Tested on TDA2xx EVM	Tested on TDA2Ex EVM	Tested on TDA3xx EVM
Video pipeline (Video 1,2,3)	YES	YES	YES	YES (Except Video 3)
Graphics pipeline (GRPX1)	YES	YES	YES	YES
Writeback pipeline	YES	YES	YES	YES
All LCD/DPI outputs	YES	YES (only DPI1 tested on EVM)	YES (only DPI1 tested on EVM)	YES (only DPI1 tested on EVM)
On-Chip HDMI 1.4 Support (Only on TDA2xx)	YES(only 1080P60,720P60,1080I60,480P resolutions in HDMI mode supported)	YES	YES	NA
HDMI 3D (Only on TDA2xx)	NO	NO	NO	NA
HDMI 36-bit RGB Color (Only on TDA2xx)	NO	NO	NO	NA
HDMI HDCP 1.4 (Only on TDA2xx)	NO	NO	NO	NA
HDMI Deep color mode (Only on TDA2xx)	NO	NO	NO	NA
8-bit Embedded Sync (BT.656)	YES	NO	NO	YES (with SIL9022)
16-bit Embedded Sync (BT.1120)	YES	NO	NO	YES (with SIL9022)
24-bit Discrete Sync	YES	YES	YES	YES
8/16 bit Discrete Sync	NO	NO	NO	NO
HDMI PLL	YES	YES	YES	NA
VIDEO PLL	YES	YES	YES	YES
YUV422I (YUYV),YUV422I (UYVY),YUV420SP, RGB888 input formats	YES	YES	YES	YES
YUV444 input formats	NO	NO	NO	NO
Tiler Memory (2D)	NO	NO	NO	NO
Tiler Rotation/Mirroring	NO	NO	NO	NO
VC1 Range Mapping (for Video Pipes)	NO	NO	NO	NO
Bypass mode	NO	NO	NO	NO
Inline SC	YES	YES	YES	YES
Inline CSC	YES	YES	YES	YES
Blending	YES	YES	YES	YES
Low-latency display (ability to queue frame to driver/hardware just before VSYNC)	YES	YES	YES	YES
Interlaced scan format	YES	YES	YES	YES
Fields merged and separated interlaced buffers	YES	YES	YES	YES
On-Chip SD-Display NTSC and PAL	YES	NA	NA	YES

UART Driver Features

Feature	Supported	Tested on TDA2xx EVM	Tested on TDA2Ex EVM	Tested on TDA3xx EVM
Single instance	YES	YES	YES	YES
Multi instance and Re-Entrant	YES	YES	YES	YES
Each Instance as Transmitter and / or receiver	YES	YES	YES	YES
DMA Mode Of Operation	YES	YES	YES	YES
POLLED Mode Of Operation	YES	YES	YES	YES
INTERRUPT Mode Of Operation	YES	YES	YES	YES

I2C Driver Features

Feature	Supported	Tested on TDA2xx EVM	Tested on TDA2Ex EVM	Tested on TDA3xx EVM
Single instance	YES	YES	YES	YES
Multi instance and Re-Entrant	YES	YES	YES	YES
Slave Device Probe IOCTL	YES	YES	YES	YES
Each Instance as Master Transmitter	YES	YES	YES	YES
DMA Mode Of Operation	NO	NO	NO	NO
POLLED Mode Of Operation	YES	YES	YES	YES
INTERRUPT Mode Of Operation	YES	YES	YES	YES

McSPI Driver Features

Feature	Supported	Tested on TDA2xx	Tested on TDA2Ex	Tested on TDA3xx	Tested on TI814x
Single instance	YES	YES	YES	YES	YES
Multi instance and Re-Entrant	YES	YES	YES	YES	YES
Each Instance as Transmitter and / or receiver	YES	YES	YES	YES	YES
DMA Mode Of Operation	YES	YES	YES	YES	YES
POLLED Mode Of Operation	YES	YES	YES	YES	NO
INTERRUPT Mode Of Operation	YES	YES	YES	YES	NO

Audio Driver Features

Feature	Supported	Tested on TDA2xx	Tested on TDA2Ex	Tested on TI814x
Multi instance and Re-Entrant	YES	YES	YES	YES
Each Instance as Transmitter and / or receiver of an audio device	YES	YES	YES	YES
DMA Mode Of Operation	YES	YES	YES	YES
POLLED Mode Of Operation	NO	NO	NO	NO
INTERRUPT Mode Of Operation	NO	NO	NO	NO

McASP Driver Features

Feature	Supported	Tested on TDA2xx	Tested on TDA2Ex	Tested on TI814x
Single instance	YES	YES	YES	YES
Multi instance and Re-Entrant	YES	YES	YES	YES
Each Instance as Transmitter and / or receiver	YES	YES	YES	YES
Multiple Data Formats	YES	NO	NO	NO
Configurations to operate: multi-slot TDM, I2S, DSP	YES	YES	YES	YES
Configurations to operate: DIT (S/PDIF)	YES	NO	NO	NO
Desired data (such as NULL tone), when idle Transmission Mechanism.	YES	YES	YES	YES
Explicit control of PIN directions for High Clock, Bit Clock and Frame Sync PINS.	YES	YES	YES	YES
DMA Mode Of Operation	YES	YES	YES	YES
POLLED Mode Of Operation	NO	NO	NO	NO
INTERRUPT Mode Of Operation	NO	NO	NO	NO

AIC31 Codec Driver Features

Feature	Supported	Tested
Multi instance and Re-Entrant	YES	YES
Independent Configuration of Transmitter and receive of an audio device with and multiple audio codecs	YES	YES
Interfaces to control the codec specific features like sample rate etc	YES	YES
Appropriate interfaces to configure the initial values of gain, sample rate etc	YES	YES

Driver Maturity

Driver Maturity

Driver	TDA2xx	TDA2Ex	TDA3xx	TI814x
VIP Capture	Beta 1.0	Beta 1.0	Beta 1.0	NA
VPE M2M	Beta 1.0	Beta 1.0	Beta 1.0	NA
DSS Display	Beta 1.0	Beta 1.0	Beta 1.0	NA
UART	Beta 1.0	Beta 1.0	Beta 1.0	Beta 1.0
McSPI	Beta 1.0	Beta 1.0	Beta 1.0	Beta 1.0
I2C	Beta 1.0	Beta 1.0	Beta 1.0	Beta 1.0
McASP	Beta 1.0	Beta 1.0	Beta 1.0	Beta 1.0
ISS Capture	NA	NA	Alpha	NA
ISS M2M ISP	NA	NA	Alpha	NA
ISS SIMCOP	NA	NA	Alpha	NA

Supported/Validated Examples

Supported/Validated Examples

Examples	Supported	Validated on TDA2xx EVM	Validated on TDA2Ex EVM	Validated on TDA3xx EVM
VIP Capture	YES	YES	YES	YES
VIP Sub-frame	YES	YES	YES	YES
VPE M2M	YES	YES	YES	NA
DSS Display	YES	YES	YES	YES
Loopback	YES	YES	YES	YES
UART ECHO	YES	YES	YES	YES
MCSPI LOOPBACK	YES	YES	YES	YES
MCSPI MASTER SLAVE SPI1 to SPI2	YES	YES	YES	YES
MCSPI PERFORMANCE APP	YES	YES	YES	YES
I2C ON Board LED Blink	YES	YES	YES	YES
Audio Sinetone	YES	YES	YES	NA
Audio Loopback Application	YES	YES	YES	NA
ISS Capture CSI2	YES	NA	NA	YES
ISS Capture Parallel	YES	NA	NA	YES
ISS M2M WDR	YES	NA	NA	YES
ISS M2M ISP	YES	NA	NA	YES
ISS M2M SIMCOP	YES	NA	NA	YES

- Examples could be found at \$BSP_Install_Dir\examples\

What is Not Supported

- Checking for most of the input parameters for out of range and invalid values is not done
- Scaler lazy loading and user coefficient loading are not supported in VIP capture driver
- McASP does not support Interrupt/Polled mode
- I2C is not supported in DMA mode
- Display writeback driver does not support mem to mem writeback mode.

Fixed in this Release

Fixed in this Release

ID	Headline	Module	Remarks
OMAPS00320415	Issues with Network and AVB usecase bring up on TDA2EX	VIP Capture Driver	NA
OMAPS00322620	[VIP] IOCTL_VPS_CAPT_SET_VIP_MAX_SIZE IOCTL returns error for TDA3xx	VIP Capture Driver	NA
OMAPS00324145	[Loopback App] Frame repeats observed for TDA3xx OV to NTSC display option	VIP Capture Driver	NA
OMAPS00323021	DSS WB driver picks the input from LCD1 bender - This should be configurable	DSS WB Driver	NA
OMAPS00291957	Display :- Low latency display is not supported for Overlays other than LCD1.	DSS Display Driver	NA
OMAPS00321488	[DSS] CPR coefficients used in TDA2ex for BT656 mode is not proper	DSS Display Driver	NA
OMAPS00322004	[DSS] LCD2 Blender gives half the FPS when two buffers are used between app and driver	DSS Display Driver	NA
OMAPS00305622	[I2C] I2C read to a known slave fails after I2C write to absent slave timeout	I2C Driver	NA
OMAPS00319806	[BSP_I2C] control does not return from GIO_submit of I2C	I2C Driver	NA
OMAPS00323928	TDA3xx Single channel and LVDS usecase hangs	I2C Driver	NA
OMAPS00307881	[Platform] "1.17 DPLL Controller Sticks When Left Clock Requests Are Removed" Errata workaround needs to be implemented	Platform	NA
OMAPS00321402	[Platform] Platform init hangs for TDA2ex on custom board	Platform	NA
OMAPS00321420	[Platform] Low jitter Video PLL M/N values should be used	Platform	NA
OMAPS00312092	[Tda3xx] - Probe to address 0x18 results in I2C Bus busy	I2C Driver	Rejected. 0x18 is the address of the Touch Screen Controller which can't be just probed without reading (as per spec)

Known Issues / Limitations

Known Issues

ID	Headline	Module	Workaround in this release
OMAPS00296239	Display: VID3 pipeline output results in black output when zorderEnable is disabled	Display Driver	Enable Z-order and assign proper order
OMAPS00297821	[Display] - BT656 display mode not working	Display Driver	This is DSS IP Limitation in TDA2xx platform
OMAPS00313660	Display: HDMI Output is shifted left by few pixels for 1080P60 Mode in TDA3xx EVM	Display Driver	None
OMAPS00313660	[Display]-sil9022a off chip HDMI display is shifted right by 3 pixels	Display Driver	None
OMAPS00314110	[DSS-SDDAC] - SDDAC display results in SyncLost	Display Driver	None
OMAPS00323796	Display WB - The entire image is not proper when scalar is enabled	Display Driver	Issue not seen when overlay width is less than 1280. For width greater than 1280 writeback frame without scaling.
OMAPS00324337	DSS : DSS Display results in 67fps for 480P60 mode When TDM is enabled	Display Driver	None
OMAPS00324341	DSS WriteBack : Display freezes when we enable DSS writeback for last quadrant and last ten half line window Region	Display Driver	None
OMAPS00324342	DSS Writeback : Alternate frames of DSS Writeback output results in blank frames when scalar is enabled.	Display Driver	None
OMAPS00324409	[display DSS] driver configures 287 lines for PAL insted of 288, per field.	Display Driver	None
OMAPS00324410	[display DSS] dss writeback writes one line less in case of interlaced display.	Display Driver	None
OMAPS00324412	DSS WB capture - Not working properly when switching from interlace to progressive without power cycle/reset the EVM	Display Driver	None
OMAPS00324461	Display write back is not working properly for interlace mode of displays. 1. DSS WB will not work with SBL load. 2. DSS WB will not work with CCS load afetr system reset & warm reset. 3. When CCS is halt and run, sometimes writeback is not storing 1 field.	Display Driver	None
OMAPS00306536	No Signal warning in the HDMI display with some TV	HDMI Driver	None
OMAPS00308882	[HDMI] - Fields are swapped for 1080I display	HDMI Driver	None
OMAPS00315883	[Display] HDMI testcases results in failed to start phy	HDMI Driver	None
OMAPS00319353	[HDMI] DSS driver prints error log for HDMI output on TDA2xx and TDA2Ex	HDMI Driver	None. Even though error print comes, display is fine.

OMAPS00301476	[LVDS] Re-run of the any LVDS option results in I2C issues	VIP Capture Driver	Disable I2C probe all at the start of device init by setting isI2cProbingReq of Bsp_DeviceInitParams init parameter to FALSE
OMAPS00301599	Random FVID2_TIMEOUT error during sensor configuration in LVDS use case	VIP Capture Driver	After debugging, this is root caused because of Deserializer watchdog time out of 500ms for the control channel to the serializer. This needs to be debugged further with the EVM team
OMAPS00319344	[VIP]TVP5158 Capture: Only single field is getting captured in Line Mux Mode	VIP Capture Driver	Use pixel mux mode
OMAPS00319355	Observed few significant frame repeats for SII9127 option of Loopback sample application on Tda2ex	VIP Capture Driver	None
OMAPS00319936	VIP YUV444 Input to YUV420SP not working	VIP Capture Driver	None
OMAPS00321570	[LVDS] Wrong pull-down to select the de-serilizer mode in LVDS multi-deserilizer setup	VIP Capture Driver	None
OMAPS00324339	Loopback : Loopback test for MULDESCAM1 and MULDESCAM6 output results in Blank output	VIP Capture Driver	None
OMAPS00294864	[TDA2xx] McASP Slave Mode Testing for audio codec application is not supported	McASP Driver	This is TDA2xx EVM Limitation
OMAPS00318479	[McASP] GPIO_create causes DSP abort on TDA2Ex	McASP Driver	None
OMAPS00324344	McAsp Audio Loopback and Audio Sinetone application results in crash on M4	McASP Driver	None
OMAPS00312940	McSPI performance app gives data mismatch for spi3 and spi4 in TDA3xx EVM	McSPI Driver	None
OMAPS00319339	[ISS] BSP Demo applications (Capture & Loopback) fails to configure OV10640 Parallel sensor in release mode.	OV10640 Sensor Driver	Use debug build or change the I2C frequency to 100KHz.
OMAPS00324039	UB960 De Initialization dosent work	Sensor Driver	None

Common

- None

VIP Capture Driver

- In case of ADV7611 HDMI in capture, first few frames in the first run after power cycle might have artifacts. This is because when the ADV7611 is configured, it programs the internal EDID for 1080p60 and does Hot Plug Assert (HPA). When this happens the video source will read the EDID and reconfigure itself for the new timing. At this time the video might be corrupted.
- 24-bit RAW capture - No support in EVM
- RGB888 input to VIP - No support in EVM
- Various discrete sync modes except HSYNC/VSYSN mode - No support in EVM
- In case of dual output streams from same capture source, below limitations applies
 - YUV422SP output should always be stream 0 (first stream)

- For YUV422I scaled and YUV420SP non-scaled outputs, YUV422I scaled output should always be stream 0 (first stream)
- Scaled outputs on both the streams are not supported

VPE M2M Driver

- None

DSS Display Driver

- None

Serial Drivers

- When UART driver's configuration is in interrupt mode and when the RX buffer size is bigger than FIFO size and when UART driver receives data of same length as FIFO size, the `GIO_read` function does not return until it receives next data due to H/W specification as given below. To workaround this limitation, RX FIFO size and RX buffer size needs to be configured as same length.
- As per the UART IP (given in TRM section 24.3.4.8.1.3.7.1 Time-out Counter), when there is a break in the continuous UART character received, it will timeout so that the driver can read the last bytes out of the UART FIFO when the bytes received is less than the FIFO threshold. In the above case, since the user has given (Receive data size == FIFO threshold), the driver gets the FIFO threshold interrupt instead of timeout interrupt. And the driver reads this out of the FIFO before the UART IP times out. Since this is not a timeout, the driver will wait for few more bytes (since RX size is greater than FIFO size) and doesn't return with timeout.
- UART Baud rates greater than 115200 are not supported due to high error percentage observed for baud rates greater than 115200
- UART single byte transfer is supported in Polled Mode and not in DMA Mode
- For TDA2xx, A15 needs to be running while loading and running applications on DSP Core
- Junk characters are observed in the UART terminal whenever we do reset on the board. This is present in TDA3xx-EVM only.

ISS Driver

OV10640 Sensor Drivers

- On some TDA3xx EVMs, I2C register write for the OV10640 sensor is failing at 400KHz I2C frequency. In this case, change the I2C frequency to 100KHz in `gBoardTda3xxI2cInstData[1].busClkKHz` in the file `bspdrivers_\src\board\src\bsp_boardTda3xx.c` file.

```
diff --git a/bspdrivers_/src/boards/src/bsp_boardTda3xx.c
b/bspdrivers_/src/boards/src/bsp_boardTda3xx.c
517306b..ba76593 100755 ---
a/bspdrivers_/src/boards/src/bsp_boardTda3xx.c +++
b/bspdrivers_/src/boards/src/bsp_boardTda3xx.c @@ -281,7 +281,7 @@ static
Bsp_BoardI2cInstData gBoardTda3xxI2cInstData[] = BSP_DEVICE_I2C_INST_ID_1, /*
instId */ SOC_I2C2_BASE, /* baseAddr */ CSL_INTC_EVENTID_I2CINT2, /* intNum */
- 400U /* busClkKHz */ + 100U /* busClkKHz */ } };
```

- I2C transaction fails on address 0x33 for some of the OV10640 Parallel sensor. In this case, change the I2C address to 0x31 for the macro `BOARD_OV10640_I2C_ADDR_CPI` in the file `src/boards/src/bsp_boardPriv.h`
- Please refer "User Guide" that came with this release, for EVM modifications required for I2C

Sensor Tuning

- Tuning the sensor is beyond the scope of this product. An higher level software (such as SDK) will have to tune sensor for the required quality.
- Quality of the sensor (and/or processing) should not be judged based on the ISS demo applications.

Validation Information

- This release is validated on TDA2xx, TDA2Ex and TDA3xx EVM for the above mentioned components

Technical Support and Product Updates

For further information or to report any problems, contact <http://e2e.ti.com> or <http://community.ti.com> or <http://support.ti.com>.

Article Sources and Contributors

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