

Addressing Mode	Format	M	Xn
Data register	Dn	0 0 0	reg
Address register	An	0 0 1	reg
Address	(An)	0 1 0	reg
Address with Postincrement	(An)+	0 1 1	reg
Address with Predecrement	-(An)	1 0 0	reg
Address with Displacement	(d ₁₆ , An)	1 0 1	reg
Address with Index	(d ₅ , An, Xn)	1 1 0	reg
Program Counter with Displacement	(d ₁₆ , PC)	1 1 1	0 1 0
Program Counter with Index	(d ₅ , PC, Xn)	1 1 1	0 1 1
Absolute Short	(xxx).W	1 1 1	0 0 0
Absolute Long	(xxx).L	1 1 1	0 0 1
Immediate	#imm	1 1 1	1 0 0

Operation Size	Suffix	S	S	S
Byte	.b	0 0	0	1
Word	.w	0 1	0	1
Long	.l	1 0	1	1

Brief Extension Word				
M	Xn	S	0 0 0	Displacement

Condition	Mnemonic	Cond
True	T	0 0 0 0
False	F	0 0 0 1
Higher	HI	0 0 1 0
Lower or Same	LS	0 0 1 1
Carry Clear	CC	0 1 0 0
Carry Set	CS	0 1 0 1
Not Equal	NE	0 1 1 0
Equal	EQ	0 1 1 1
Overflow Clear	VC	1 0 0 0
Overflow Set	VS	1 0 0 1
Plus	PL	1 0 1 0
Minus	MI	1 0 1 1
Greater or Equal	GE	1 1 0 0
Less Than	LT	1 1 0 1
Greater Than	GT	1 1 1 0
Less or Equal	LE	1 1 1 1

Direction	D	D
Dn ← <ea> → Dn	0	0
<ea> ← Dn → <ea>	1	1

Direction	D	D
Register to memory	0	1
Memory to register	1	0

Direction	d	D
Right	R	0
Left	L	1

Data Type	Letter
Immediate	I
Bit Index	N
Displacement	D
Optional Displacement	D
Register List Mask	M

Data Size	Letter
Byte	B
Word	W
Long	L
Any	