

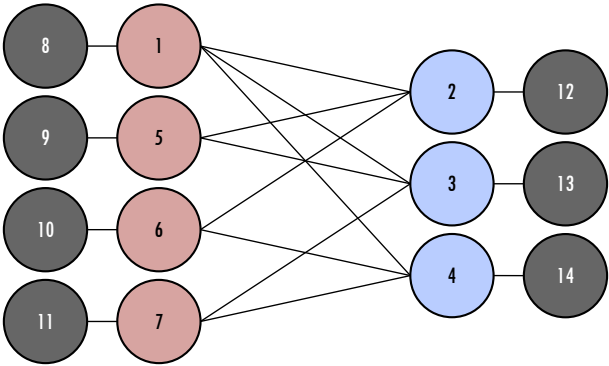
Manual validation summary – Steane

This document shows outputs by the algorithm for the declared input ZX graph.

The sample of outcomes is illustrative, not comprehensive. Random choices are involved. The range of possible outcomes is, while finite, potentially larger than shown here.

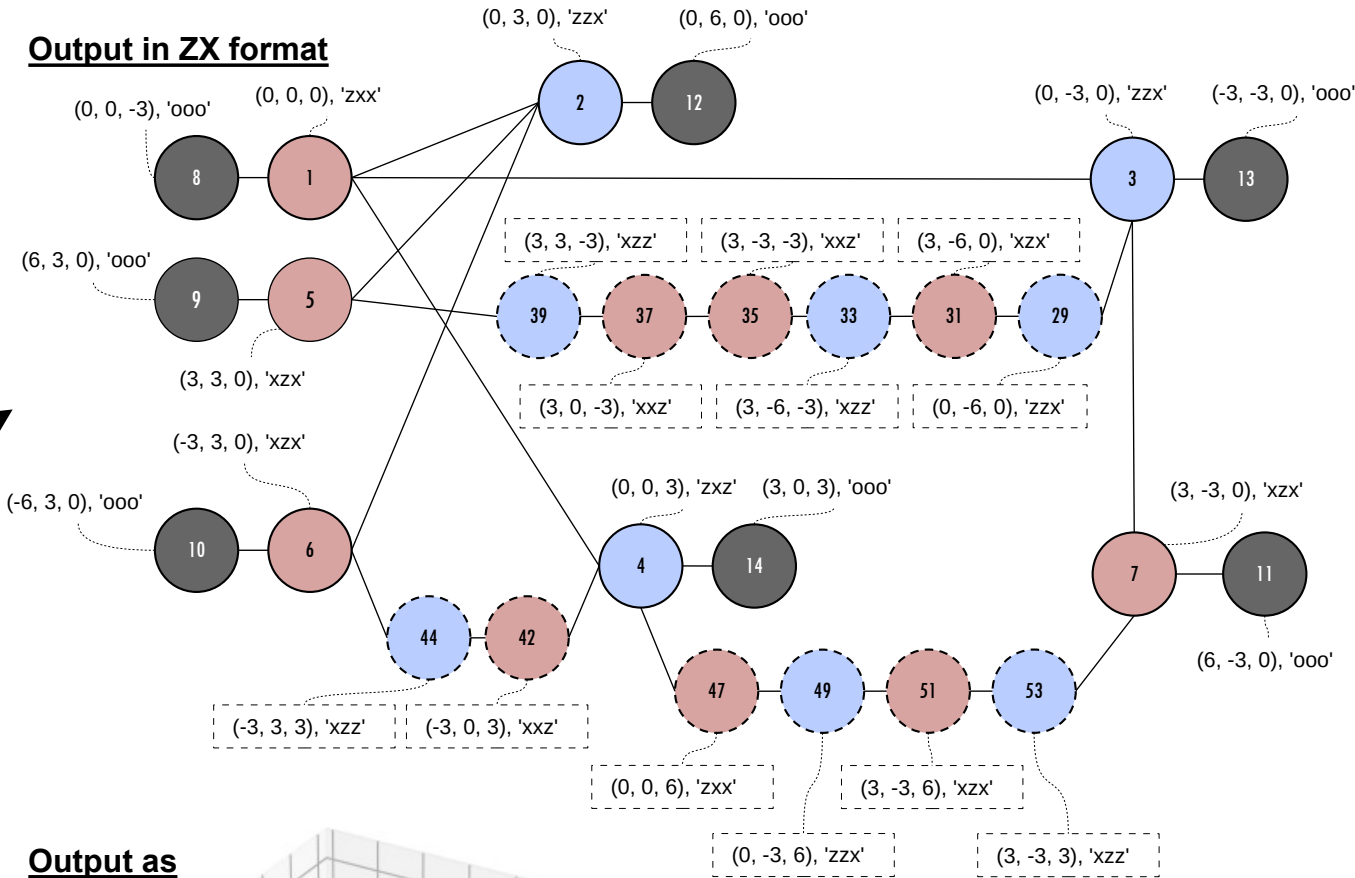
Inputs are not meant to reflect actual circuits. They are conceived to test the diversity of operations the algorithm needs to take overall.

Input ZX graph

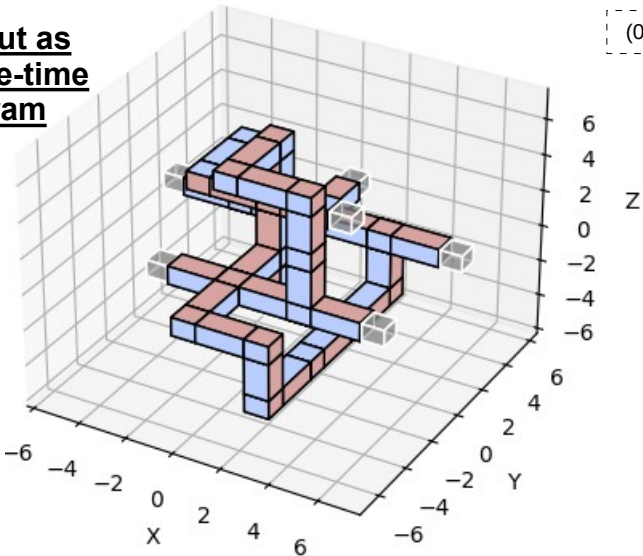


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Output in ZX format



Output as space-time diagram



- X
- Y
- Simple edge
- Hadamard edge
- Nodes in original ZX-graph
- Additional nodes needed to clear 3D paths