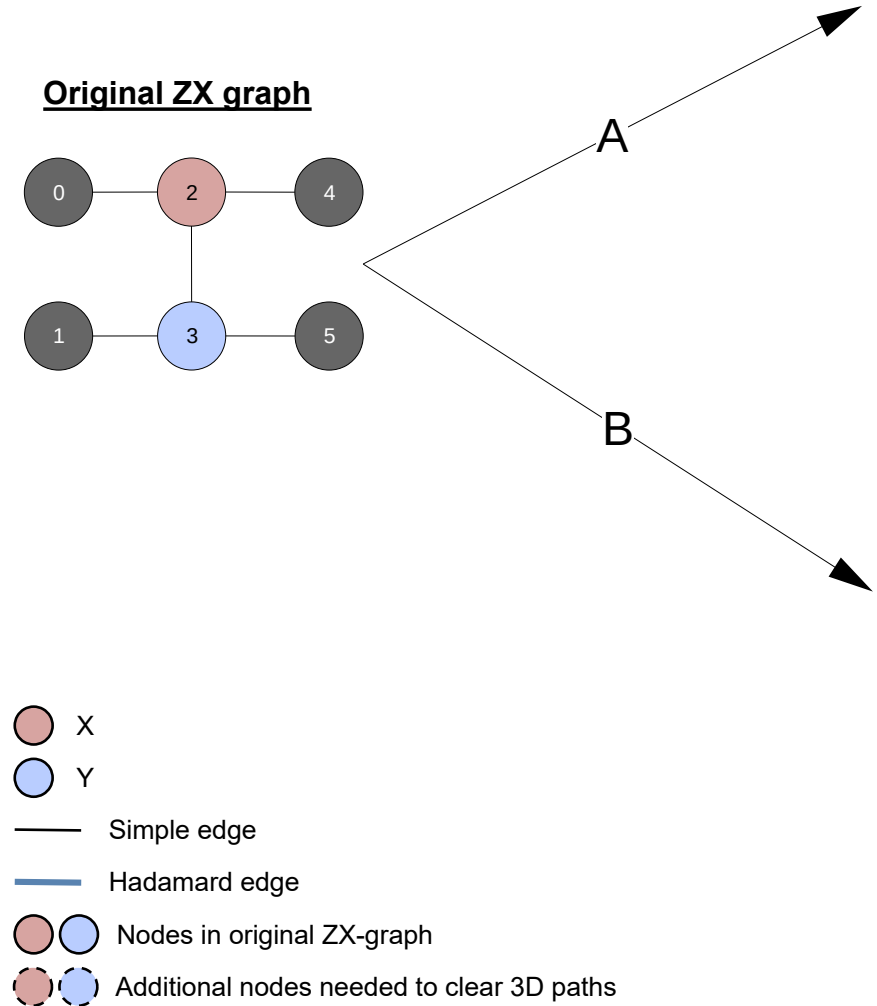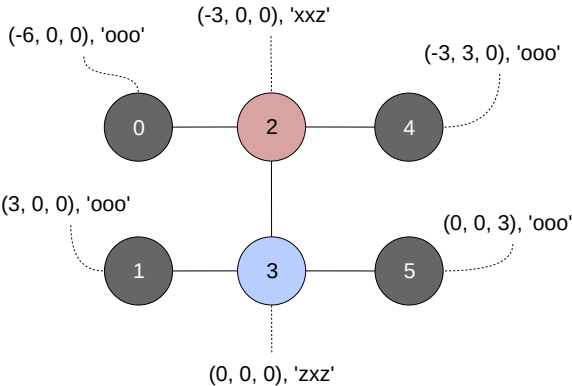# Manual validation summary – CNOT

This document shows outputs by the algorithm for the declared input ZX graph.

The sample of outcomes is illustrative, not comprehensive. Random choices are involved. The range of possible outcomes is, while finite, potentially larger than shown here.
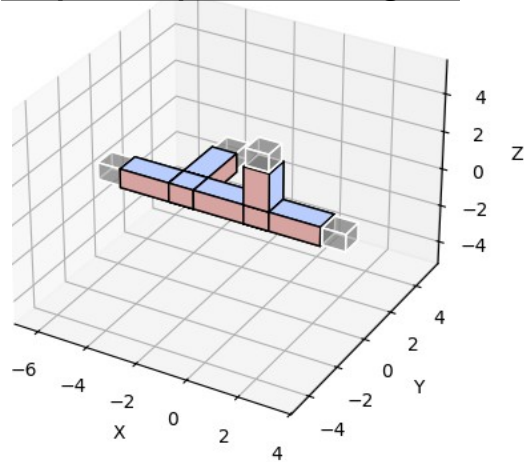
Inputs are not meant to reflect actual circuits. They are conceived to test the diversity of operations the algorithm needs to take overall.
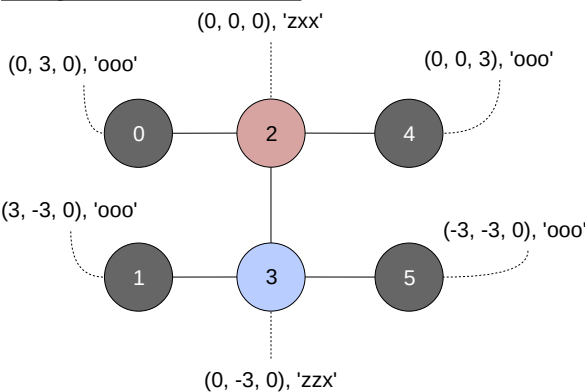
Original ZX graph

A

B

Output in ZX format

(-6, 0, 0), 'ooo'
(-3, 0, 0), 'xxz'
(-3, 3, 0), 'ooo'
(3, 0, 0), 'ooo'
(0, 0, 3), 'ooo'
(0, 0, 0), 'zxz'

Output as space-time diagram

Output in ZX format

(0, 0, 0), 'zxx'
(0, 3, 0), 'ooo'
(0, 0, 3), 'ooo'
(3, -3, 0), 'ooo'
(-3, -3, 0), 'ooo'
(0, -3, 0), 'zzx'

Output as space-time diagram

X

Y

—— Simple edge

—— Hadamard edge

Nodes in original ZX-graph

Additional nodes needed to clear 3D paths