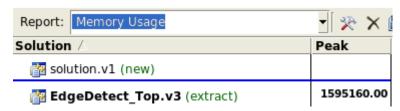
Report: General 🔻 🔀 🛍 📺 🚎										
Solution A	Latency	Latency	Throug	Throug	Slack	Total Area				
🚰 solution.v1 (new)										
EdgeDetect_Top.v3 (extract)	7	70.00	6	60.00	6.48	1613.00				

Report: Run Time												
Solution /	Total	Analyze	Compile	Libraries	Assembly	Loops	Memories	Cluster	Architect	Allocate	Schedule	D
solution.v1 (new)												
	39.82	6.79	6.54	0.78	0.94	2.63	1.52	0.10	1.32	0.83	4.54	



Report: Timing	<b>▼</b>   🎠 × [	<b>ll</b> 🏢 🖪										
Solution /	Latency	Latency	Throug	Throug	Iterations	Total Cy	Logical I	Max Inp	Logical	Max Out	Logical	Ma
solution.vl (new)												Ш
EdgeDetect_Top.v3 (extract)	7	70.00	6	60.00			3.24	3.24	2.70	3.36	3.52	Γ
Report: Area Score												
Solution /	Registers	MUX	Functional	Logic	Memory	FSM Reg	FSM Comb	FSM	Datapath	Total Reg	Total Area	Ro
Solution.v1 (new)												
EdgeDetect_Top.v3 (extract)	0.00	587.00	401.00	619.00	0.00	0.00	6.00	6.00	1607.00	0.00	1613.00	Г