

solution.v1 (new)						
top.v2 (extract)	9	90.00	10	100.00	7.59	2837.16
top.v7 (extract)	2	20.00	1	10.00	7.97	6887.11

```

VSIM 2> run -all
# SCVerify intercepting C++ function 'top::run' for RTL block 'top'
#           DUT instance '0x2aaab4b03b80'
# Info: HW reset: TLS_rst active @ 0 s
# Output =  0
# Output = -1
# Output = -2
# Info: Execution of user-supplied C++ testbench 'main()' has completed with exit code = 0
#
# Info: Collecting data completed
#   captured 10 values of din
#   captured 10 values of coeffs
#   captured 3 values of dout
# Info: scverify_top/user_tb: Simulation completed
#
# Checking results
# 'dout'
#   capture count      = 3
#   comparison count   = 3
#   ignore count       = 0
#   error count        = 0
#   stuck in dut fifo  = 0
#   stuck in golden fifo = 0
#
# Info: scverify_top/user_tb: Simulation PASSED @ 146 ns
# ** Note: (vsim-6574) SystemC simulation stopped by user.
# 1

```

