```
capture count
comparison count
ignore count
           stuck in dut fifo
stuck in golden fifo
      'crc32_dat_out'
           capture count
comparison count
ignore count
           error count = 0
stuck in dut fifo = 0
stuck in golden fifo = 0
       dat_out_pix
                                             = 57600
           capture count
      capture count = 57
comparison count = 57
ignore count = 0
error count = 0
stuck in dut fifo = 0
stuck in golden fifo = 0
'dat_out_sof'
                                             = 57600
           capture count
           capture count = 57
ignore count = 67
ignore count = 0
error count = 0
stuck in dut fifo = 0
stuck in golden fifo = 0
       'dat_out_eol'
                                           = 57600
           capture count
          capture count = 5:
comparison count = 5:
ignore count = 0
error count = 0
stuck in dut fifo = 0
stuck in golden fifo = 0
                                                       = 57600
= 0
# Info: scverify_top/user_tb: Simulation PASSED @ 581286 ns
# ** Note: (vsim-6574) SystemC simulation stopped by user.
# 1
```

