DEC IF EXE MEM WB -Lid_bus reg_val reg_addr reg_cop req_wdata req_size -Mux_bus-L1D cache -ack_val-M cntr 1.1 ack_rdata -condcond 1.0 l_ack_val operation -operation--inst--result-PC -takenn-ME and order -ack_rdata -type-**—19:15**– -src1--result--24:20 --imm--src2-—imm-Reg_file -imm— -W_data-XS -Data_in-—-рс--src2-2.1 -W_data--Pc + 4 2.2 -Pc+4----Pc+4---Pc+4--imm-L1I cache -addr----Pc+4---Instr[31:6]--SX_20/12 Data_in -imm--Pc+4inst type Enb/flash рс -src1--l_req_val_□ ack val Enb/flash Enb/flash Enb/flash -i_ack_val— Hz_ctrl