## **Patterns**

- **Functional** 
  - -1,112,583 received data and 3,499,997 transmitted data
- **Stuck** at
  - **→33** patterns (2108 test cycles)
- Transition delay
  - → 40 patterns (2702 test cycles)
- Path delay
  - 9 patterns (434 test cycles)

# RS232-T1600

### Trojan Description

Trojan trigger is a sequential comparator whose trigger input probability is 5.77e-09. Whenever Trojan gets triggered, its payload gains control over xmit\_doneH and rec\_readyH primary output signals.

### Trojan Taxonomy

Insertion phase: Design

**→**Abstraction level: Gate-level

Activation mechanism: Internally conditionally triggered

**Effects:** Change functionality

AND2X1 U304(.A(iCTRL)..B(rec readvH temp)..Y(rec readvH)):

Physical characteristics: Functional

SDFFSRX1 iDatasend\_reg\_1 (.D(iXMIT\_N27), .SI(iXMIT\_bitCell\_cntrH\_0\_), .SE(test\_se), .CK(sys\_clk), .SN(1'b1), .RN(n264), .Q(n400));
SDFFSRX1 iDatasend\_reg\_2 (.D(n190), .SI(iXMIT\_state\_2\_), .SE(test\_se), .CK(sys\_clk), .SN(1'b1), .RN(n266), .Q(n401));
NAND4X1 U293(,A(iXMIT\_N46),B(iXMIT\_N45),.C(iXMIT\_N44),D(iXMIT\_N29),.Y(iXMIT\_N\_CTRL\_1\_));
NAND4X1 U294(.A(iXMIT\_N28),.B(iXMIT\_N27),.C(iXMIT\_N26),.D(iXMIT\_N25),.Y(iXMIT\_N\_CTRL\_2\_);
NAND4X1 U295(.A(iXMIT\_N24),B(iXMIT\_xmit\_ShiftRegH\_7\_),C(iXMIT\_xmit\_ShiftRegH\_6\_),D(iXMIT\_xmit\_ShiftRegH\_5\_),.Y(iXMIT\_xmit\_CTRL));
OR4X1 U296(.A(i'b0),.B(iXMIT\_N\_CTRL\_1\_),C(iXMIT\_N\_CTRL\_2\_),D(iXMIT\_xmit\_CTRL),Y(iXMIT\_CRTL));
NAND4X1 U297(.A(iRECEIVER\_next\_state\_2\_),B(iRECEIVER\_state\_0\_),C(iRECEIVER\_state\_1\_),D(iRECEIVER\_state\_2\_),Y(iRECEIVER\_state\_CTRL));
NAND2X1 U300(.A(n400),B(n401),Y(iRECEIVER\_bitCell\_CTRL\_));
OR2X1 U301(.A(iKECEIVER\_state\_CTRL),B(iRECEIVER\_N\_CTRL\_1\_),Y(iCTRL));
AND2X1 U303(.A(iXMIT\_CRTL),B(iRECEIVER\_CTRL),Y(iCTRL));



# RS232-T1600

#### **Functional**

Trojan activation probability	The number of Transmitted	The number of Received	Trojan activation	Payload Change	Simulation Length (No. Clock)	Length of each Receive/Transmission
5.77e-09	3 499 997	1 112 583	0	0	168 000 000	176/150

#### **Structural**

Circuit activity in Trojan-free	Circuit activity in Trojan_inserted	Trojan inputs activity	Activity inTrojan circuit	Trojan Triggered	Payloadchanged	Stuck-at unmatched
70 667	73 193	12 123	52	0	394	0

**Transition delay** 

Circuit activity in Trojan-free	Circuit activity in	Trojan inputs activity	Activity inTrojan circuit	Trojan Triggered	Payloadchanged	Transition unmatched
94 436	97 898	16 042	128	0	537	0

**Path delay** 

Circuit activity in Trojan-free	Circuit activity in Trojan-inserted	Trojan inputs activity	Activity inTrojan circuit	Trojan Triggered	Payloadchanged	Transition unmatched
35 963	37 381	5 592	68	0	194	0

