Bo Tian | Résumé

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Education and Skills

○ Bachelor's degree in Computer Science, Tsinghua University. (Aug. 2012 - July 2016 Expected)
○ Programming languages: C++, Python. Platform: Linux, OS X. Tools: VIM, Git.
○ Major GPA: 87.9 out of 100, Top 35%. *Operating Systems* ranked the 1st out of 132 students.

Research Publication

Learning Tag Embeddings and Tag-specific Composition Functions in RNN

○ Published in the best *NLP* (Natural Language Processing) conference *ACL2015*, second author.
○ Proposed a novel variant of *Recursive Neural Network* with 10x faster speed for sentiment analysis.

Internship Experience

Google Mountain View, Search Actions Group (July 2015 - Oct. 2015)

○ Designed and developed an integration testing framework for Google's new voice action system.

Google New York City, Speech Group (June 2014 - Sept. 2014)

○ Reduced recognition error rate on Question Answering from 30% to 10% in production.
○ Carried out large scale speech recognition experiments and analysis with MapReduce.

Tsinghua National Laboratory for Information Science and Technology (Feb. 2014 - Now)

○ Research topic: Deep Neural Networks for Sentiment Analysis and Text Classification.

LingoChamp Inc. A start-up company with 10M+ users currently (June 2013 - June 2014)

○ The first Research Intern, built up the initial speech evaluation infrastructure from scratch.

Algorithm Competitions

○ National Olympiad in Informatics in China, Gold Medal, 2011
○ Tsinghua Artificial Intelligence Contest, First Prize, 2012
○ Tencent Hackathon, Rank 3rd in North China, 2013

Projects

Porting of Linux Ethernet Driver E1000 to Ucore OS

○ Independently designed and implemented a Linux Kernel Service Adapter to port Linux's Kernel Driver without touching driver code to the Ucore teaching OS (like XV6) in Tsinghua University.

iTravel

○ An Android app that helps to design a train travel plan according to time and cost.
○ Original algorithms on this shortest-path-like problem which significantly improve the speed.

FPGA WordPad

○ A text editor built on FPGA board with keyboard as input and VGA display port as output.
○ Designed a tiny customized processor with data management unit for text processing.