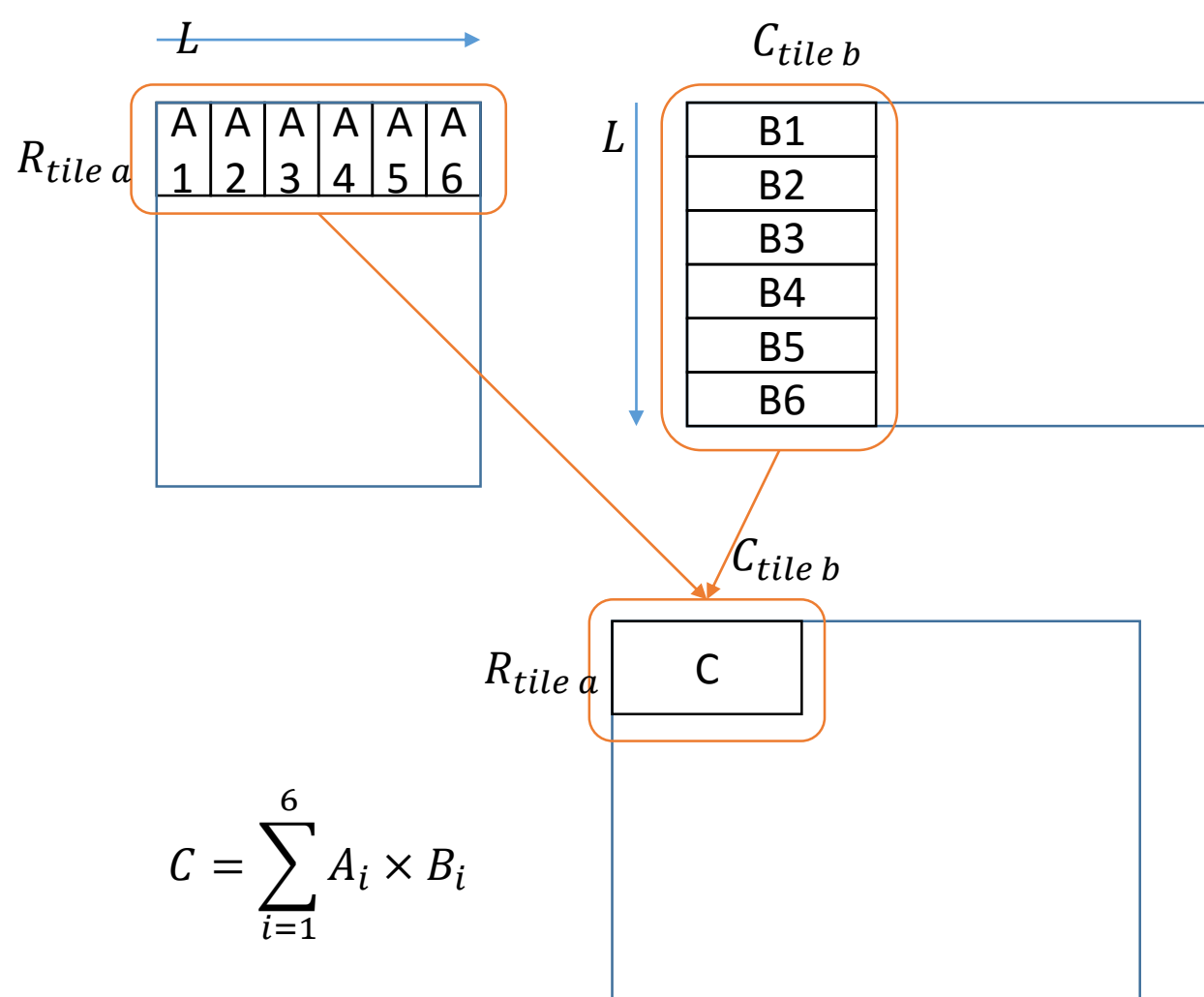


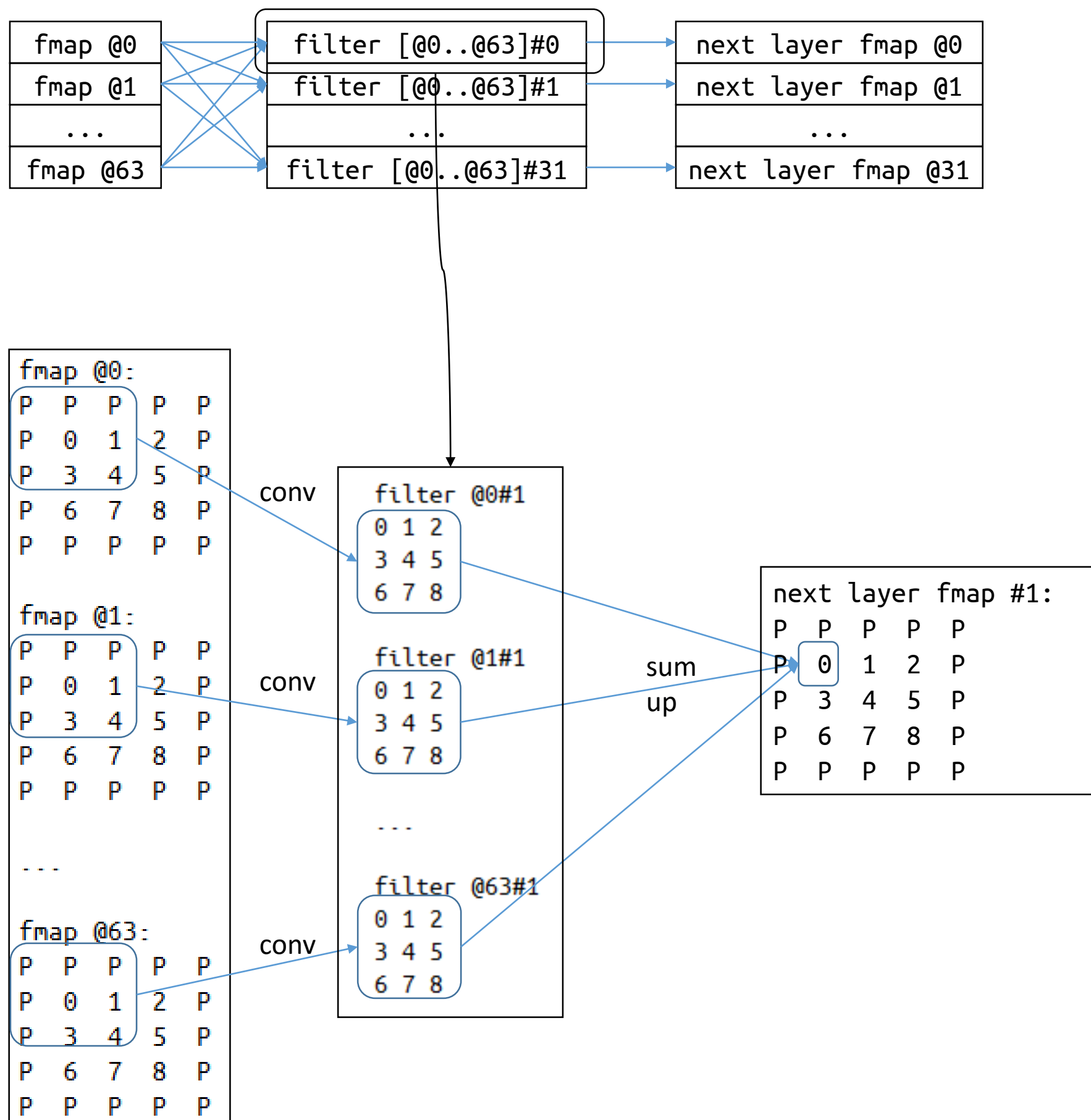
GEMM on FPGA

Tiling for data locality

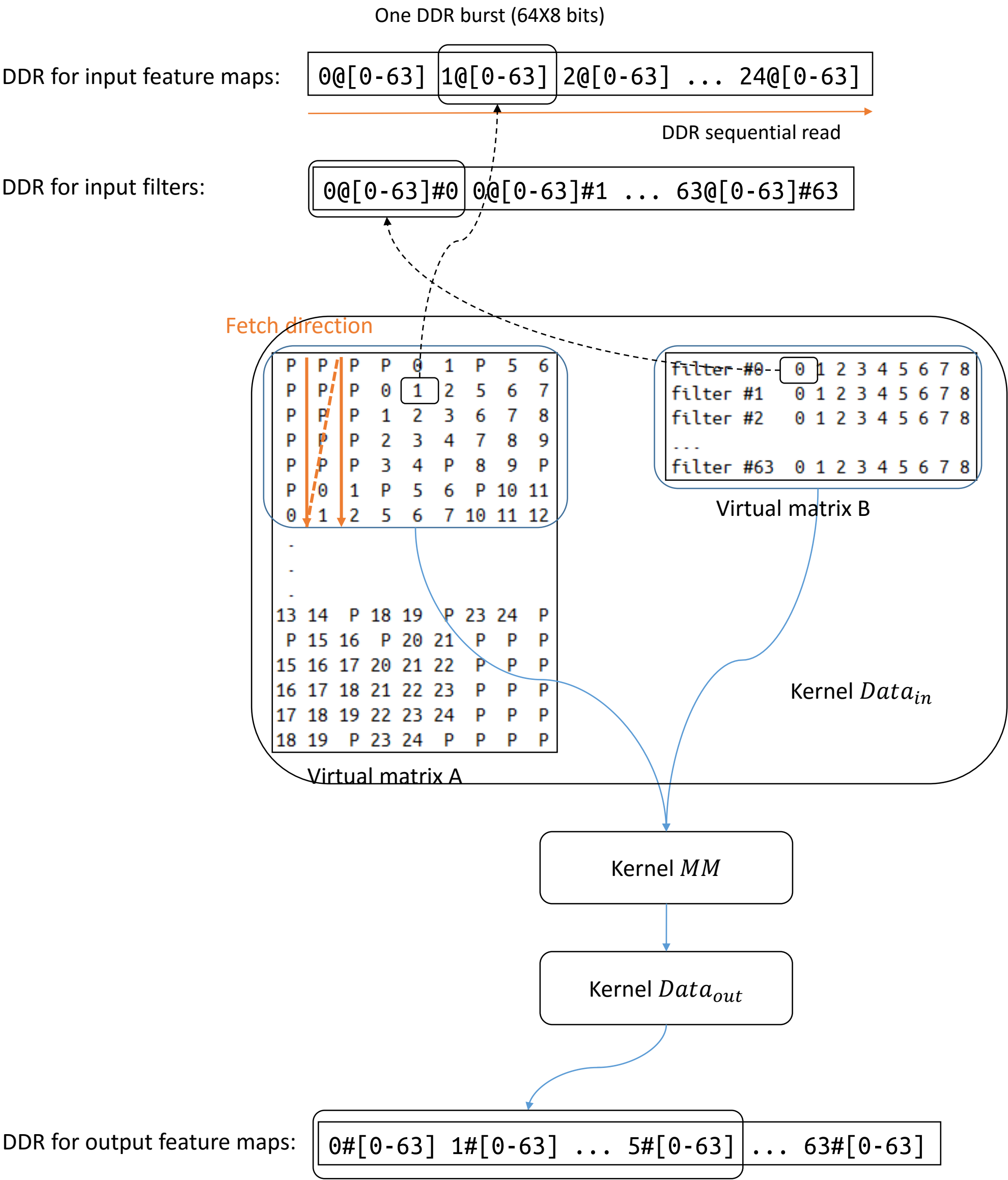
Important for computing
intensive applications like CNN



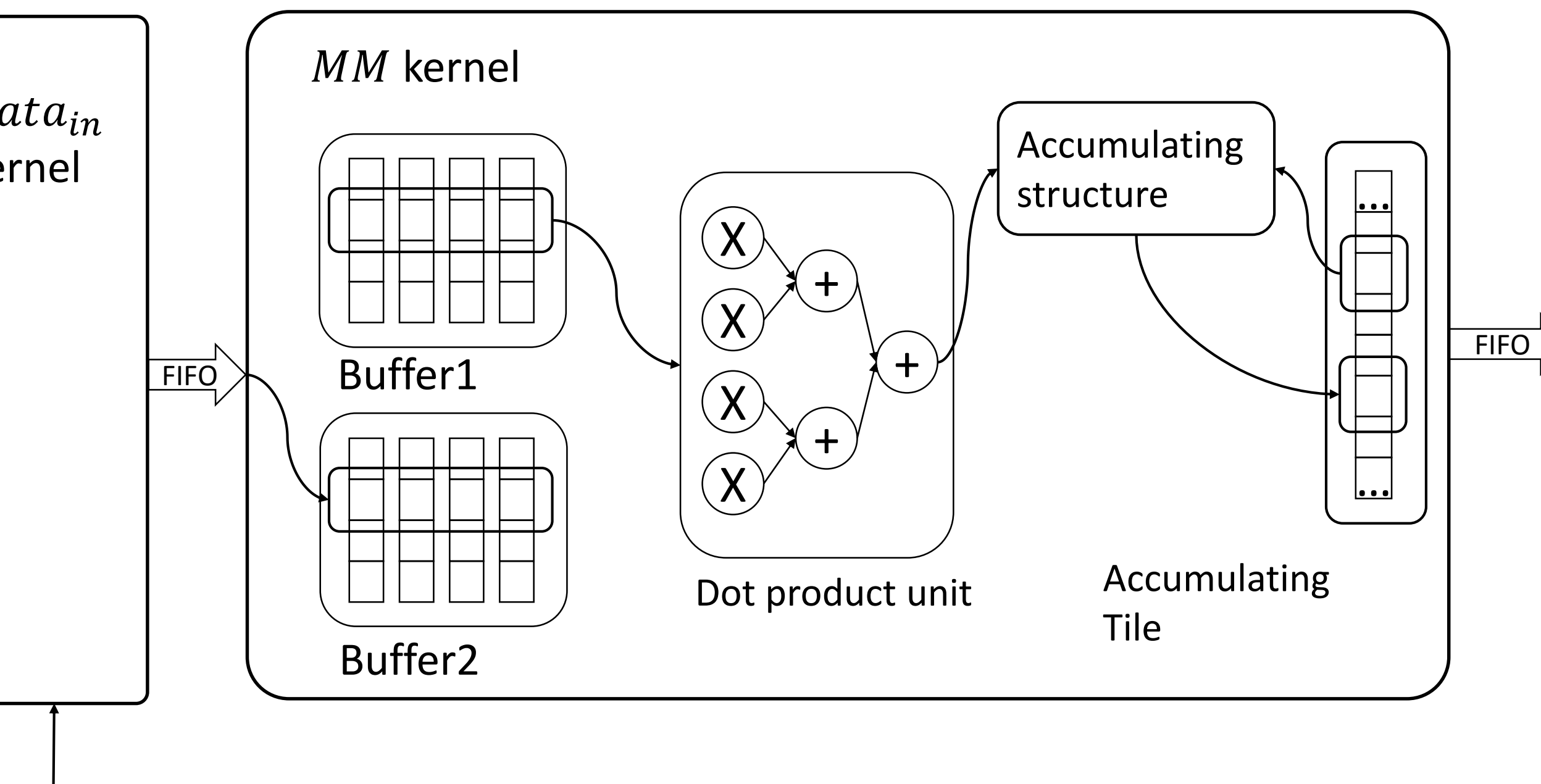
Convolution layer: Data locality can be a problem



Rearrange the fmap layout that best fit the GEMM's fetch order.



Hardware structure for previous graph



DDR3 on Catapult FPGA